

# Schedulability Analysis of Timed Systems

with contributions from

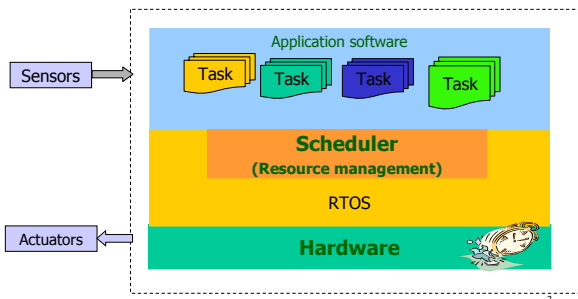
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Pavel Kracal, Leonid Mokrushine, Christer Nordström,  
Paul Pettersson and Anders Wall

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## PROBLEM SETTING

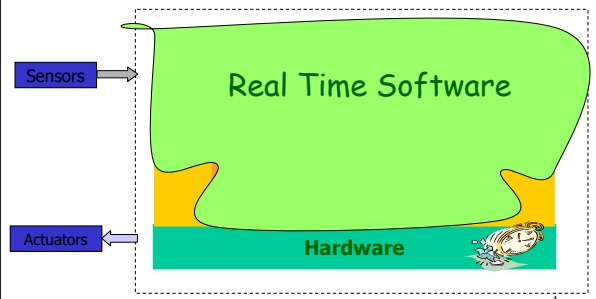
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## Real Time Systems



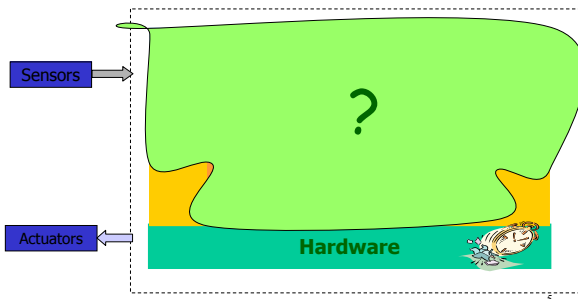
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## Real Time Systems



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## Real Time Systems



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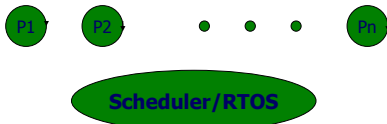
## "Who is Who" in Timed Systems

- Real Time Scheduling [RTSS ...]
  - Task models, Schedulability analysis
  - Real time operating systems
- Automata/logic-based methods [CAV, TACAS ...]
  - (Timed) Automata, Petri Nets, Process Algebras ...
  - Modelling, Model checking ...
- (Real-Time) Programming Languages [...]
  - Esterel, Signal, Lustre, Ada ...
- ... ..

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## "Classic" Real Time Scheduling

Periodic tasks



well-developed techniques e.g. Rate-Monotonic Scheduling

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## Rate-Monotonic Scheduling

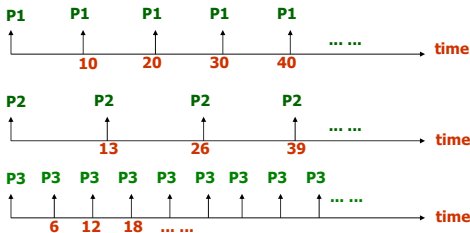
- $P_1 \dots P_n$  arrive at **fixed rates**
- Fixed Priority Order: **higher frequency => higher priority**
- Always **run the task with highest priority (FPS)**
- Schedulability can be checked by solving (or UB test):

$$R_i = B_i + C_i + \sum_{j \in \text{HP}(i)} \lceil R_j/T_j \rceil * C_j$$

If  $R_i \leq D_i$  for all tasks  $P_i$ , the system is schedulable

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## Arrival Rates (of Tasks): Periodic



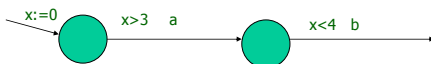
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## In "real life"

- tasks may share many resources (not only CPU time)
- tasks may have complex control structures and interactions
- tasks may not be that "regular" (often non-periodic)

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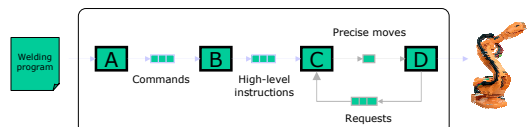
## Task Arrival Patterns: Timed Traces



(3.3, a) (3.4, b),  
(6.5, a),  
(3.6, a) (3.9, b),  
(3.14, a) (3.14159, b)  
... ..

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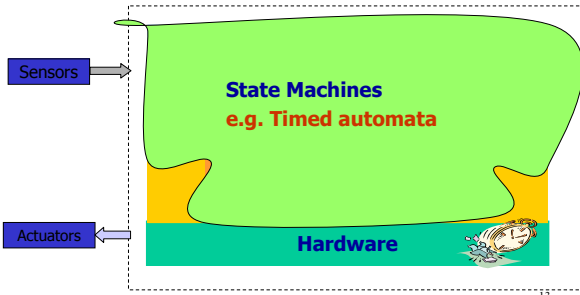
## The ABB Robot Controller



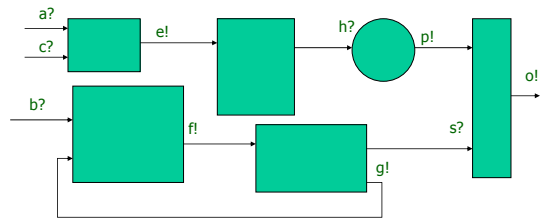
- ABB robot controller (2 500 000 loc)
- Real time tasks A,B,C,D
- Read inputs from channels write output to channels
- Task priority order  $D > C > B > A$  (FPS)
- Buffer overflow/underflow, WCRT

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## Automata-Based Approaches



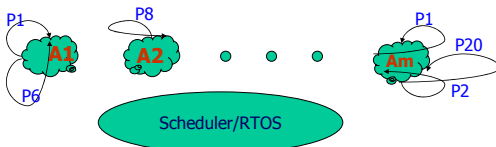
## Networks of Real-Time Components



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## Automata-based Approaches

A controller = a set of **timed automata** accepting events trigger tasks  $P_i$ 's



How to schedule tasks/automata? Worst-case response times?

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## Problems to solve

- **Schedulability analysis:** check  
 $(A_1 \parallel A_2 \parallel \dots \parallel A_n \parallel \text{Scheduler})$  satisfies  $K$ 
  - A **scheduler** is given e.g. FPS, RMS, EDF etc
  - $K$  is a requirement specifying e.g. safety
- **Schedule synthesis:** find  $X$  such that  
 $(A_1 \parallel A_2 \parallel \dots \parallel A_n \parallel X)$  satisfies  $K$

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## OUTLINE

- A Model for Timed Systems [1998]
  - Timed automata with tasks
- Schedulability and Decidability [TACAS 02,04]
  - Timed automata with bounded subtraction
- More Efficient Algorithms [TCS 06, IC 07]
  - Schedulability analysis using 2 clocks
  - (similar to Rate-Monotonic Scheduling)
- TIMES Demo
- Current/Ongoing Work

Implemented in  
the **TIMES** tool

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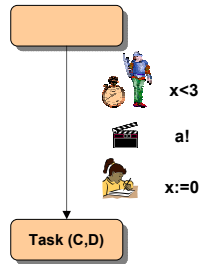
## The MODEL

(Timed Automata with Tasks)

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## Timed Automata with Tasks

- Events
  - Discrete Transitions
- Timing constraints
  - Clocks / Guards / Resets
  - Complex arrival rates
- Tasks
  - Asynchronous execution
  - WCET, Deadline
  - Scheduling policy
  - Precedence constraints
  - Resource constraints



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## Tasks = Executable Programs (e.g. C, Java)

- Task parameters:
  - C: WCET
  - D: Relative Deadline
  - (other parameters for scheduling e.g. Priority)

- Task Interface:

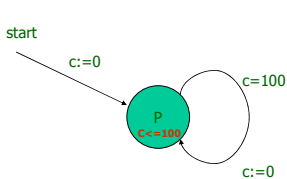
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Task P
{
  v1 := F1(v1...vn)
  ...
  vn := Fn(v1...vn)
}
    
```

(a set of variables updated)

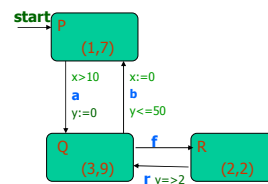
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## Example: periodic tasks

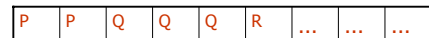


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## Timed Automata with Tasks (Example)



- Processor 1 (event handler)
  - Initially, P in the queue
  - Run-to-Completion/Stabilization
    - Whenever a available and x>10, Q is put in the queue
    - Then
      - Whenever b available and y<=50, P is put in the queue
      - Whenever f available, R is put in the queue.
- Processor 2 (task handler)
  - Schedule and Compute tasks in the queue



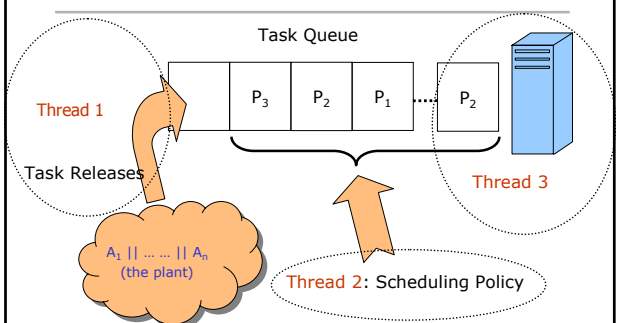
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## Timed Automata with Tasks

- Assume a set of tasks  $\Pr$
- A timed automaton with tasks is a tuple:  $\langle N, n_0, T, M \rangle$ 
  - $\langle N, n_0, T \rangle$  is a standard timed automaton
    - N is a set of nodes
    - $n_0$  is the initial node
    - $T \subseteq N \times (B(C) \times Act \times 2^C) \times N$  is the set of 'edges'
      - C is a set of clocks
      - Act is a set of actions
      - $B(C)$  is the set of clock constraints e.g.  $x < 10$  etc
  - $M: N \rightarrow 2^{\Pr}$  is a mapping which assigns each node a set of tasks

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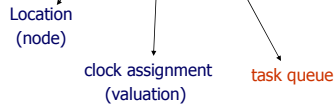
## The Execution Platform



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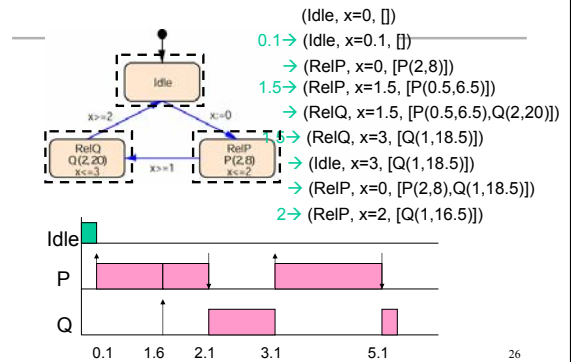
## States/Configurations of automata

A state is a triple:  $(m, u, q)$



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## Run of TAT



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## Sch and Run

- Sch** is a function sorting task queues according to a given scheduler e.g FPS, EDF, FIFO etc

Example: EDF  $[P(2, 10), Q(4, 7)] = [Q(4, 7), P(2, 10)]$

- Run** is a function corresponding to running the first task of the queue for a given amount of time.

Examples: Run(0.5,  $[Q(4, 7), P(2, 10)]$ ) =  $[Q(3.5, 6.5), P(2, 9.5)]$   
 Run(5,  $[Q(4, 7), P(2, 10)]$ ) =  $[P(1, 5)]$

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## Semantics (as transition systems)

- States:**  $\langle m, u, q \rangle$

- $m$  is a location
- $u$  is a clock assignment (valuation)
- $q$  is a queue of tasks (ready to run)

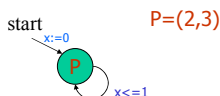
- Transitions:**

- $\langle m, u, q \rangle \xrightarrow{a} \langle n, r(u), \text{Sch}[M(n)::q] \rangle$  if  $\langle m \xrightarrow{g} n \rangle$  &  $g(u)$
- $\langle m, u, q \rangle \xrightarrow{d} \langle m, u+d, \text{Run}(d, q) \rangle$  where  $d$  is a real

**OBS:**  $q$  is growing (by actions) and shrinking (by delays)

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## "Zenoness" = Non-Schedulability



Zeno:  $\infty$  many P's may arrive within 1 time unit !

$P(2,3) \ P(2,3) \ P(2,3) \ P(2,3) \ P(2,3) \ \dots$

But after 2 copies, the queue will be non-schedulable

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# SCHEDULABILITY

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## Schedulability of automata

a state is a triple:  $(m, u, q)$

location

clock assignment

task queue

- A state is schedulable if  $q$  is schedulable
- An automaton is schedulable if all reachable states are

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## Schedulability of Automata

Assume a scheduler  $Sch$ :

- A state  $(m, u, q)$  is schedulable with  $Sch$  if
  - $Sch(q) = [P_1(c_1, d_1), P_2(c_2, d_2), \dots, P_n(c_n, d_n)]$  and
  - $(c_1 + \dots + c_i) \leq d_i$  for all  $i \leq n$  (i.e. all deadlines met)
- An automaton is schedulable with  $Sch$  if all its reachable states are schedulable
- An automaton is schedulable with a class of scheduling policies if it is schedulable with every  $Sch$  in the class.

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## DECIDABILITY

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## Schedulability Analysis (Non-preemptive scheduling)

FACT [1998]

For Non-preemptive schedulers, the schedulability of an automaton can be checked by reachability analysis on ordinary timed automata.

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## Proof ideas (1):

Size of schedulable queues is bounded

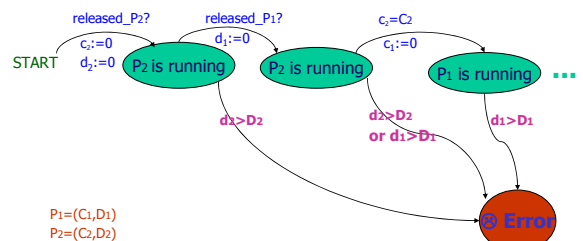
- The maximal number of instances of  $P_i$  in a schedulable queue is bounded by  $M_i = \lceil D_i/C_i \rceil$
- The maximal size of schedulable queues is bounded by  $M_1 + M_2 + \dots + M_n$
- To code the queue/scheduler, for each task instance, use 2 clocks:
  - $c_i$  remembers the computing time
  - $d_i$  remembers the deadline

...	$(c_i, d_i)$	...
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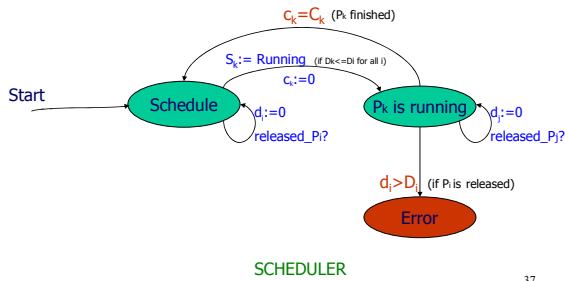
## Proof ideas (2):

### The scheduler as an automaton



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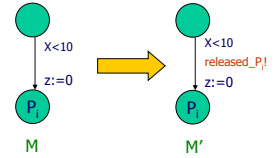
## The scheduler automaton



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## Proof Ideas (3)

- Modify the original automaton  $M$ : adding 'release!' to inform the scheduler



- Check reachability of the error state for  $M' \parallel \text{SCHEDULER}$

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## How about preemptive scheduling?

- We may try the same ideas
  - Use clocks to remember computing times and deadlines
- BUT a running task may be stopped to run a more 'urgent' task
  - Thus we need stop-watches to remember "accumulated computing times"
- Then the schedulability problem is undecidable?
- This is wrong !!

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## Decidability Result [TACAS 2002]

### FACT

For Preemptive schedulers, the schedulability of an automaton can be checked by reachability analysis on Bounded Subtraction Timed Automata (BSA).

### NOTE

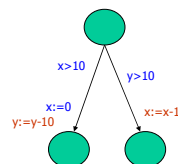
- Reachability for BSA is decidable
- Preemptive EDF is optimal; thus the general schedulability checking problem is decidable.

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## Timed automata with subtraction

i.e. Subtraction Automata, [McManis and Varaiya, CAV94]

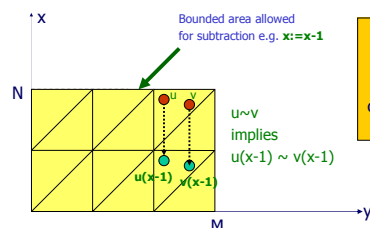
- Subtraction automata are timed automata extended with subtraction on clocks
- That is, in addition to reset  $x := 0$ , it is also allowed to update a clock  $x$  with  $x := x - n$  where  $n$  is a natural number



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## Bounded Subtraction Automata

- A subtraction automaton is bounded if its clocks are non-negative and bounded with a maximal constant (or subtraction is only allowed in the bounded zone).



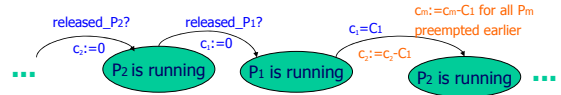
**FACT:**  
Location Reachability checking is decidable!

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## Schedulability Checking as a reachability problem for Bounded Subtraction Automata

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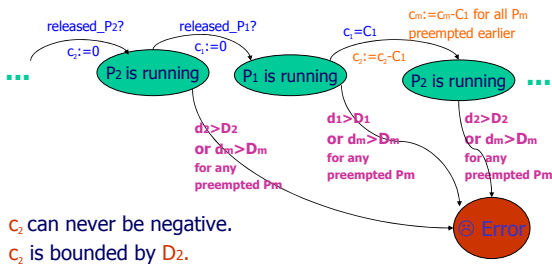
## Proof ideas (no stop but subtraction :-)



- Model the scheduler as a subtraction automaton
  - Do not stop the computing clock  $c_i$  when a new task  $P_i$  is released
  - Let  $c_i$  for  $P_i$  (preempted) run until the task  $P_i$  (with higher priority) finishes, then perform  $c_i := c_i - C_i$  (note:  $C_i$  is the computing time for  $P_i$ ).

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## Proof ideas (clocks are bounded):



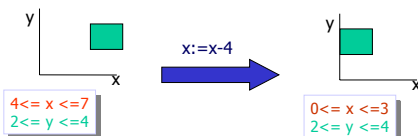
- $c_2$  can never be negative.
- $c_2$  is bounded by  $D_2$ .

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**END of proof**

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## Schedulability analysis using DBM's



Subtraction on Clocks, added to DBM-library (UPPAAL)

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## Complexity

#clocks (needed)  
 $= 2 \times \text{\#instances}$  (maximal number of schedulable task instances)  
 $= 2 \times \sum_i \lceil D_i / C_i \rceil$

This is a huge number in the worst case  
 But the run-time complexity is not so bad!

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## It works anyway !!!

- #active tasks in the queue is normally small, and the run-time complexity is only related to #active clocks
- If Too many active tasks in the queue (i.e. Too many active clocks), the check will stop sooner and report "non-schedulable"
- AND the analysis can be done symbolically!

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## WE CAN DO BETTER ! [TACAS 03, TCS 06]

For fixed priority scheduling strategies (FPS),  
we need only 2 clocks (and ordinary timed automata)!

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## The 2-CLOCK ENCODING

(for fixed-priority scheduling strategies)

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### Main Idea

- Check the schedulability of tasks one by one according to priority order (highest priority first)
- This is similar to response time analysis in RMS

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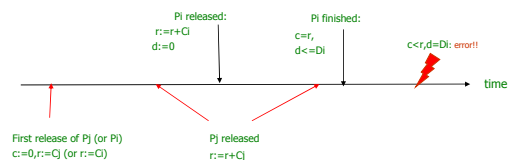
To code the queue/scheduler, we need:

- 1 integer variable for  $P_i$ :
  - $r$  denotes the response time as in RMS (the total computing time needed before  $P_i$  finishes)
- 2 clocks for  $P_i$ :
  - $c$  remembers the accumulated computing time (so much has been computed so far)
  - $d$  remembers the "deadline"

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Intuition of the encoding:  $R_i = C_i + \sum_{\text{priority}(P_j) > \text{priority}(P_i)} C_j$

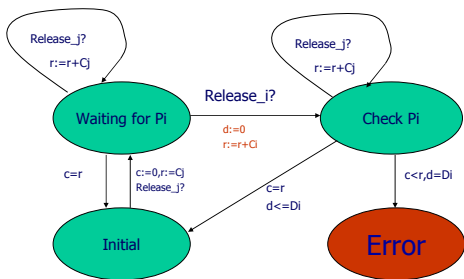
– Assume:  $\text{priority}(P_j) > \text{priority}(P_i)$  and  $P_i$  is analyzed



When  $P_i$  finishes,  $r = R_i$

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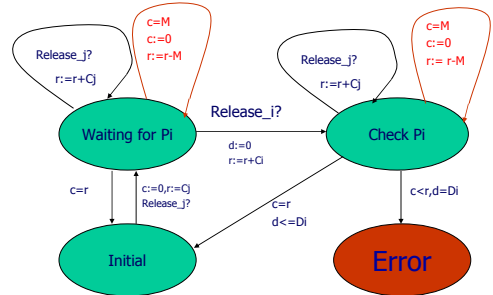
## The "FPS scheduler": analyzing $P_i$



Note that it is not clear that  $c$  and  $r$  are bounded !

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## The "FPS scheduler": analyzing $P_i$ (we need the boundedness)



OBS:  $r-c$  is the only interesting info, so  $M$  can be any integer! Let  $M=C_i$

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## $c$ and $r$ are bounded

- $c$  is bounded by  $M$
- $r$  is bounded by  $r_{\max} + C_i$ 
  - Where  $r_{\max}$  is the maximal value of  $r$  from previous analysis for all tasks  $P_j$  with higher priority

So the scheduler is a standard TA **END**

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## SUMMARY: Decidability

- For **Non-preemptive** schedulers, the problem can be solved using standard TA.
- For **preemptive** schedulers, the problem can be solved using BSA (Bounded Substraction Automata).
- For **fixed-priority** schedulers, the problem can be solved using TA with only **2 extra clocks** – similar to the classic RMA technique (Rate-Monotonic Analysis).

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## Undecidability

Unfortunately, the problem will be undecidable if the following conditions hold together:

- Preemptive scheduling
- Interval computation times
- Feedback i.e. the finishing time of tasks may influence the release times of new tasks.

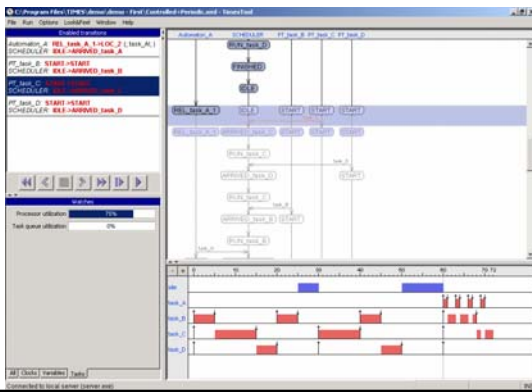
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## Conclusions/Remarks

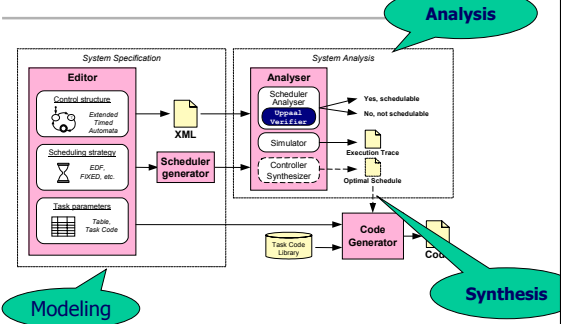
- Unification** of model-checking, real time scheduling, and synchronous programming: a **unified model** for timed systems (can express complex temporal and resource constraints).
- The **first decidability result** (and **efficient algorithms**) for preemptive scheduling in dense time models:
  - The analysis is symbolic (using DBM's in the UPPAAL tool)
- Implementation: **TIMES**

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# TIMES demo

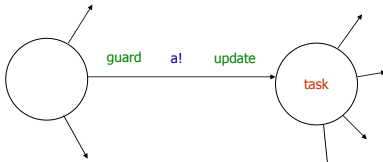


## An Overview of TIMES



## The INPUT LANGUAGE is very much like "guarded commands"

OBS: **guard** and **update** may contain data variables (integer, array)



- guard, update:** "synchronous" computation which takes "no time"  
- we adopt the **synchronous hypothesis** [CONCUR'04]
- task:** "asynchronous" computation which takes time

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## Tasks = Executable Programs (e.g. C, Java)

- Task Type**
  - Synchronous or Asynchronous
  - Non-Periodic (triggered by events) or Periodic
- Task parameters:** C, D etc
  - C: Computing time and D: Relative Deadline
  - other parameters for scheduling e.g. priority, period
- Task Interface** (variables updated 'atomically')
  - $X_i := F_i(X_1 \dots X_n)$
- Tasks may have shared variables
  - with automata
  - with other tasks (priority ceiling protocols)
- Tasks with precedence constraints

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## Functionality/Features of TIMES

- GUI**
  - modeling: automata with asynchronous tasks
  - editing, task library, visualization etc
- Simulation**
  - symbolic execution as MSCs and Gant Charts
- Verification**
  - all you do with UPPAAL
  - Schedulability analysis
- Code Generation**

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## Code Generation in TIMES

- Run Time Systems**
  - Event Handler, OS interrupt processing system or Polling
  - Task scheduler, generated from task parameters
- Application Tasks = threads (or processes)**
  - Already there! (written in C)
  - Current version of TIMES support LegoOS

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