MIPS Instruction Reference

ADD – *Add (with overflow)*
ADDI -- *Add immediate (with overflow)*
ADDIU -- *Add immediate unsigned (no overflow)*
ADDU -- *Add unsigned (no overflow)*
AND -- *Bitwise and*
ANDI -- *Bitwise and immediate*
BEQ -- *Branch on equal*
BGEZ -- *Branch on greater than or equal to zero*
BGEZAL -- *Branch on greater than or equal to zero and link*
BGTZ -- *Branch on greater than zero*
BLEZ -- *Branch on less than or equal to zero*
BLTZ -- *Branch on less than zero*
BLTZAL -- *Branch on less than zero and link*
BNE -- *Branch on not equal*
DIV -- *Divide*
DIVU -- *Divide unsigned*
J -- *Jump*
JAL -- *Jump and link*
JR -- *Jump register*
LB -- *Load byte*
LUI -- *Load upper immediate*
LW -- *Load word*
MFHI -- *Move from HI*
MFLO -- *Move from LO*
MULT -- *Multiply*
MULTU -- *Multiply unsigned*
NOOP -- *no operation*
OR -- *Bitwise or*
ORI -- *Bitwise or immediate*
SB -- *Store byte*
SLL -- *Shift left logical*
SLLV -- *Shift left logical variable*
SLT -- *Set on less than (signed)*
SLTI -- *Set on less than immediate (signed)*
SLTIU -- *Set on less than immediate unsigned*
SLTU -- *Set on less than unsigned*
SRA -- *Shift right arithmetic*
SRL -- *Shift right logical*
SRLV -- *Shift right logical variable*
SUB -- *Subtract*
SUBU -- *Subtract unsigned*
SW -- *Store word*
SYSCALL -- *System call*
XOR -- *Bitwise exclusive or*
XORI -- *Bitwise exclusive or immediate*