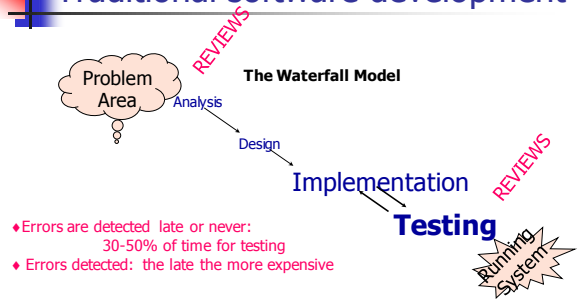


Modeling Real-Time Systems

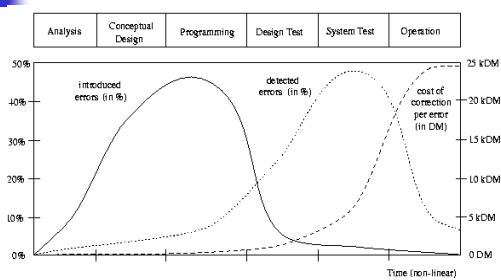
1

Traditional software development



2

Introducing, Detecting and Correcting errors



3

Finding errors as early as possible!

HOW?

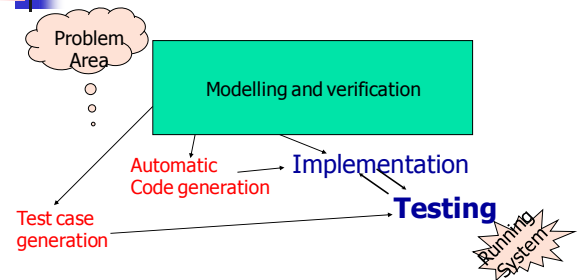
4

Modeling and Verification

- "modeling" is a design process: describe systematically the abstract behaviour of a system
 - It is to create a model or a design proposal of the system to be developed
- "verification" is to check whether the model satisfies given requirement specifications
 - It is similar to testing, but testing on the model

5

Software development



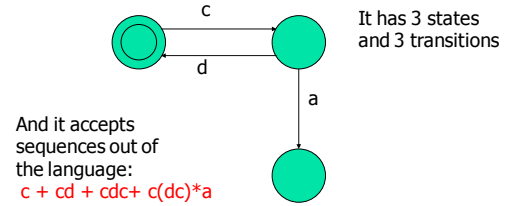
6

Basic ideas

- Modeling
 - Use state-machines, also known as automata, to describe system components
 - A system will be a network of automata
- Verification
 - Check properties of the automata using software tools like UPPAAL

7

Finite State Automata



8

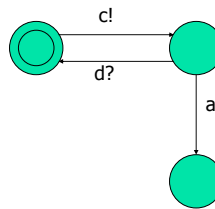
Automata as tasks

- We view an automaton as a task where the action labels stand for synchronization actions e.g. Ada's rendezvous
- For example, the previous example could be a network protocol where c and d stand for "connect!" and "disconnect?" and a for "abort!"

9

Input and Output Actions

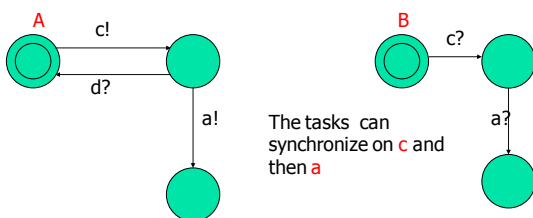
We use ? and ! to denote the pairs of complementary actions in rendezvous communication



10

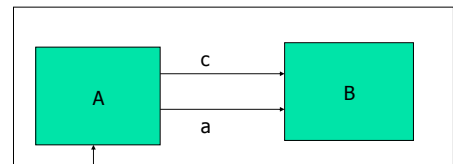
Networks of automata

We use ? and ! to denote the pairs of complementary actions in rendezvous communication



11

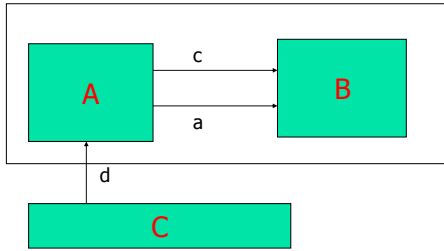
The static architecture of a concurrent system: $A \parallel B$



where c , a , d stand for "ports" or "channels"

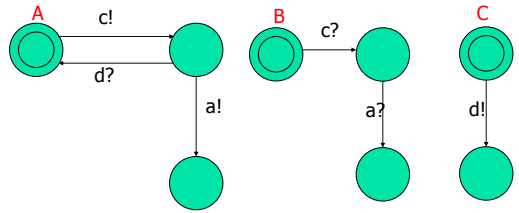
12

The static architecture of a concurrent system: $A \parallel B \parallel C$



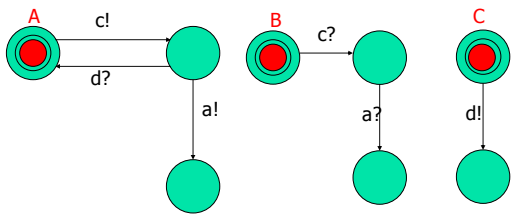
13

Networks of automata



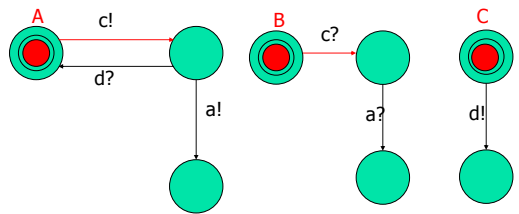
14

Networks of automata: initial state



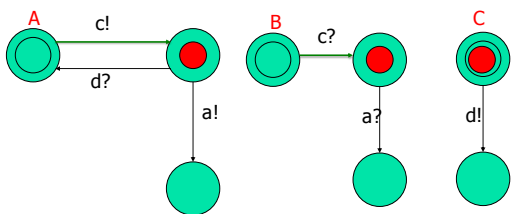
15

Networks of automata: enabled transition



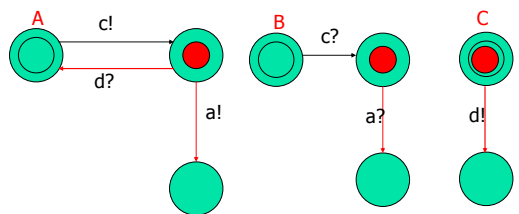
16

Networks of automata: transition taken



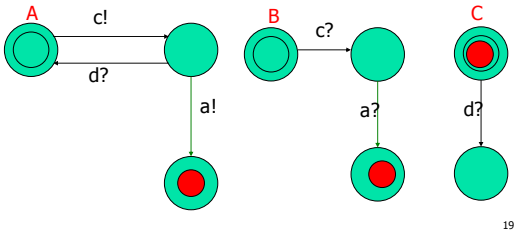
17

Networks of automata: nondeterministic choices (transitions)



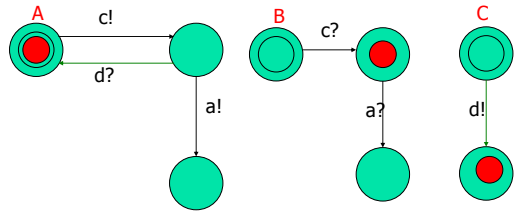
18

Networks of automata: non-deterministic transitions (1)



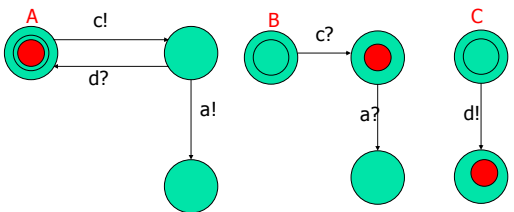
19

Networks of automata: non-deterministic transition (2)



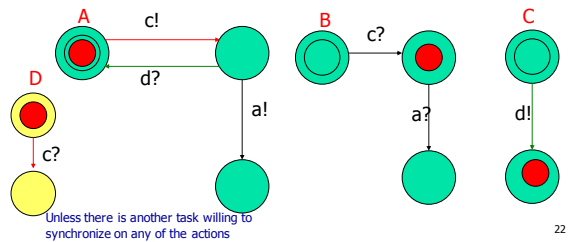
20

Networks of automata: deadlock!



21

Networks of automata: deadlock!



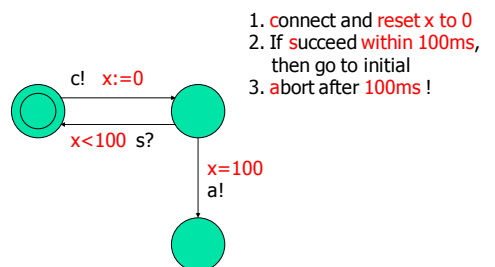
22

Clocks and timing constraints

- Now we assume that the system has a finite number of clocks
 - The clocks start to **increase from 0** when the system starts, and they run at the same rate
 - The clocks can be **tested** using e.g. $x < 100$
 - The clocks can be **reset to 0** on a transition using an assignment: $x := 0$

23

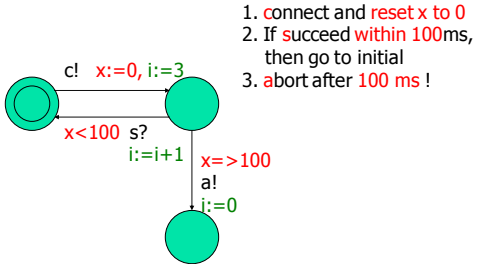
Timed automata: clocks



- connect and **reset x to 0**
- If **succeed within 100ms**, then go to initial
- abort after 100ms!**

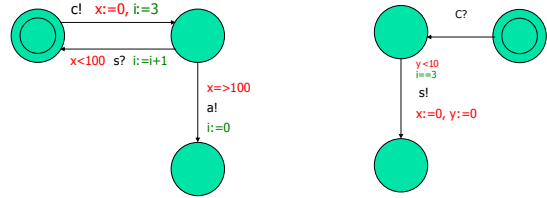
24

Timed automata: integers



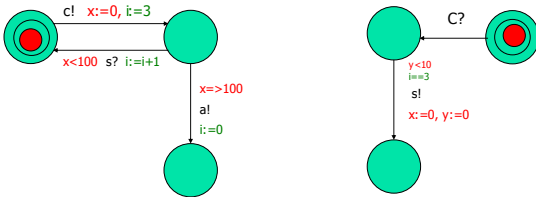
25

Network of Timed automata



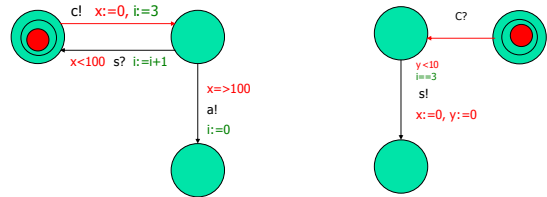
26

Network of Timed automata: initial states



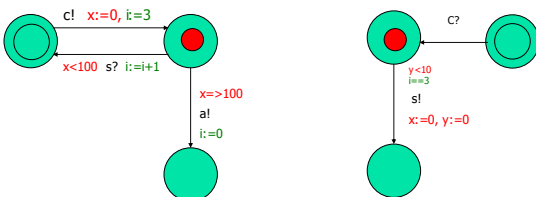
27

Network of Timed automata: enabled ransition



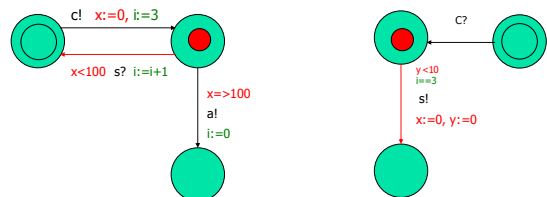
28

Network of Timed automata: transition taken



29

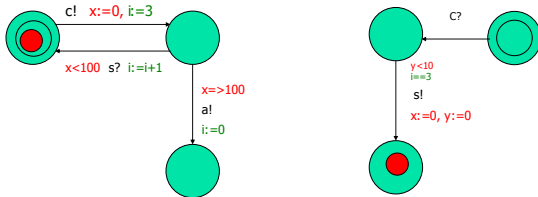
Network of Timed automata: enabled ransition



This depends on the values of x , y , and i

30

Network of Timed automata: transition taken



This depends on the values of x , y , and i

31

Timed automata (definition)

- a timed automaton is a **finite graph**
 - a finite many nodes N with an initial node
 - a finite many edges E between nodes
 - an edge may be labelled with three elements
 - guard
 - action ($a?$, $a!$, or nothing)
 - assignment
- (they may not appear)

32

Guard

- a clock constraint
 - $g ::= x \leq n \mid x \geq n \mid x < n \mid x > n \mid g \wedge g$
 - where n is any natural number
- a predicate over data variables
 - "any logical expression" you may write in C

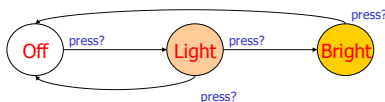
33

assignment

- a clock reset: $x:=0$ for any clock x
- a sequence of assignments in the form $i:=e$
(or most of the programming constructs in C)

34

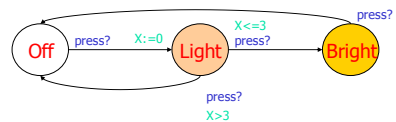
Intelligent Light Control



WANT: if **press** is issued twice **quickly** then the **light** will get **brighter**; otherwise the light is turned **off**.

35

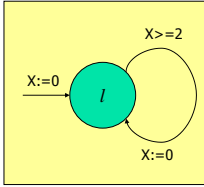
Intelligent Light Control (with timer)



Solution: Add real-valued clock x

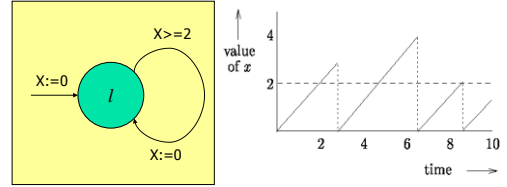
36

Timed Automata: Example



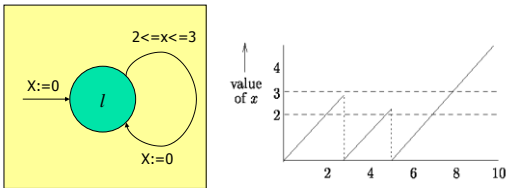
37

Timed Automata: Example



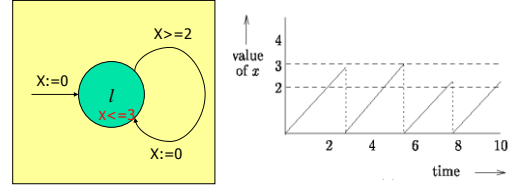
38

Timed Automata: Example



39

Timed Automata: Example



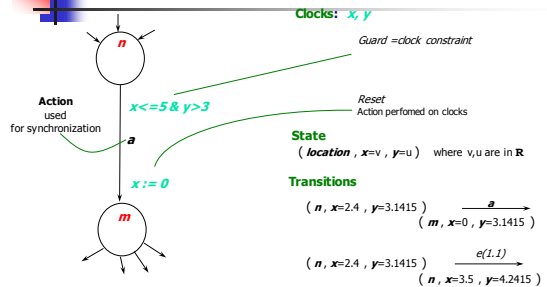
40

Timed Automata: Semantics

- States: (n, u) where
 - n stands for the current node or location
 - u stands for the current values of clocks and integers (i.e. the memory of a machine)
- Transitions: delay and actions
 - (n, u) moves to $(n, u+d)$ after d time units
 - (n, u) moves to (m, v) when an action over channel "a" is taken, and v is the new values of clocks and integer variables

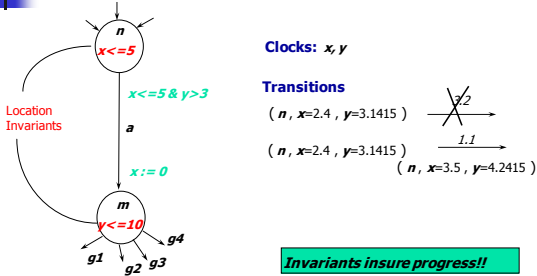
41

Timed Automata: Semantics



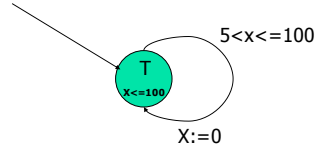
42

Timed Automata with *Invariants*



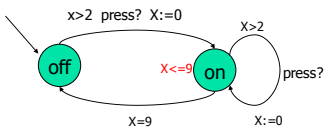
43

Timed Automata: Example (task with jitter)



44

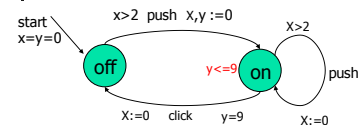
Timed Automata: Light Switch



- Switch may be turned on whenever at least 2 time units has elapsed since last "turn off"
- Light automatically switches off after 9 time units if it is not pressed.

45

Timed Automata: Example



$(off, x = y = 0) \xrightarrow{3.5} (off, x = y = 3.5) \xrightarrow{push} (on, x = y = 0) \xrightarrow{\pi} (on, x = y = \pi) \xrightarrow{push} (on, x = 0, y = \pi) \xrightarrow{3} (on, x = 3, y = \pi + 3) \xrightarrow{9 - (\pi + 3)} (on, x = 9 - (\pi + 3), y = 9) \xrightarrow{click} (off, x = 0, y = 9) \dots$

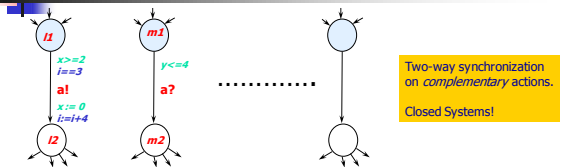
46

Modeling Concurrency

- Products of automata
- Parallel composition

47

Networks of Timed Automata + Integer Variables +



Example transitions

$(i1, m1, \dots, x=2, y=3.5, i=3, \dots) \xrightarrow{a!} (i2, m2, \dots, x=0, y=3.5, i=7, \dots)$

48

Modeling Ada Programs

49

Rendezvous

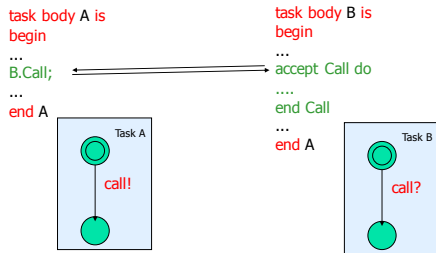
```

task body A is
begin
...
B.Call;
...
end A

task body B is
begin
...
accept Call do
...
end Call
...
end B
    
```

50

Rendezvous



51

Buffer

```

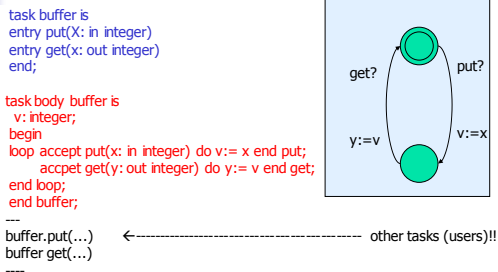
task buffer is
entry put(x: in integer)
entry get(x: out integer)
end;

task body buffer is
v: integer;
begin
loop accept put(x: in integer) do v:= x end put;
accept get(x: out integer) do x:= v end get;
end loop;
end buffer;

buffer.put(...) ← other tasks (users)!!
buffer.get(...)
    
```

52

Buffer



53

Conditional/Timed entry call

```

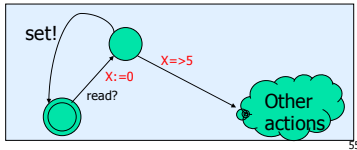
loop
--get temperature
select
Controller.set(T); -- set new temperature
or delay 5 --other actions
end select;
end loop;
    
```

54

Conditional/Timed entry call

```

loop
  --get temperature
  select
    Controller.set(T); -- set new temperature
  or delay 5
  end select;
end loop;
  
```



55

Timeout and message passing

```

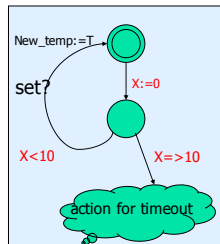
loop
  select
    accept set(T : temperature) do
      New_temp:=T;
    end Call;
  or
    delay 10.0;
    --action for timeout
  end select;
  --other actions
end loop;
  
```

56

Timeout and message passing

```

loop
  select
    accept set(T : temperature) do
      New_temp:=T;
    end Call;
  or
    delay 10.0;
    --action for timeout
  end select;
  --other actions
end loop;
  
```



57

Periodic Activity

```

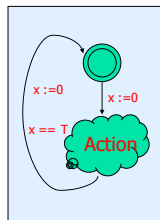
task body T is
  Interval : constant Duration := 5.0;
  Next_Time : Time;
begin
  Next_Time := Clock + Interval;
  loop
    Action;
    delay until Next_Time;
    Next_Time := Next_Time + Interval;
  end loop;
end T;
  
```

58

Periodic Activity

```

task body TaskP is
  T : constant Duration := 5.0;
  Next_Time : Time;
begin
  Next_Time := Clock + T;
  loop
    Action;
    delay until Next_Time;
    Next_Time := Next_Time + T;
  end loop;
end TaskP;
  
```



59