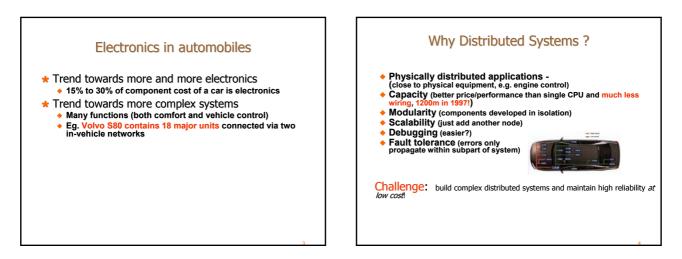
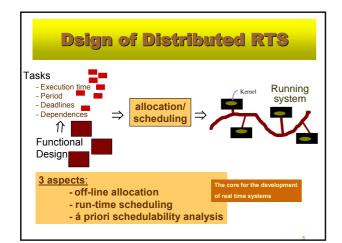
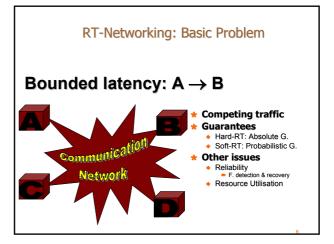
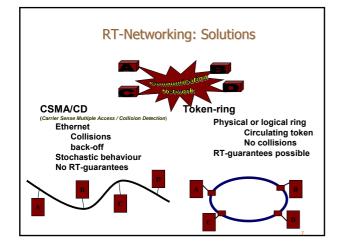


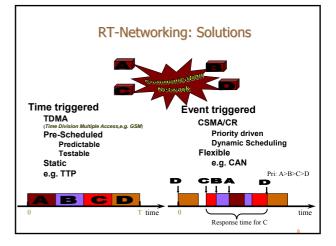
A Distributed Real-Time System High-spee Low-speed PDM ECM TCM AUM CCM UEM PHM SRS ETM SAS DIM SWM RTI PSN ABS REM CEM DDM

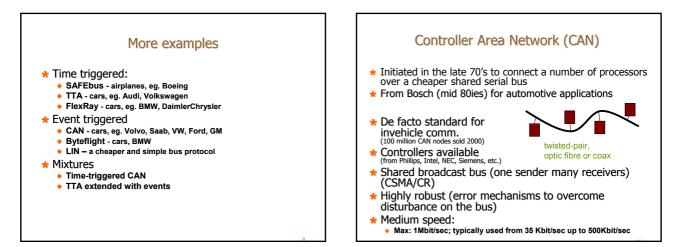


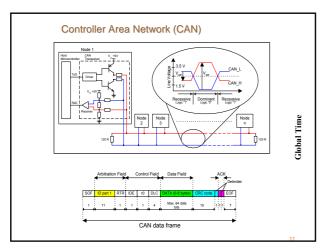


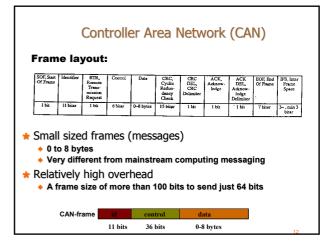


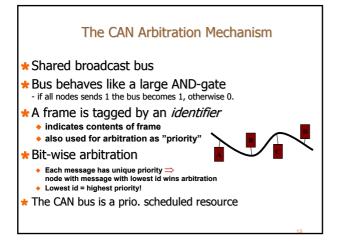


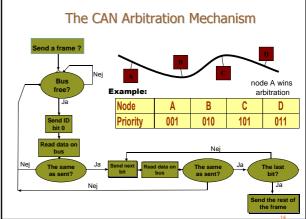




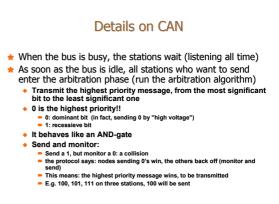


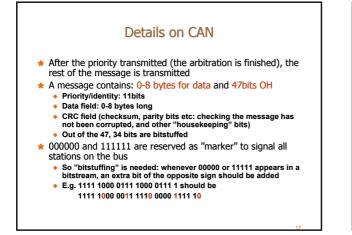


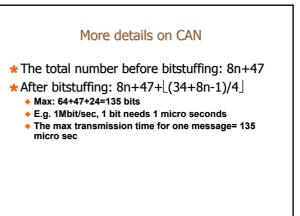


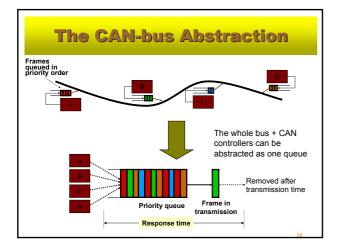


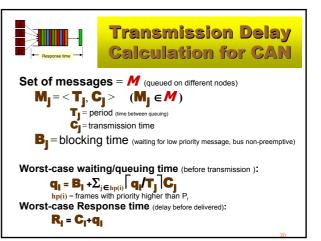
Details on CAN * Each message has a priority: a unique static number, used as the identifier of the message * Arbitration mechanism to ensure: the highest priority message is the one transmitted * Limits on speed and length (physical/electrical properties): to send 1Mbit/sec, wire/bus must be no longer than 50m To send 0.5Mbit/sec, the bus must be no longer than 100m The bus can have an arbitrary number of nodes Each station has a queue for messages ordered by priorities

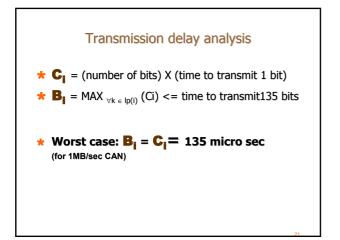


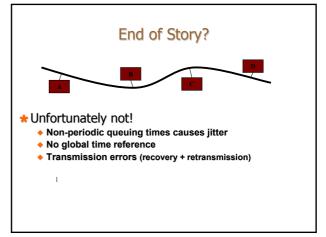


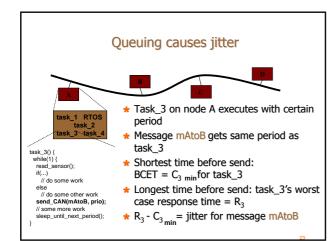


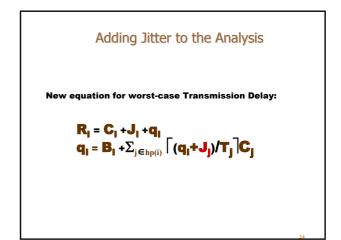








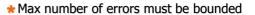




Error handling

- * Several types of errors:
 - Checksum error, acknowledge error, bit error, ...
- * When error is detected by node it sends an error frame
 - starting with 6 dominant bits (000000) in a row
 - tells other nodes that error occurred
 - other nodes then also send error frames
 - Arbitration restarts when bus is idle
- In effect, error frames are used to resync protocol engine

Transmission Errors



- ★ Fault hypothesis ⇒
 - Error function E(t) = max time required for error signalling and recovery in any time interval of length t

New equation for worst-case transmission delay:

Send msg

on bus

Initial

processing



Analysis of Distr.

Send msg

on bus

Inflate

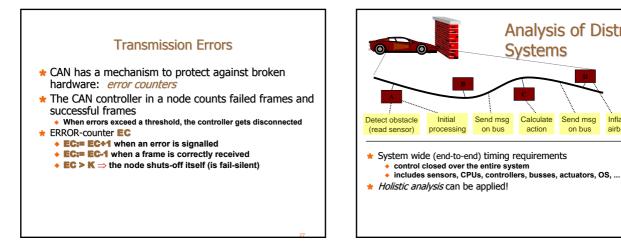
airbag

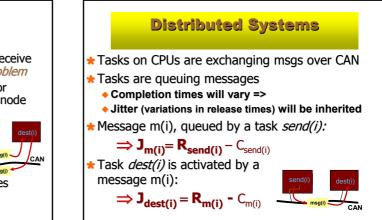
time

Systems

Calculate

action



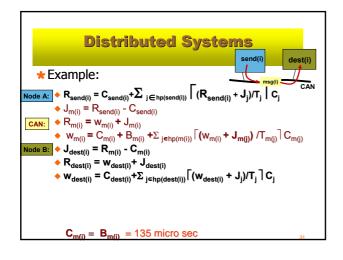


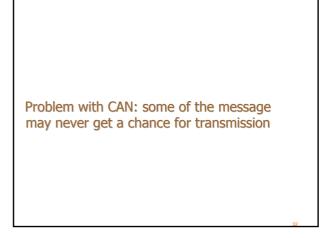
Holistic Scheduling Problem

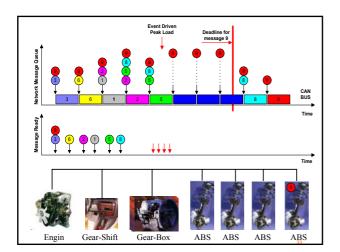
- * When tasks on a node can both send and receive messages we have a *holistic scheduling problem*
- The equations giving the worst case time for tasks depends on messages arriving at the node
- ★ We cannot apply the processor scheduling analysis before we get values from the bus scheduling analysis

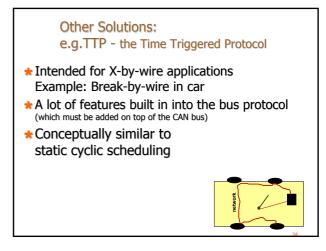


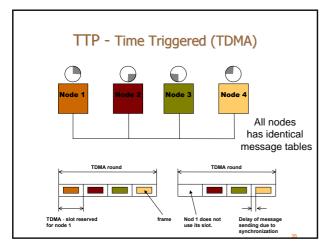
- * Similarly: We cannot apply the bus scheduling analysis before we get values from the processor scheduling analysis
- * Solution: Holistic Analysis

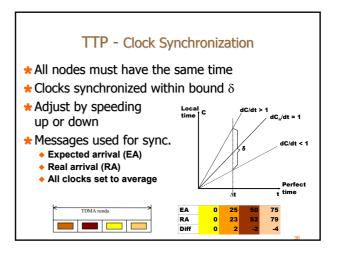


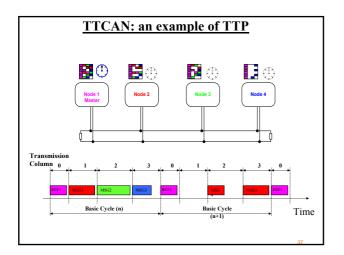


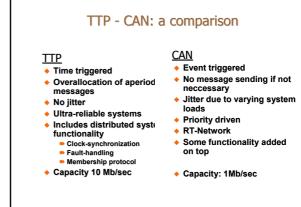












Trends for RT networks in Automotives

- * Today CAN dominates
- Time-triggered seems to be the future for X-by-wire: TTP e.g. FlexRay, TTCAN
- * Future cars will include many different and parallel buses:
 - CAN for comfort
 - TT for X-by-wire
 - MOST for multimedia
 - + etc.