

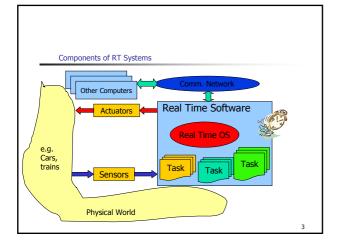
Overall Structure of RT Systems

- Hardware (CPU, I/O device etc)
 - a clock!
- A real time OS (function as standard OS, with predictable behavior and well-defined functionality)

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 A collection of RT tasks/processes (share resourses, communicate/synchronize with each other and the environment)



Characteristics of a RTS Large and complex — vary from a few hundred lines of assembler or C to 20 million lines of Ada estimated for the Space Station Freedom Concurrent control of separate system components — devices operate in parallel in the real-world; better to model this parallelism by concurrent entities in the program Facilities to interact with special purpose hardware — need to be able to program devices in a reliable and abstract way Mixture of Hardware/Software: some modules implemented in hardware, even whole systems, SoC

Characteristics of a RTS (ctn.)

- Extreme reliability and safety embedded systems typically control the environment in which they operate; failure to control can result in loss of life, damage to environment or economic loss
- Guaranteed response times we need to be able to predict with confidence the worst case response times for systems; efficiency is important but predictability is essential

Terminology

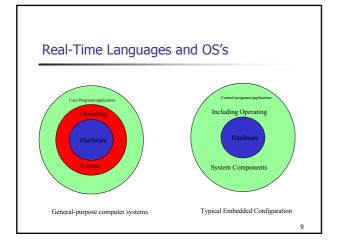
- Continuous interaction with the environment:
 - Reactive Systems
- Must react to the environment in time:
- Time-sensitive systems
- Embedded in electronic and/or mechanical devices, complex systems:
 - Embedded systems
- A failaure may cause the loss of lifes ...:
 - Safety-critical systems/fault-tolerant systems

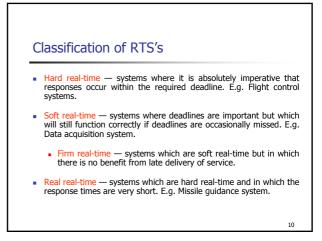
Terminology (ctn.)

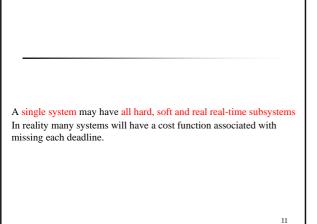
- It often deals with continuous variables e.g. temperature, speed, etc (hybrid systems, dynamics systems)
- RT system may consist of many processes running on
 - single processor (concurrent/multi-task systems)
 - tightly-coupled processors (parallel systems), multicores, MPSoC
 - loosely-coupled processors connected by a network (distributed systems)

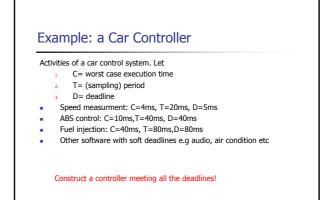
Real-time Programming Languages

- Assembly languages
- Sequential programming languages e.g. Pascal, C. • Both normally require operating system support.
- High-level concurrent languages e.g. Concurrent Pascal, Ada, Modula-2, Java.
 - No/less operating system support!
- We will consider:
 - Ada 95 and C





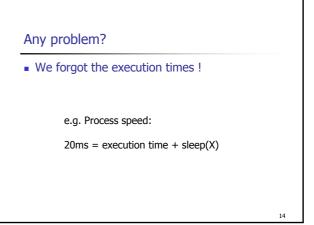




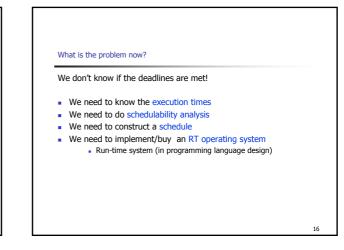
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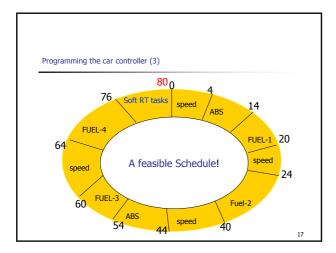
Programming	the	car	controller	(1))
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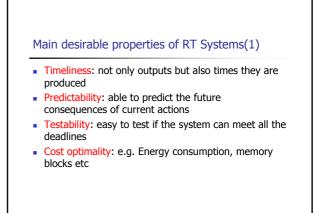
Process Speed:	Process ABS	
Loop	Loop	
<pre>read sensor,compute,display sleep (0.02) /*period*/</pre>	Read sensor, compute, react sleep(0.04)	
End loop	End loop	
Process Fuel	Soft RT Processes	
Loop	Loop	
read data, compute, inject	read temperature	
sleep(0.08)	el hiss, stereo	
End loop		
·	End loop	



Process ABS Loop next:=get-time + 0.04 Read sensor, compute, react sleep until next End loop	
Soft RT Processes Loop read temperature elevator, stereo End loop	







Main desirable properties of RT Systems (2)

- Maintainability: modular structure to ease system modification
- Robustness: must not collapse when subject to peak load, exception, manage all possible scenarios
- Fault tolerance: hardware and software failures should not cause the system to crash - function down-grading

Predictability: the most important one

- The system behaviour is known before it is put into operation!
 - e.g. Response times, deadlock freedom etc

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Difficult (impossible?) to achieve!

This is not so easy, why? RT OS:

- System calls: difficult to know the worst execution times (theoretically impossible, halting problem)
- Cache (hit ratio, never exact), pipelines ...
- DMA stealing CPU memory cycle (when CPU running a hard task)
- Interrupt handling may introduce unbounded delays
- Priority inversion (low-prority tasks blocking high-prior taskts)
- Memory management (static allocation may not be enough, dynamic data structures e.g. Queue), no virtual memory
- Communication delays in a distributed environment

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This is not so easy, why? RT Tasks:

- Difficult to calculate the worst case execution time for tasks (theoretically impossible, halting problem)
 - Avoid dynamic data structures
 - Avoid recursion
 - Bounded loops e.g. For-loops only
- Complex synchronization patterns between tasks: potential deadlocks (formal verification)

Problems to solve ...

- Missing deadlines (!)
- Deadlocks/livelocks
- Uncontrolled exception (ARIAN 5)
- Clock jitter (the golf war, Scud missile)
- 57micro sec/min, 343ms/100 hours
 687 meters
- Priority inversion (the Mars project)
- Uncontrolled code size, cost, ...
- Wrong timeout periods
- Non-determinism and/or Race condition
- Overloaded