Weak Consistency (TSO as an Example)



Parosh Aziz Abdulla¹



Mohamed Faouzi Atig¹



Ahmed Bouajjani²



Tuan Phong Ngo¹

¹Uppsala University

²IRIF, Université Paris Diderot & IUF

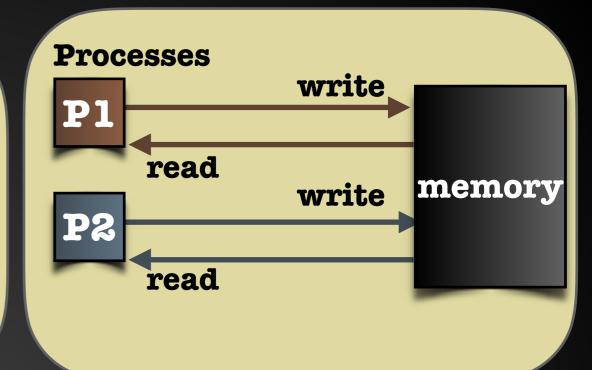
Outline

- Weak Consistency
- Total Store Order (TSO)
- Dual TSO
- Verification
- Specification
- Synthesis

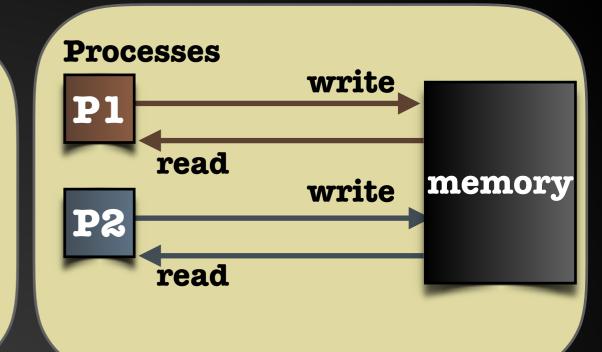
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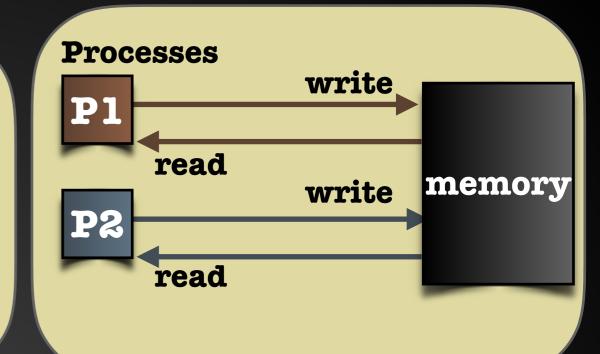
- Shared memory
- Processes: atomic read/write
- Interleaving of the operations



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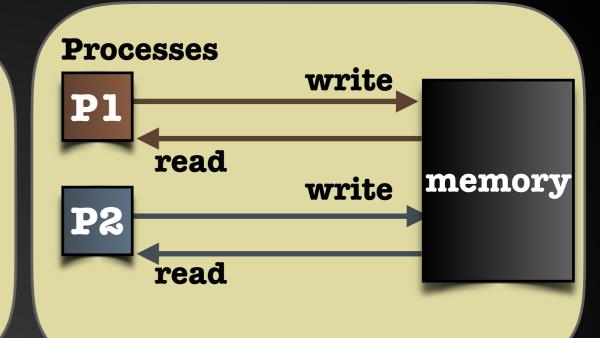


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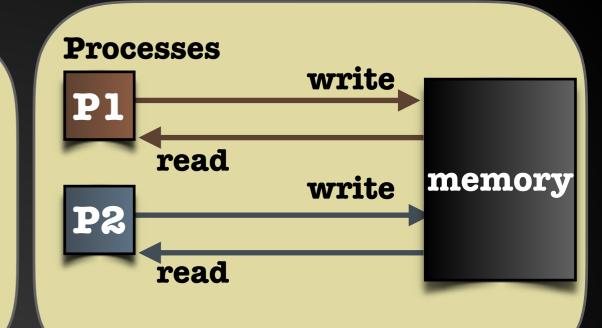
P1: w(x,1)

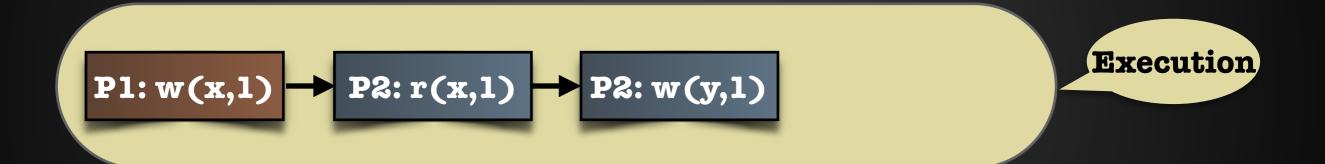
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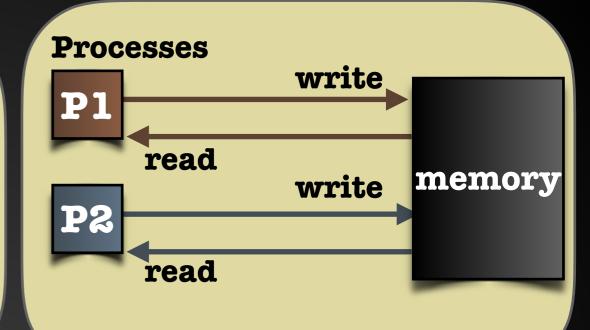


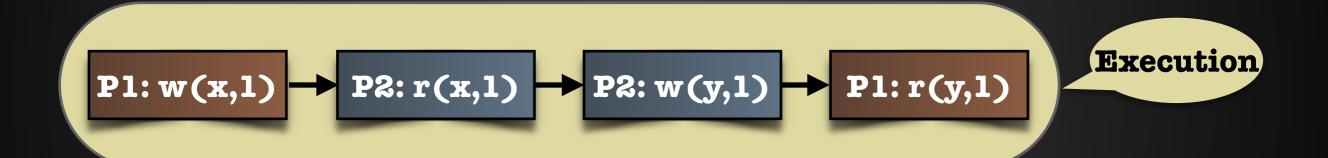
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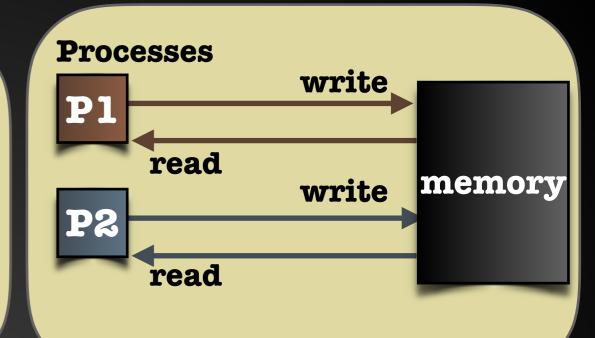


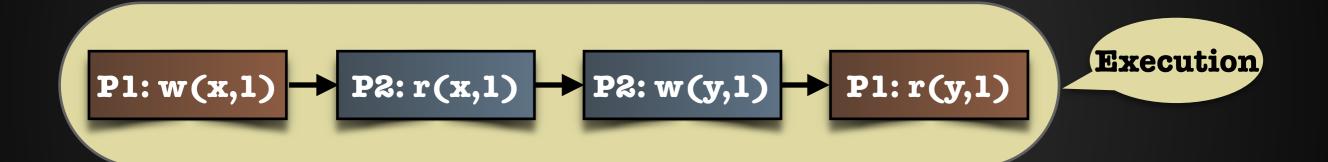
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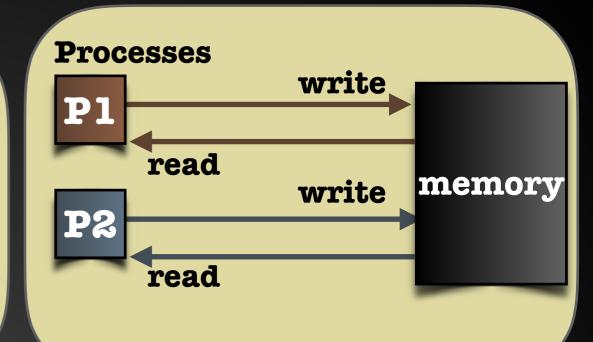


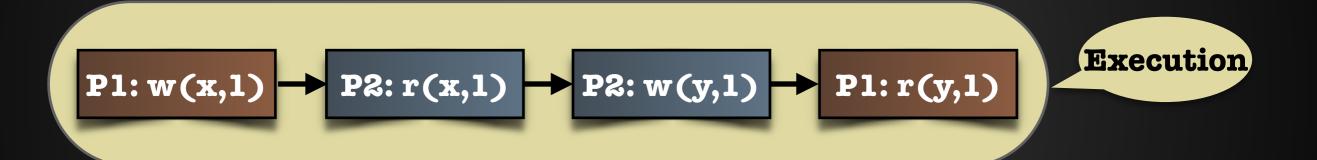
- Shared memory
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- + Simple and intuitive



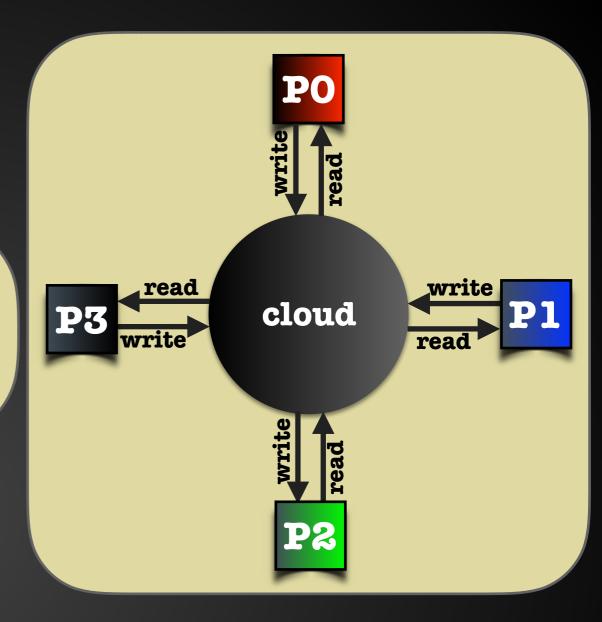


- Shared memory
- Processes: atomic read/write
- Interleaving of the operations
- + Simple and intuitive
- Too strong

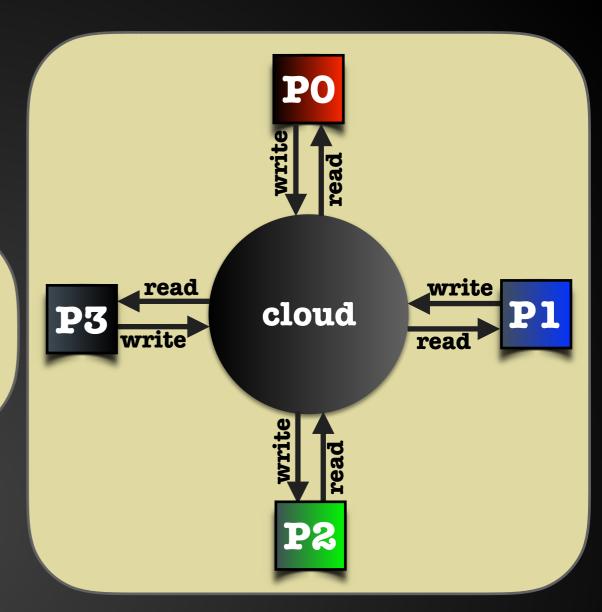




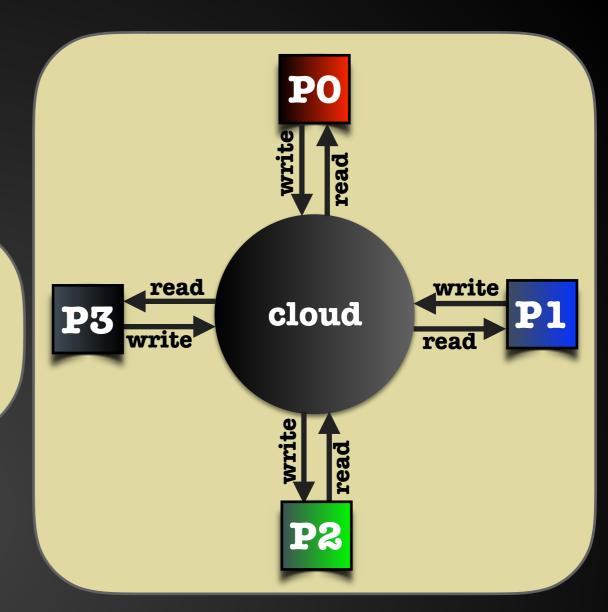
- Processes perform local operations
- Operations propagated asynchronously



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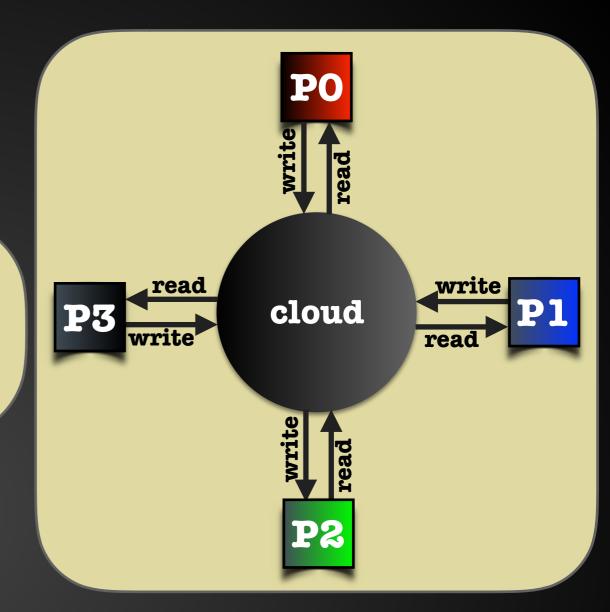


- Processes perform local operations
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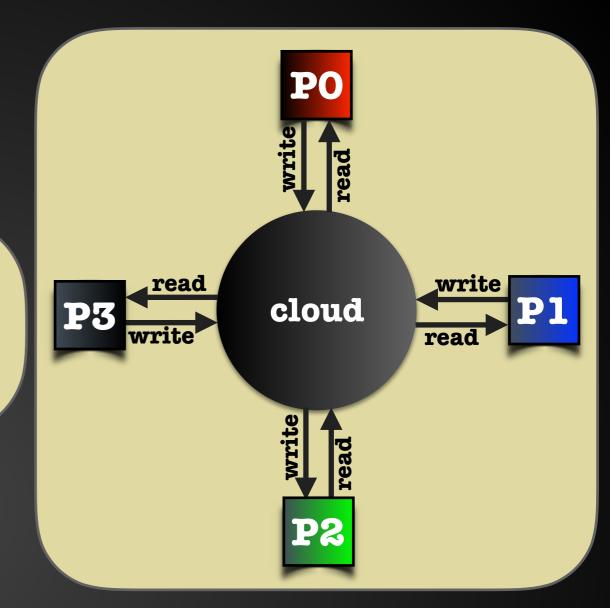
P0: w(x,1)

- Processes perform local operations
- Operations propagated asynchronously



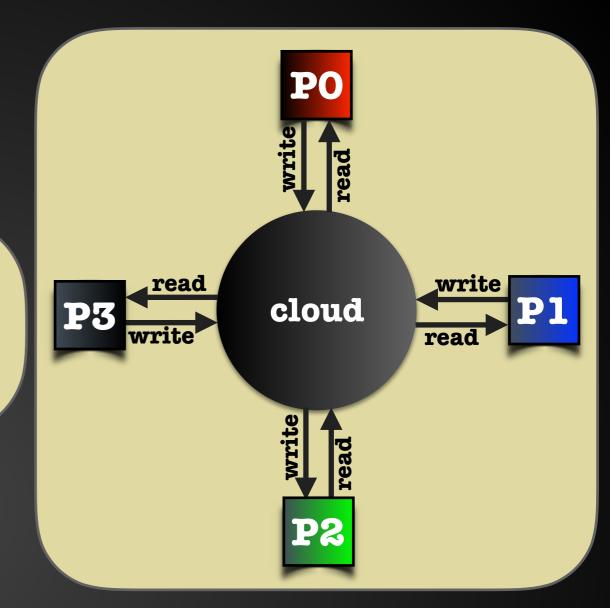
P0: $w(x,1) \rightarrow P1: w(x,2)$

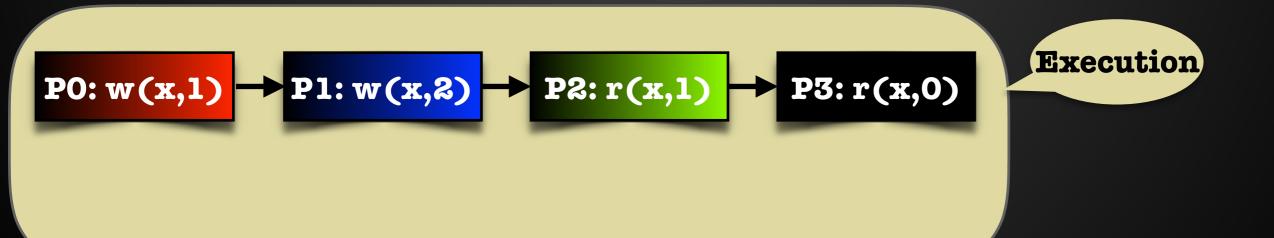
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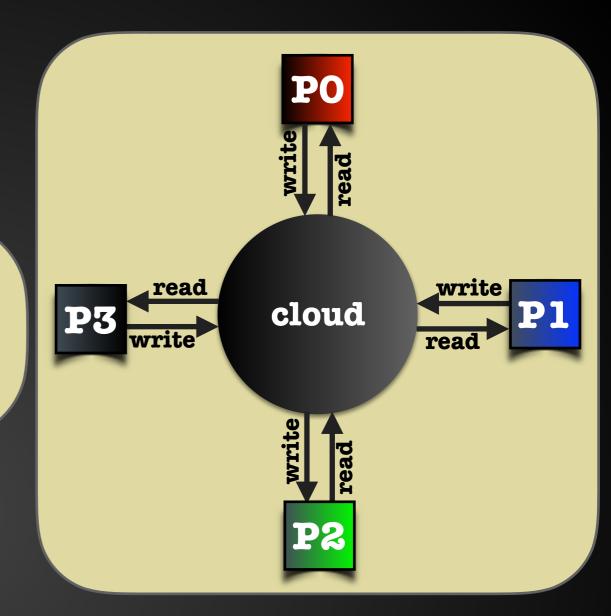


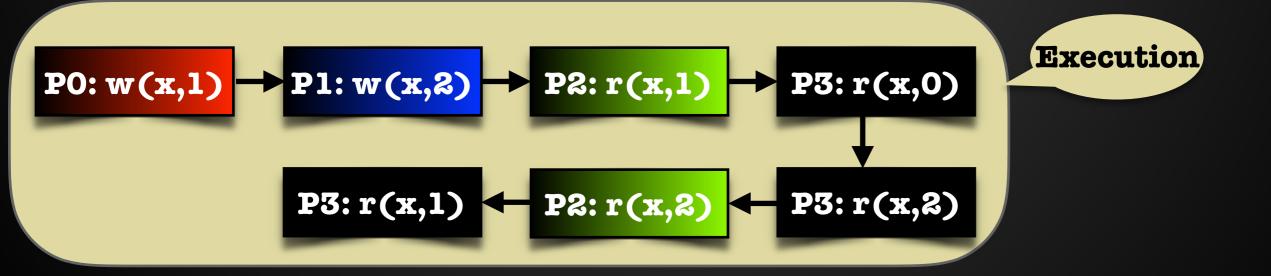
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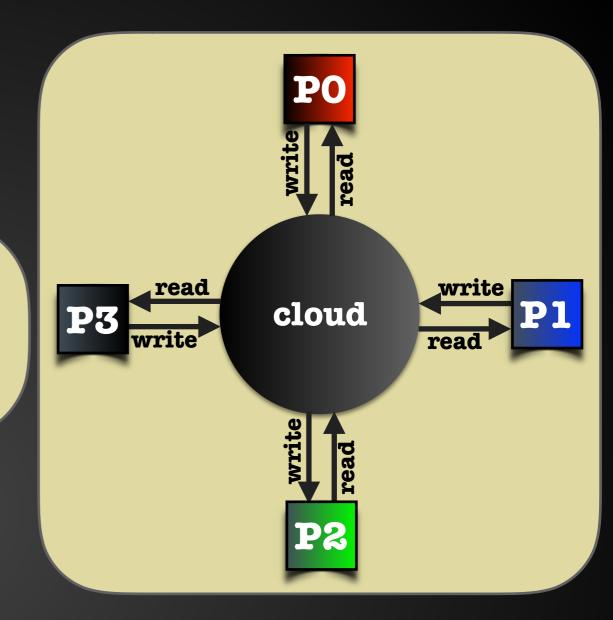


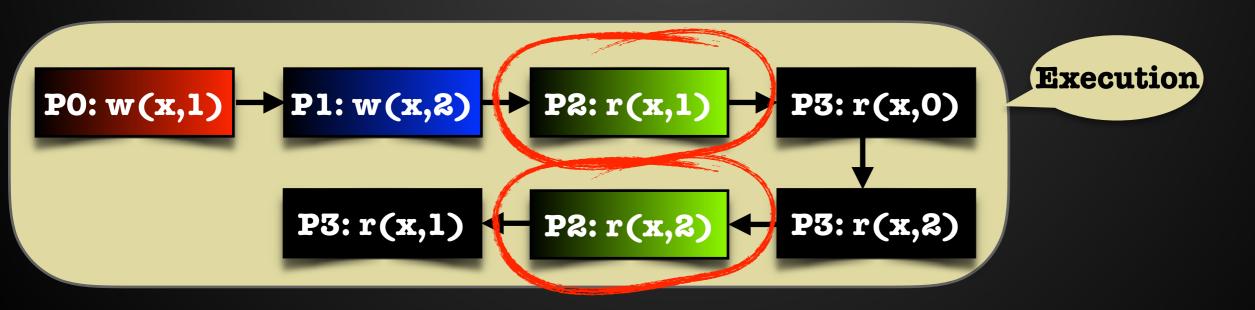
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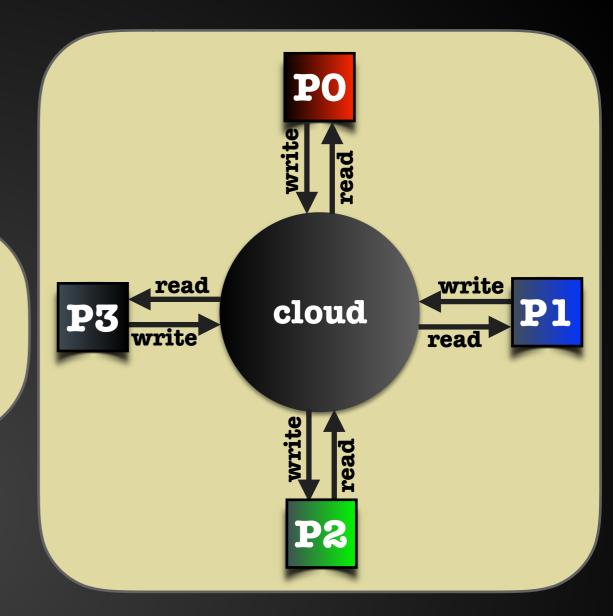


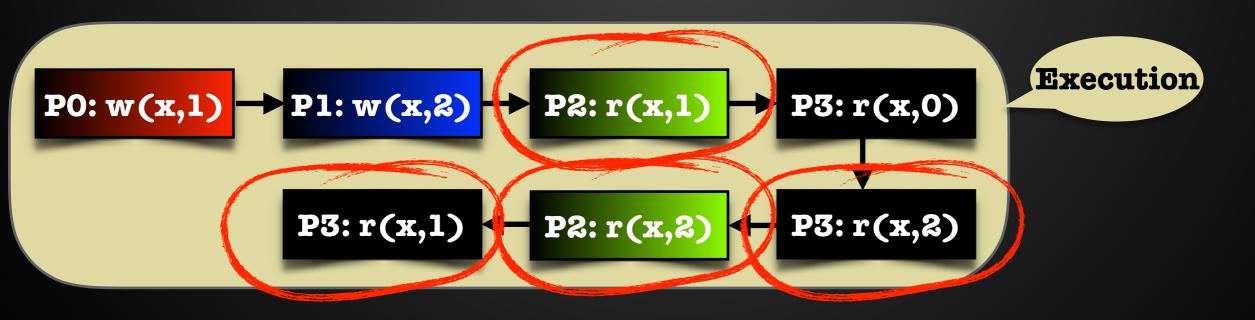
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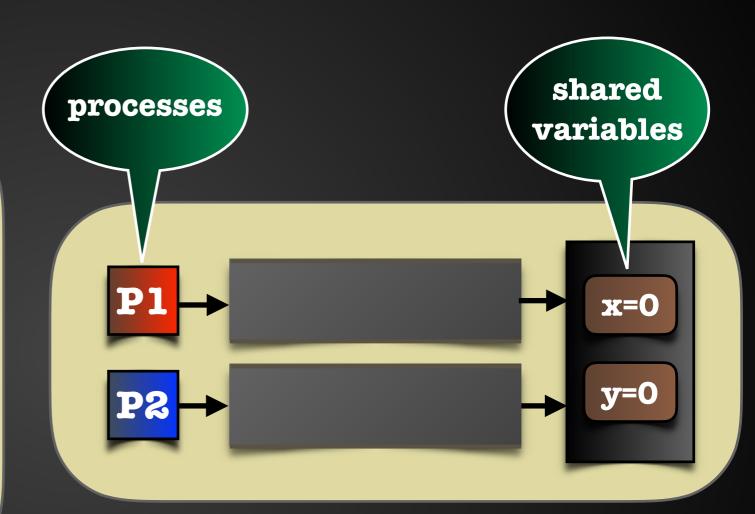
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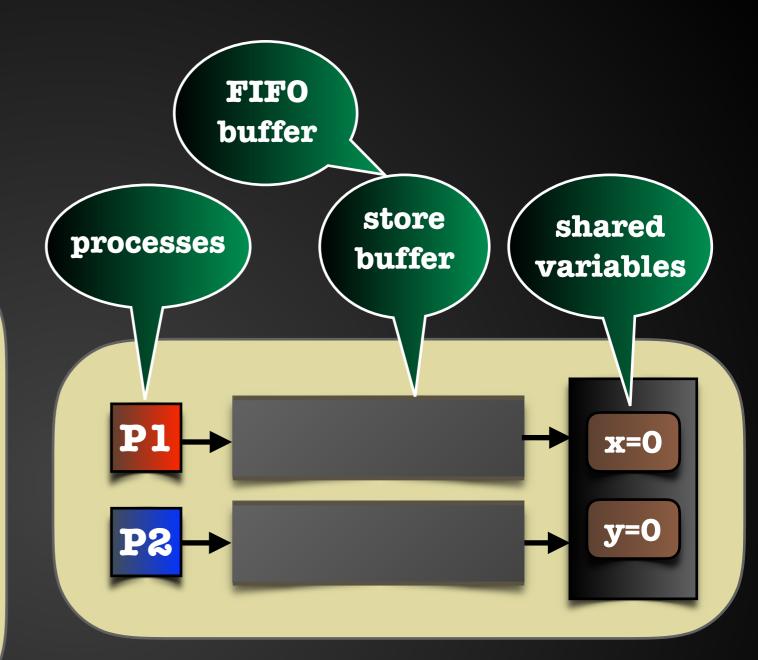
TSO - Total Store Order

- Widely used:
 - Used by Sun SPARCv9
 - Formalization of Intel x86
- Memory access optimization:
 - Write operations are slow
 - Introduce store buffers



TSO - Total Store Order

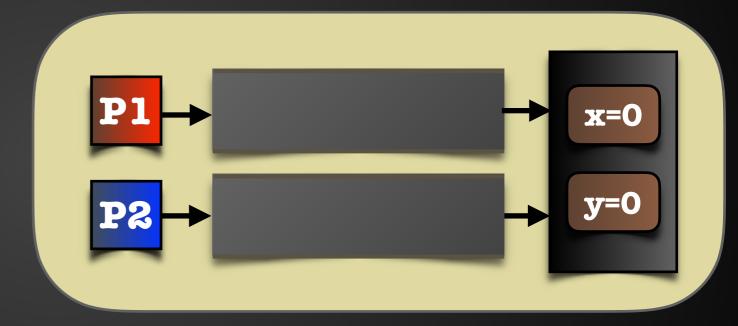
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P1: write: x = 1

P1: write: x = 2

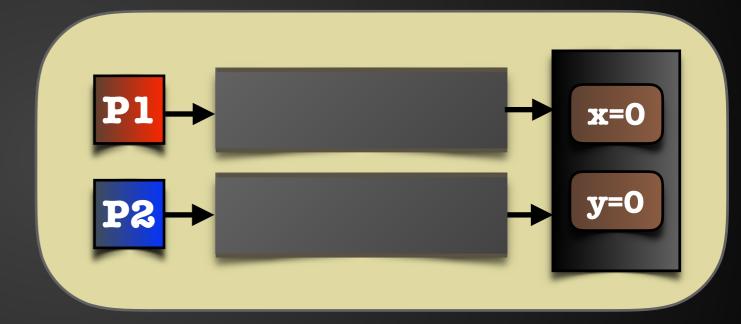
P1: read: x = 2



P1: write: x=1

P1: write: x = 2

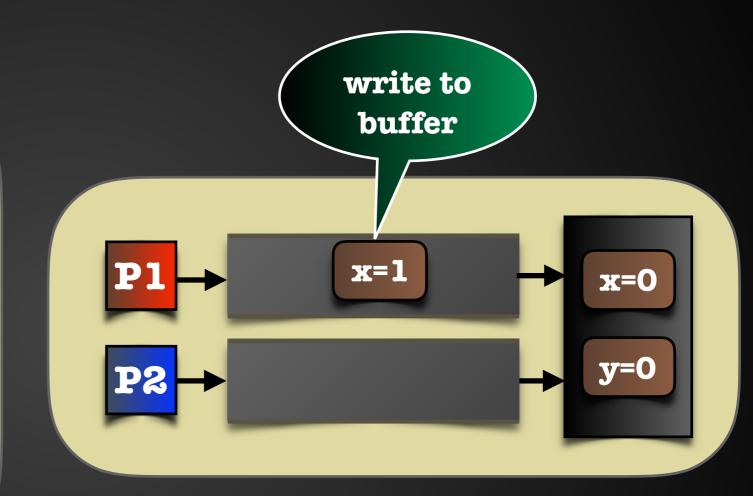
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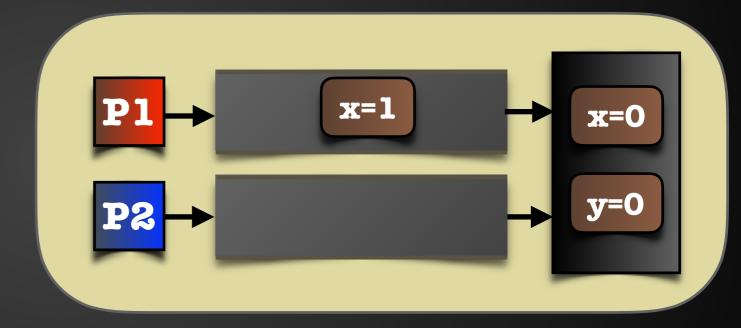
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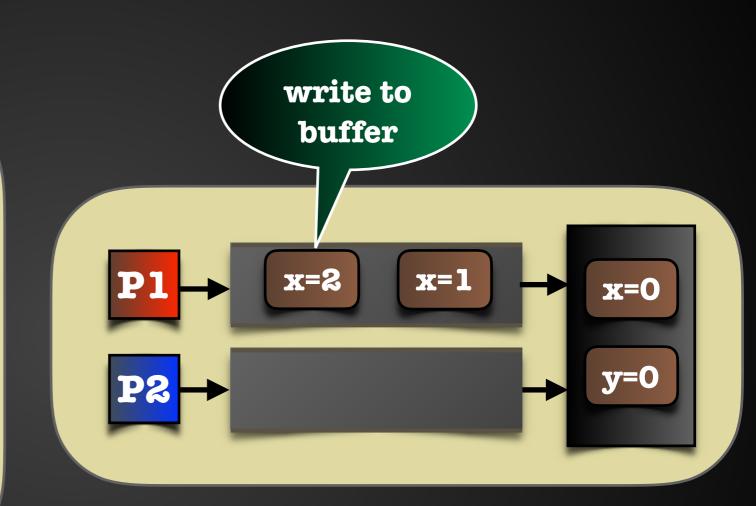
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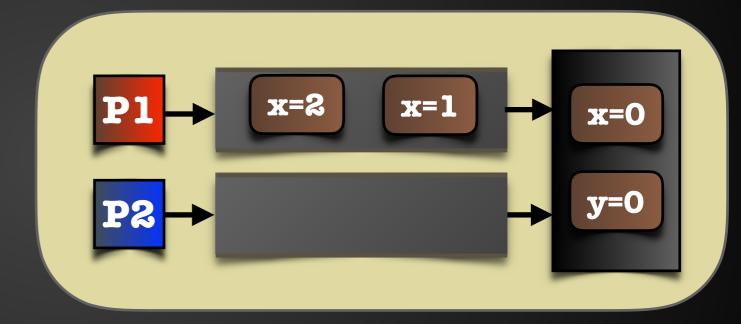
P1: read: x = 2



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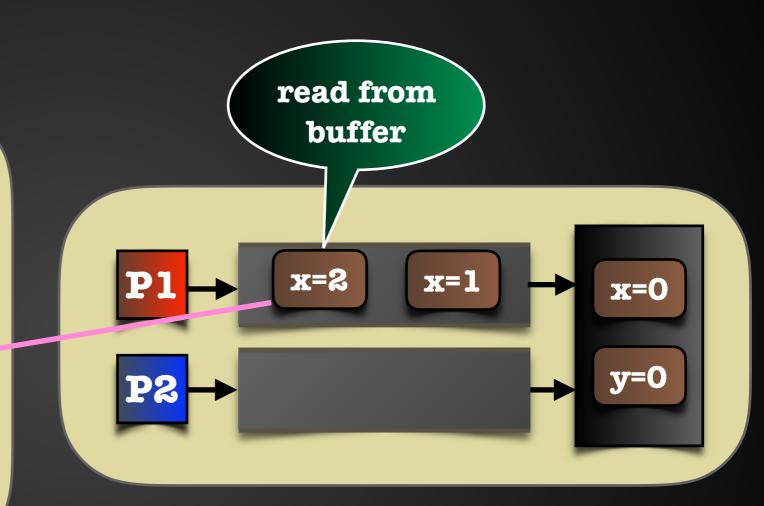
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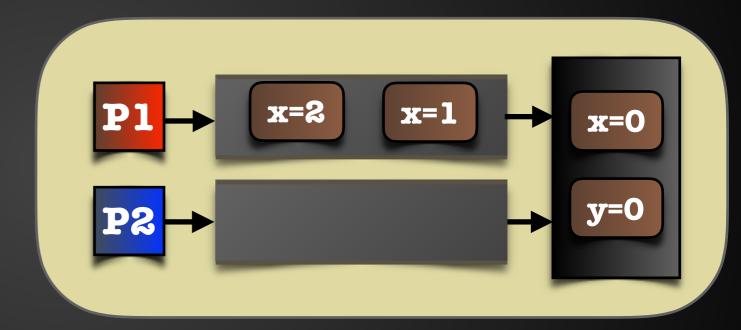
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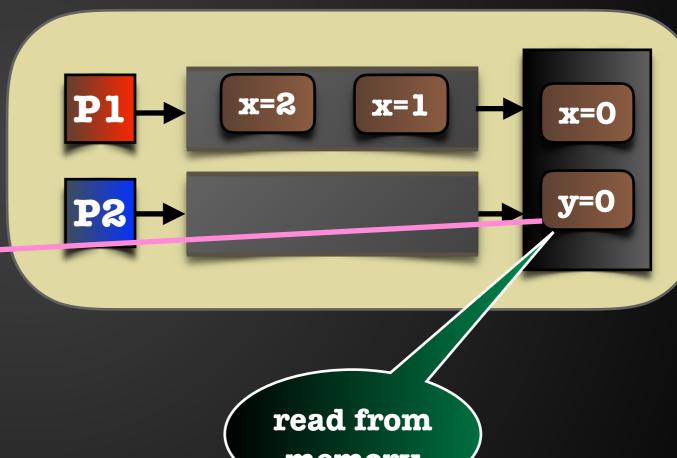


P1: write: x = 1

P1: write: x = 2

P1: read: x = 2

P1: read: y = 0

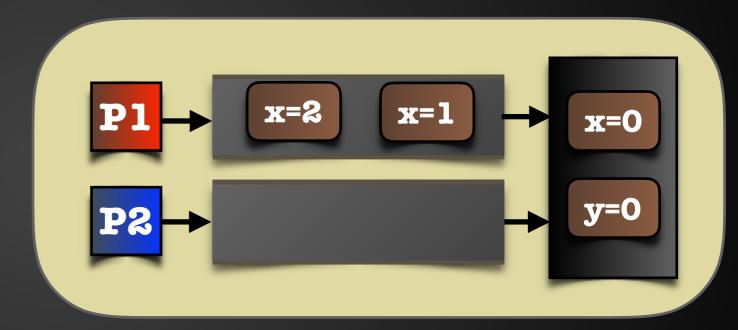


memory

P1: write: x = 1

P1: write: x = 2

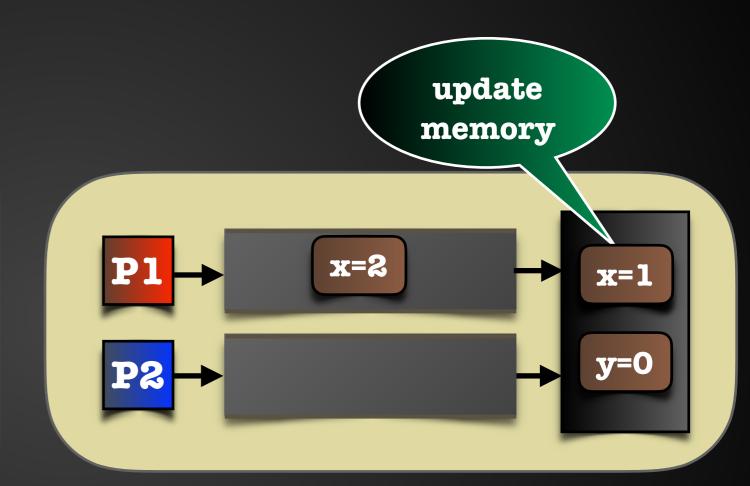
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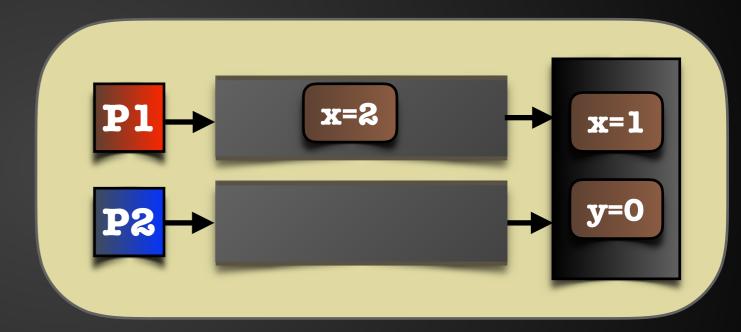
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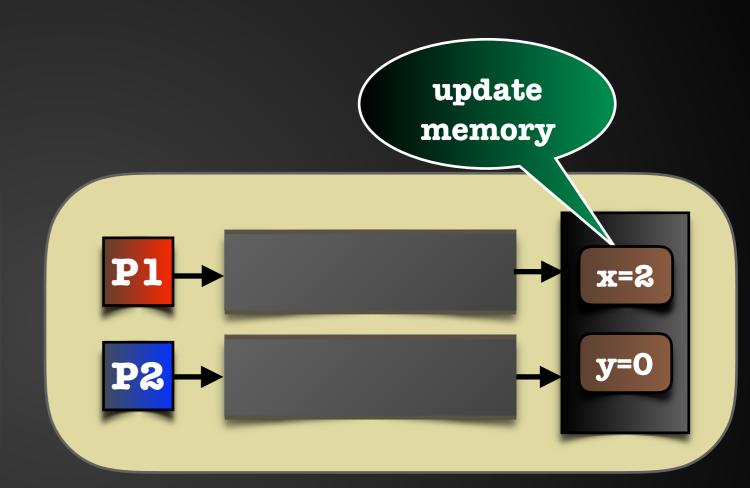
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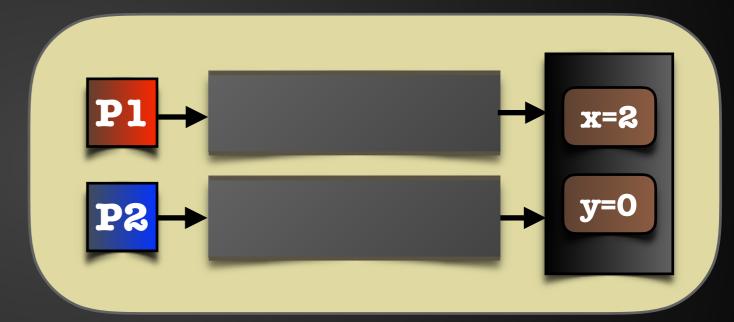


P1: write: x = 1

P1: write: x = 2

P1: read: x = 2

- write to buffer
- read from buffer
- read from memory
- update memory



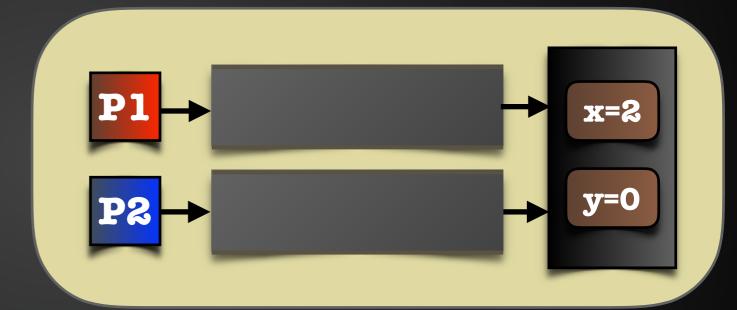
TSO - Classical Semantics

P1: write: x = 1

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P1: read: x = 2

P1: read: y = 0



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TSO - Classical Semantics

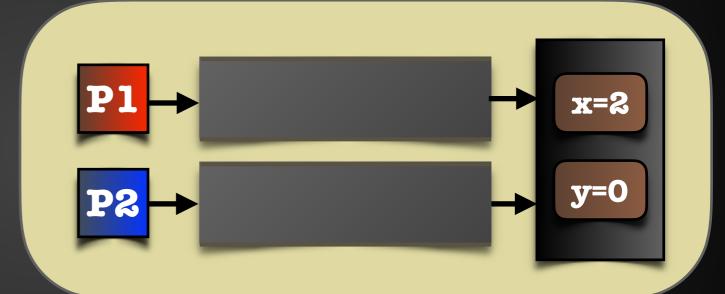
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TSO

- Extra behaviors
- Potentially bad behaviors

Initially:
$$x = y = 0$$

P1 P2

write: x = 1 write: y = 1

read: y = 0 read: x = 0

critical section critical section

P1

P2

x = 0

y = 0

Sequential Consistency = Interleaving

Initially:
$$x = y = 0$$

P1

P2

write: x = 1

write: y = 1

read: y = **0**

read: x = **0**

critical section

critical section

P1

Pa

x = 0

y = 0

At most one process at its CS at any time

Sequential Consistency = Interleaving

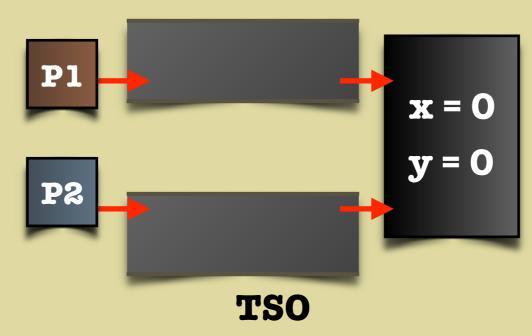
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P1 P2

write: x = 1 write: y = 1

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critical section critical section



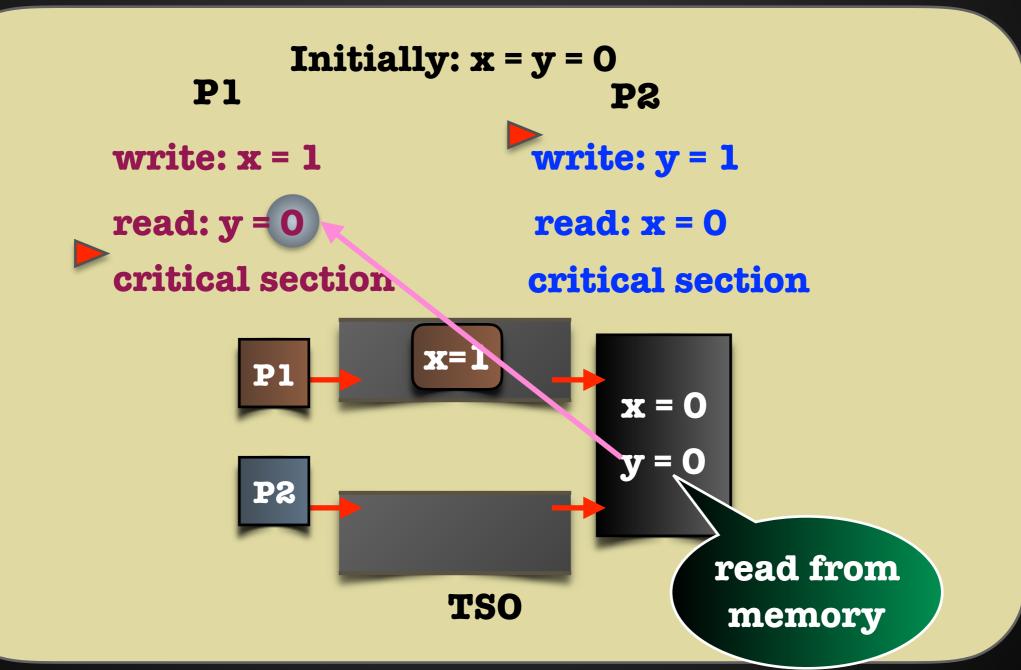
Initially: x = y = 0
P1
P2
write: x = 1
read: y = 0
critical section

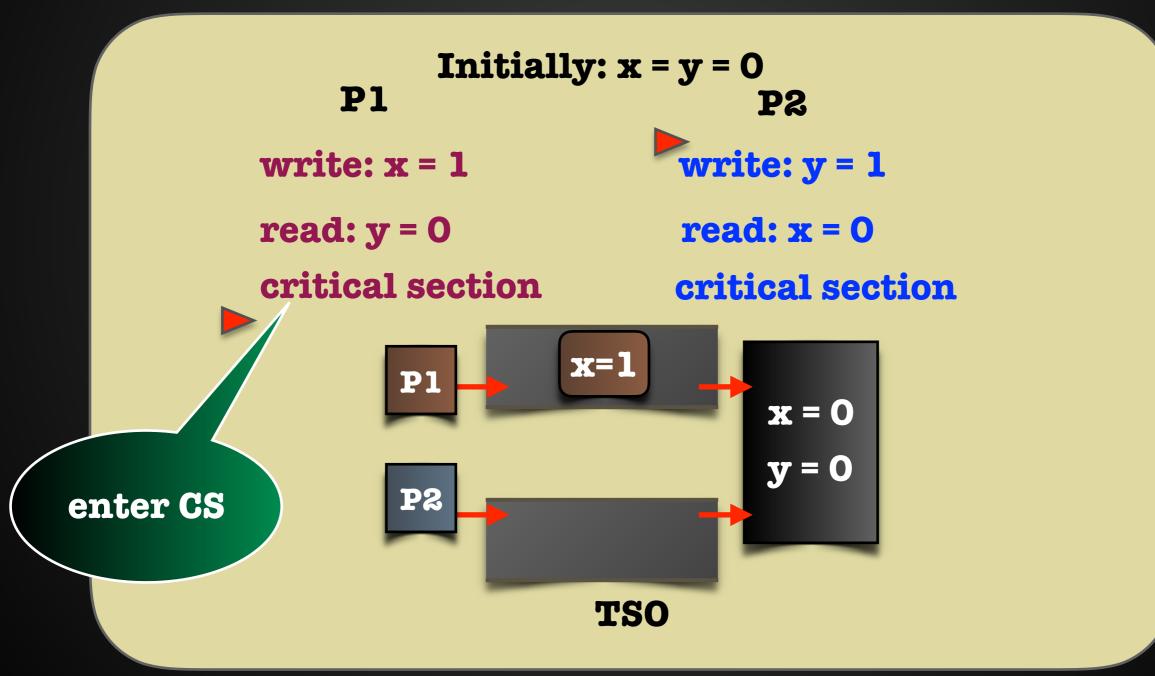
x=0
y = 0

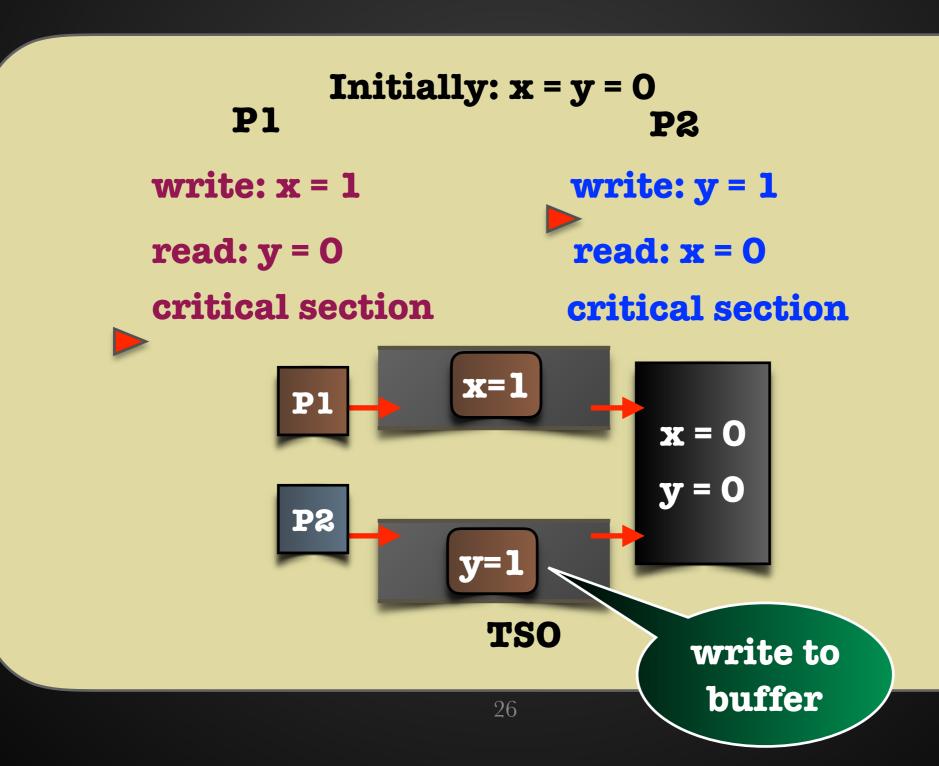
TSO

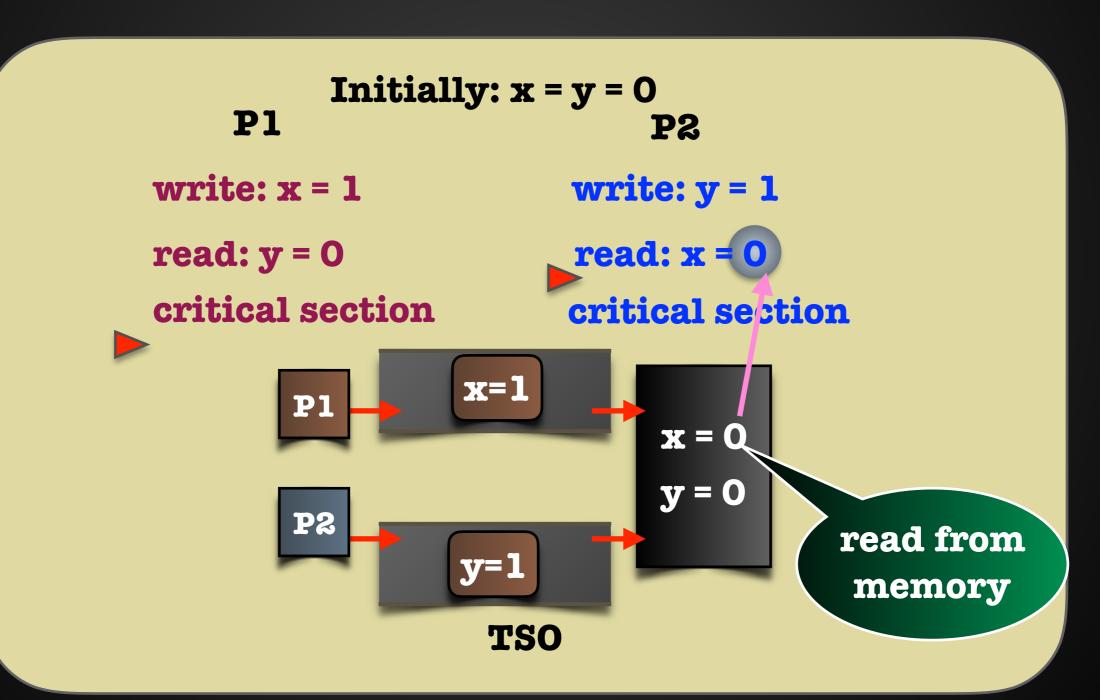
P2

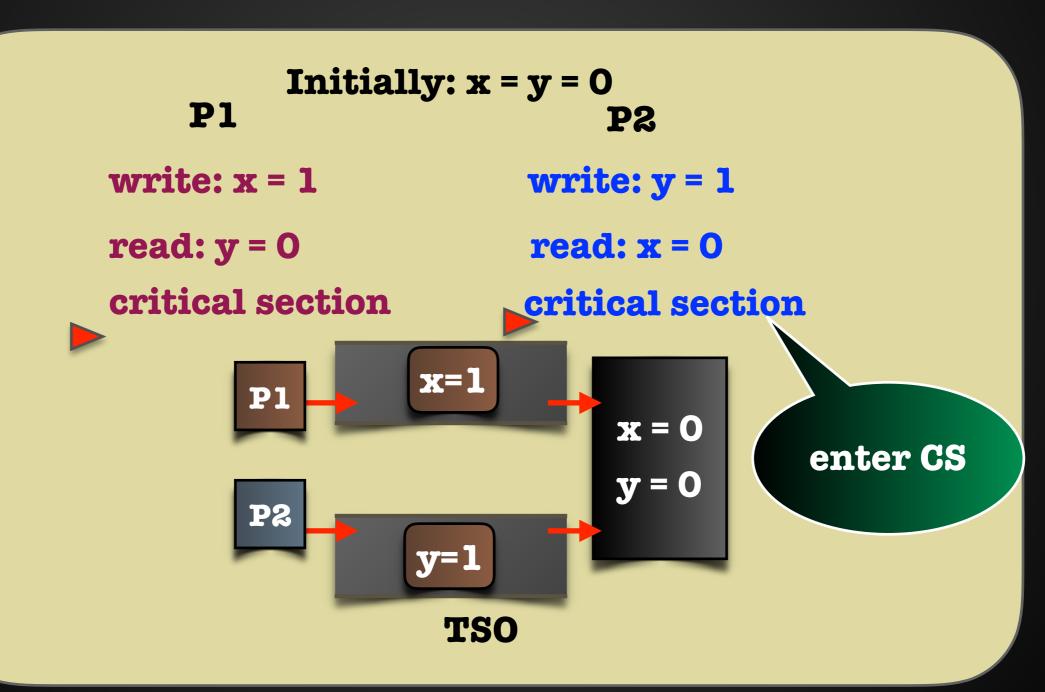
write to buffer











2 processes in CS

at the same time

Initially:
$$x = y = 0$$

write: y = **1**

read: x = 0

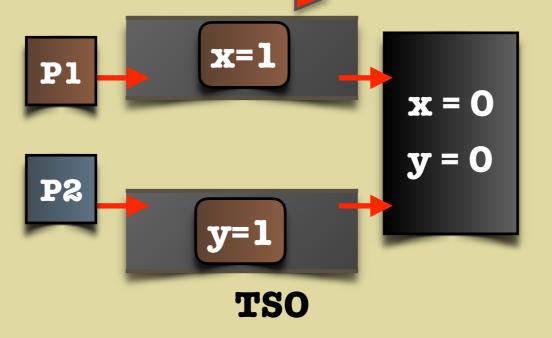
critical section crit

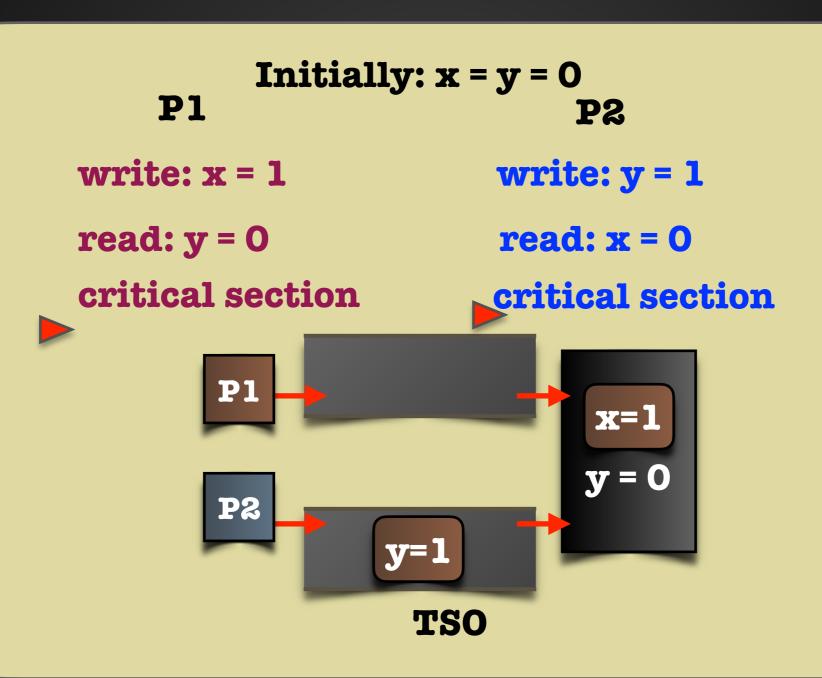
P1

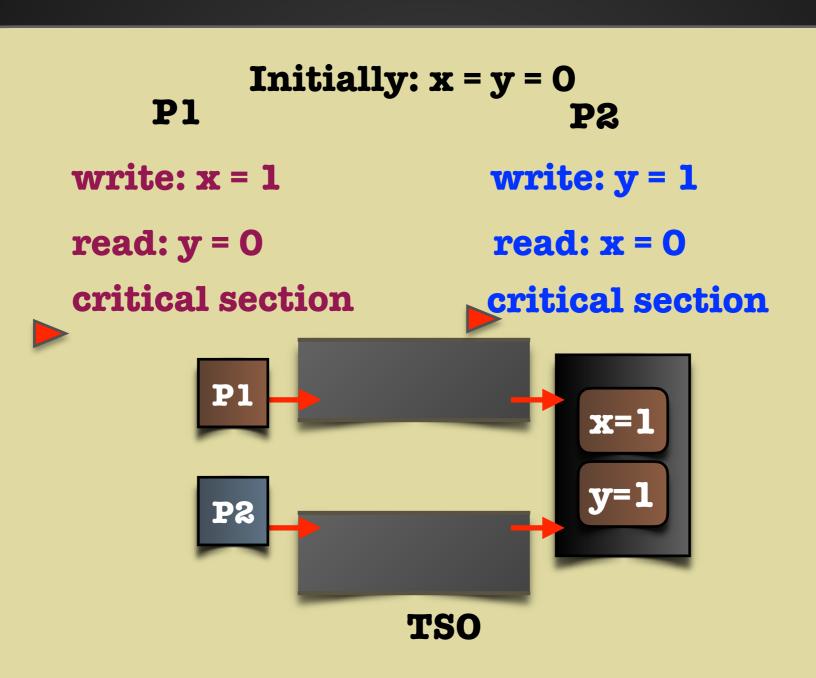
write: x = 1

read: y = **0**

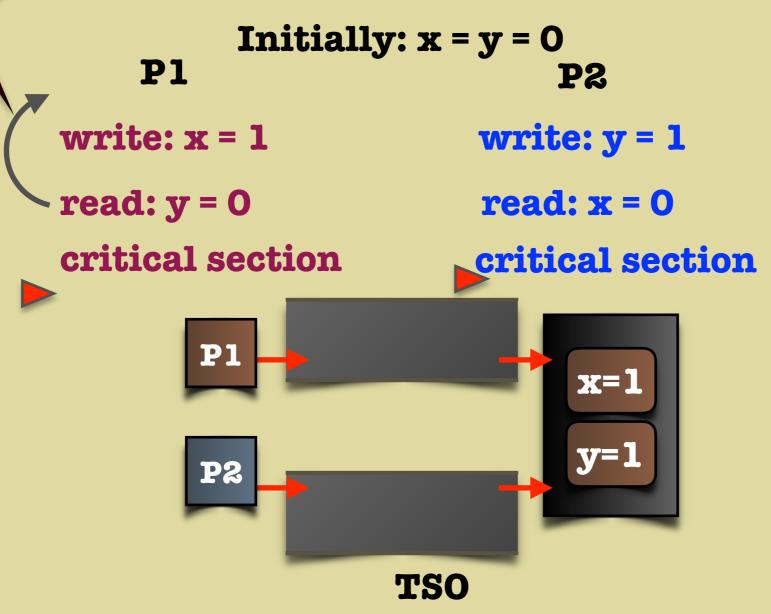
critical section







"read overtaking write"



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write: x = 1

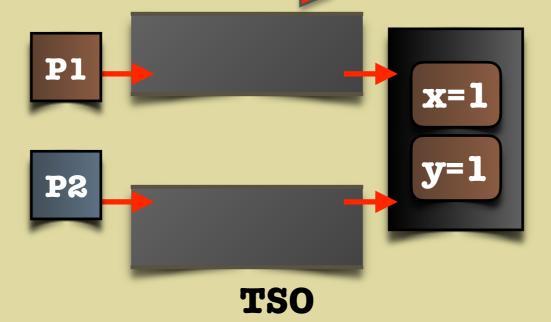
read: y = **0**

critical section

write: y = 1

read: x = 0

critical section



Weakly Consistent Systems

- Cloud
- Weak memories
- Weak cache protocols
- etc

- + Efficiency
- Non-intuitive behaviours

Weakly Consistent Systems

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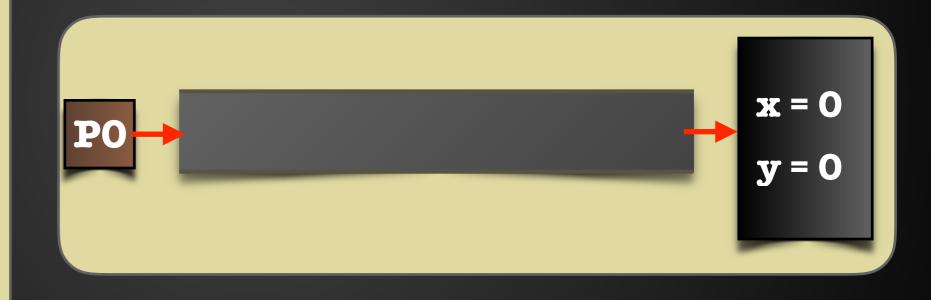
- + Efficiency
- Non-intuitive behaviours

- Semantics
- Correctness analysis: simualtion, testing, verification, synthesis
- Methods and tools: decidability, complexity, algorithms
- Specifications

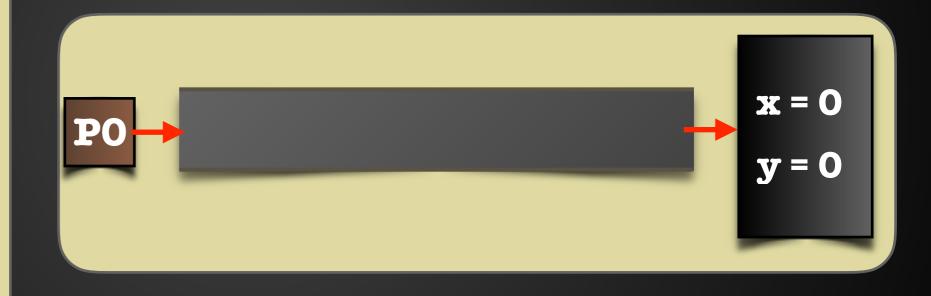
while (1)
write: x=1



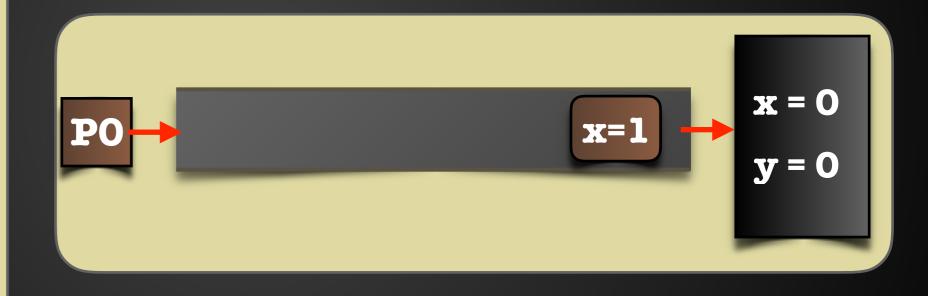
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while (1)
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PO: write: x = 1
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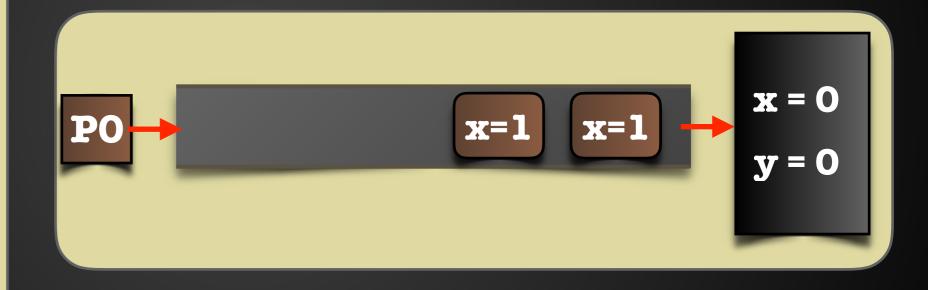
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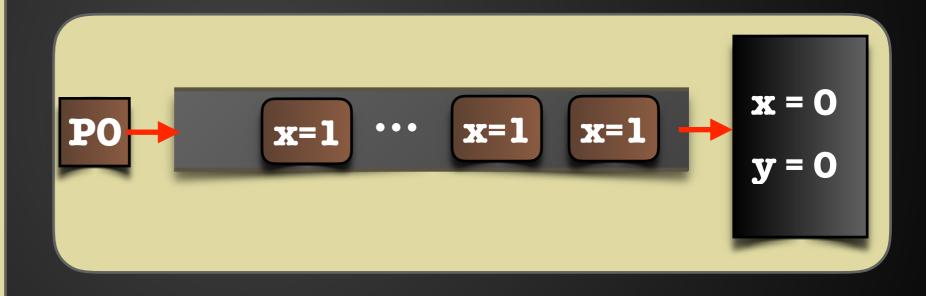
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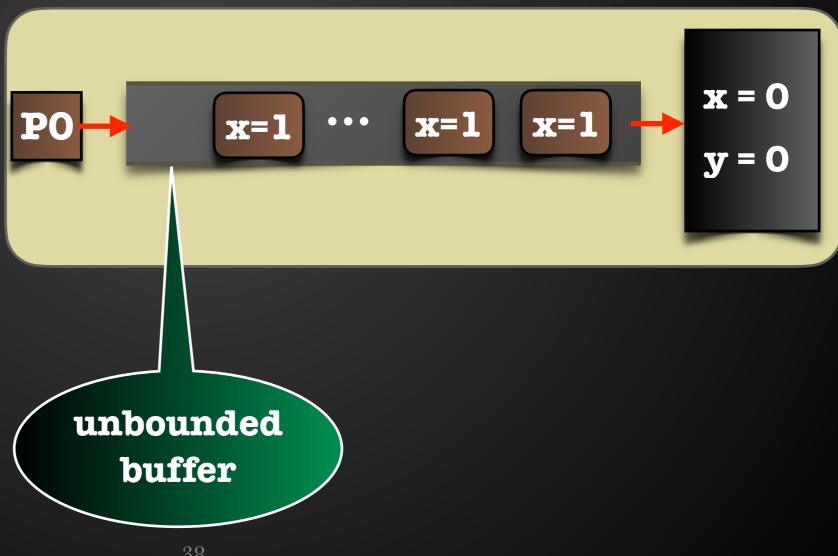
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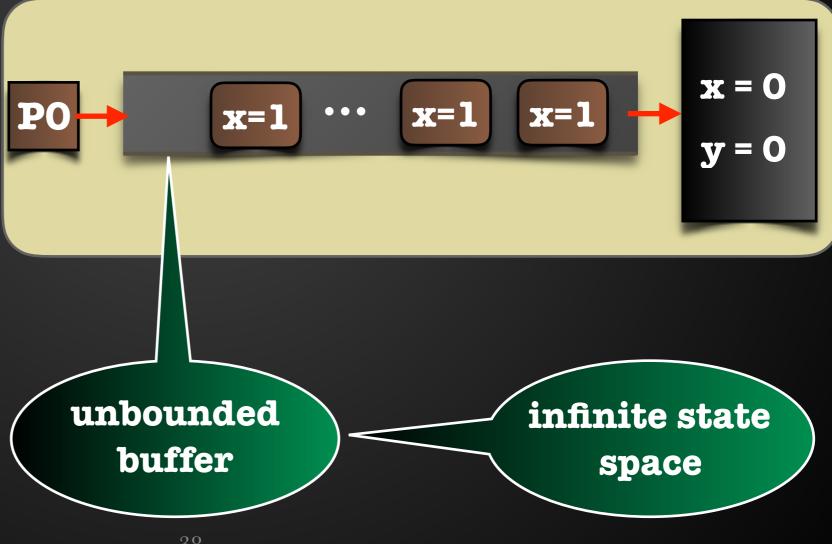
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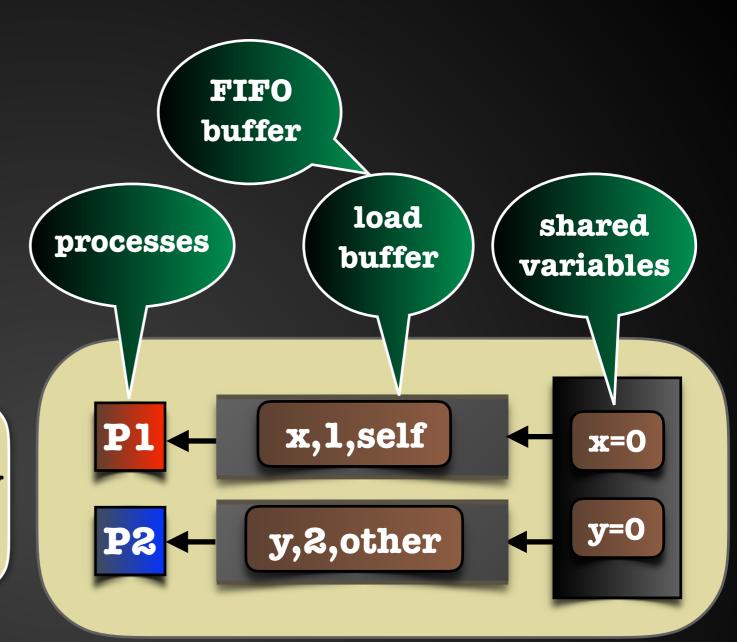


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- Total Store Order (TSO)
- Dual TSO
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- Synthesis

Dual TSO

- store buffer load buffer
- write immediately updates memory
- buffers contain expected reads
- messages: self, other

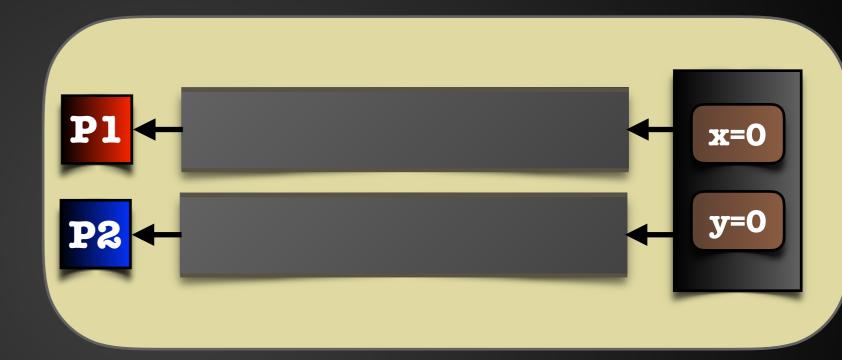


Dual TSO

P1: write: x = 1

P1: read: x = 1

P1: read: y = 0



P1: write: x = 1

P1: read: x = 1

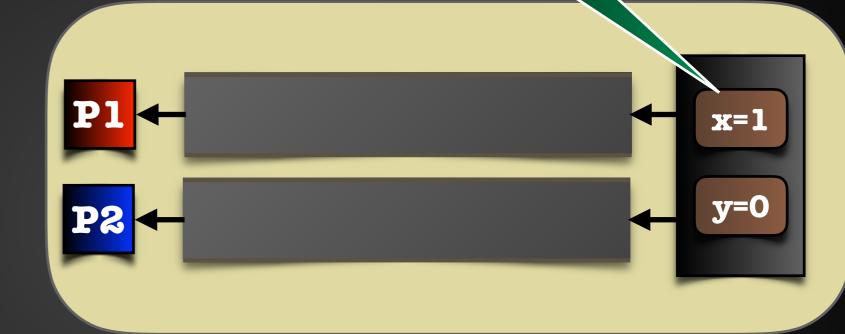


P1: write: x = 1

P1: read: x = 1

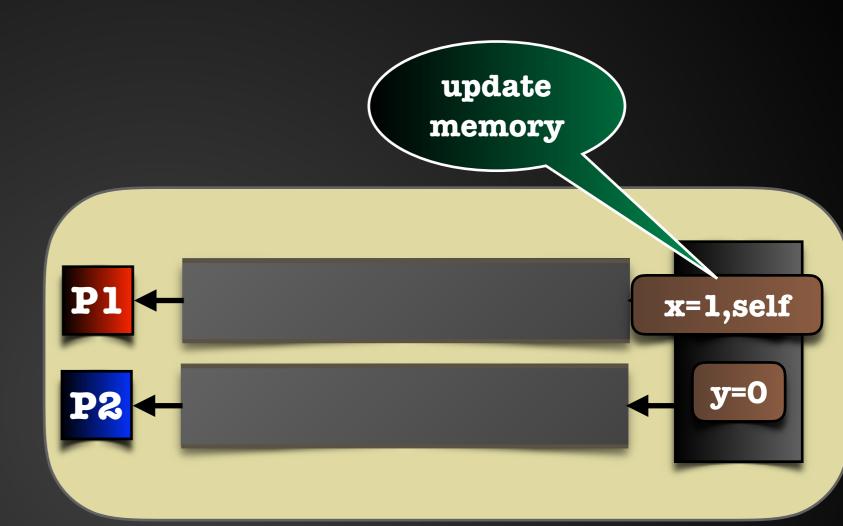
P1: read: y = 0

update memory



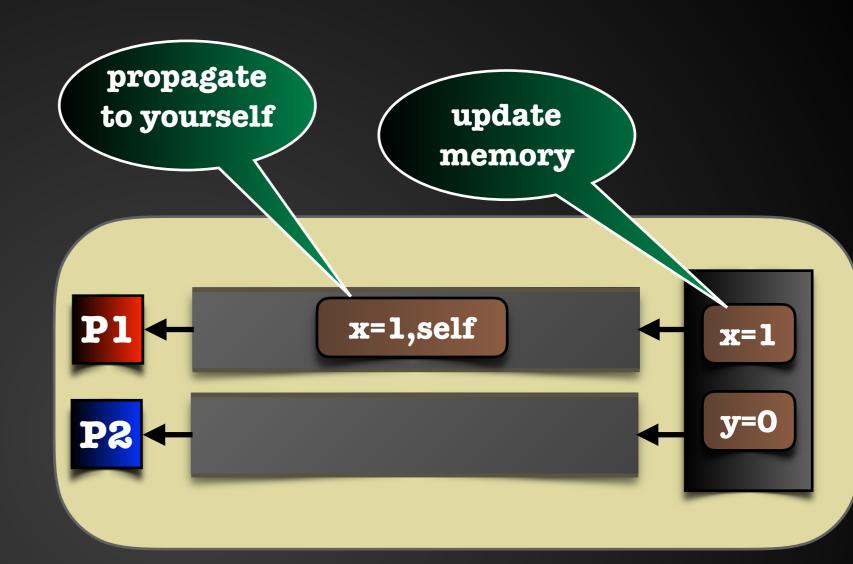
P1: write: x = 1

P1: read: x = 1



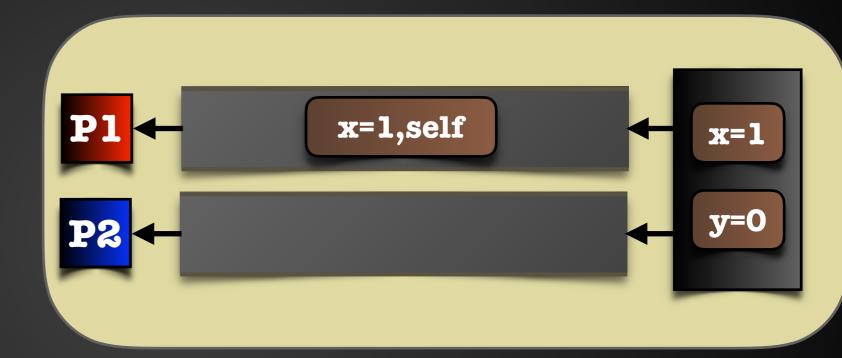
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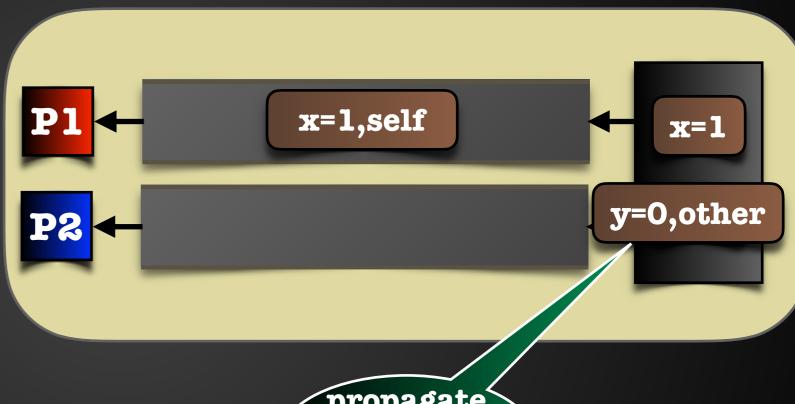


propagate from memory

P1: write: x = 1

P1: read: x = 1

P1: read: y = 0

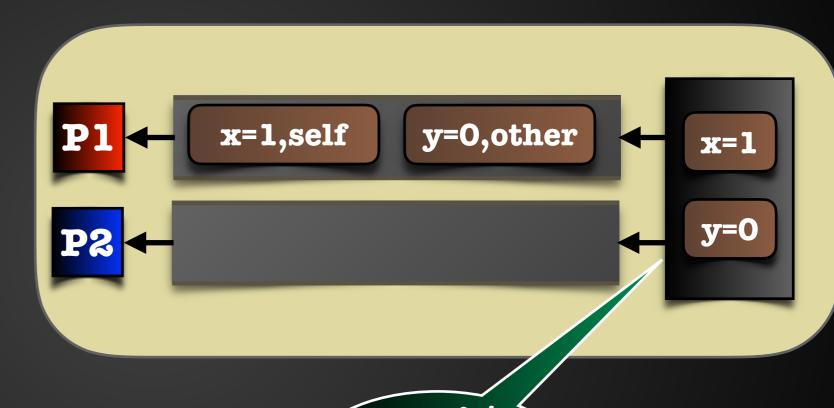


propagate from memory

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P1: read: x = 1

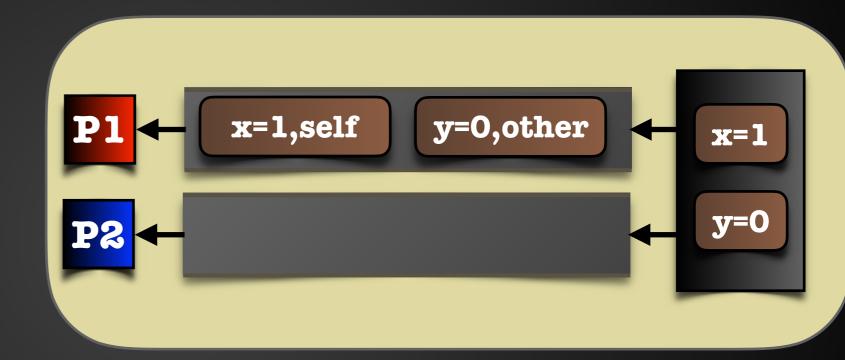
P1: read: y = 0



propagate from memory

P1: write: x = 1

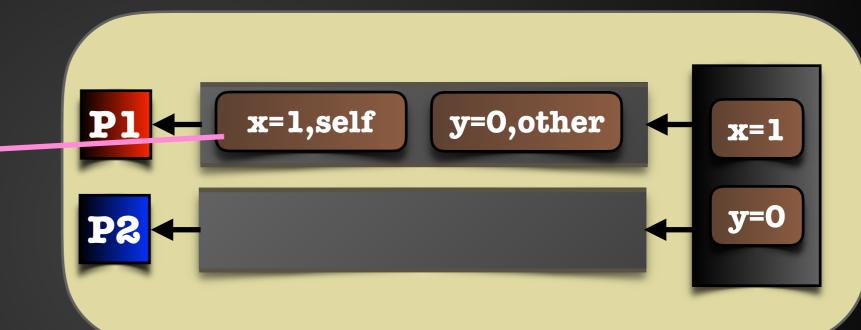
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P1: read: x = 1

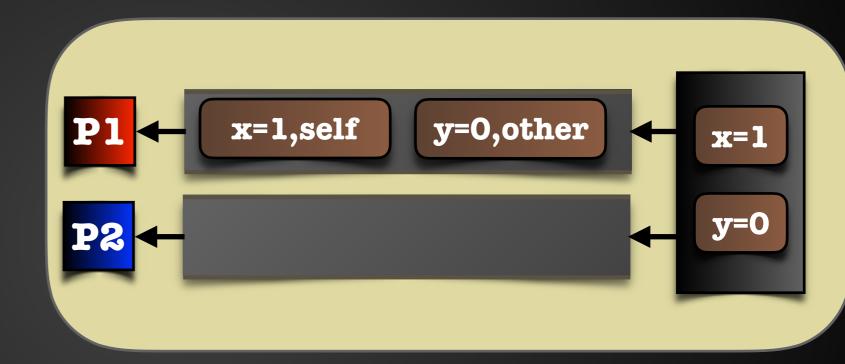
P1: read: y = 0



read own write

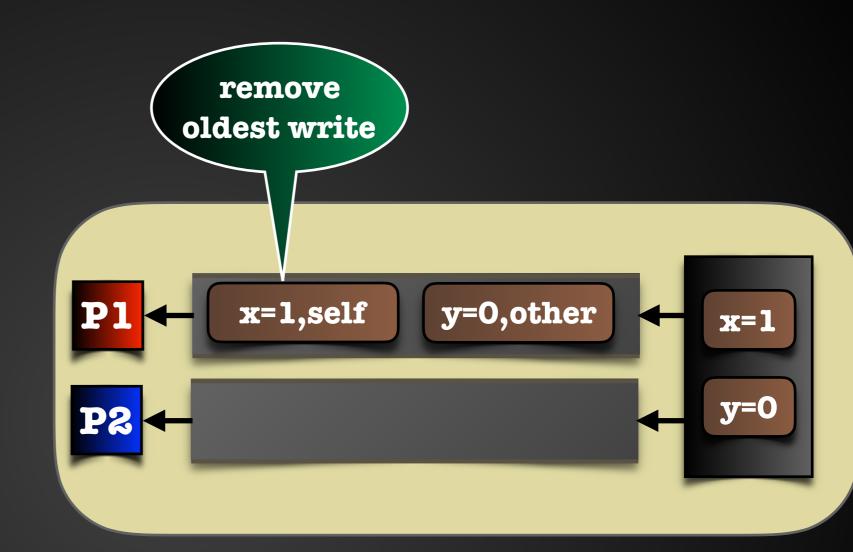
P1: write: x = 1

P1: read: x = 1



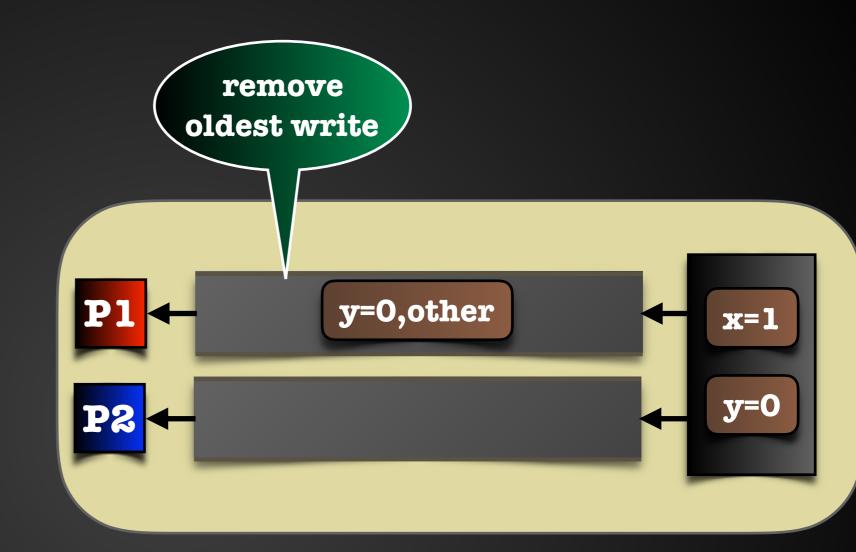
P1: write: x = 1

P1: read: x = 1



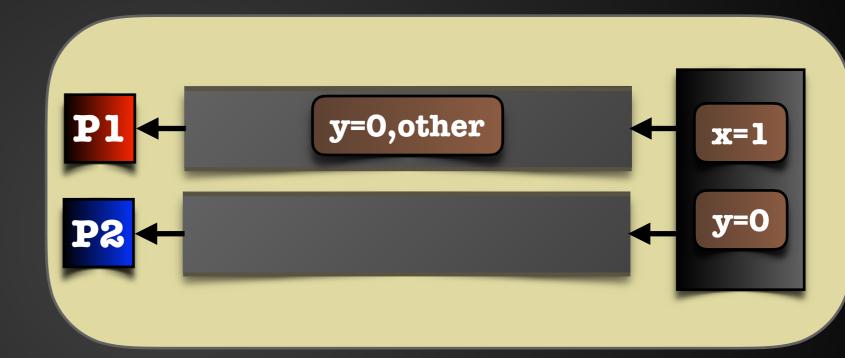
P1: write: x = 1

P1: read: x = 1



P1: write: x = 1

P1: read: x = 1



read oldest write

Dual TSO

P1: write: x = 1

P1: read: x = 1/

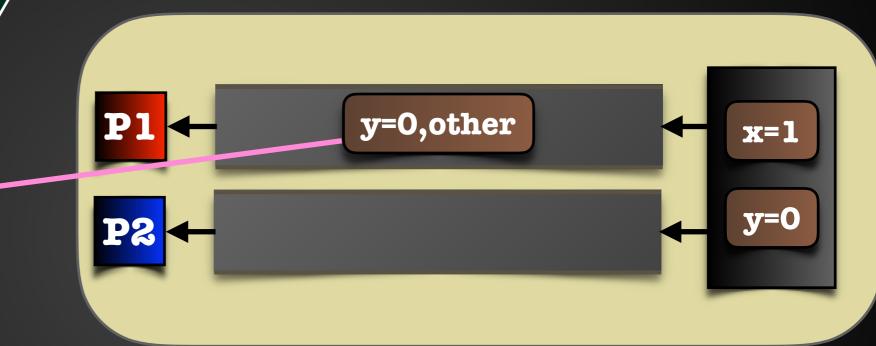


read oldest write

Dual TSO

P1: write: x = 1

P1: read: x = 1/



P1: write: x = 1

P1: read: x = 1



- write + self-propagation
- propagate from memory
- read own-writes
- read oldest write
- remove oldest write

P1: write: x = 1

P1: read: x = 1



- write + self-propagation
- propagate from memory
- read own-writes
- read oldest write
- remove oldest write

P1: write: x = 1

P1: read: x = 1



- write + self-propagation
- propagate from memory
- read own-writes
- read oldest write
- remove oldest write

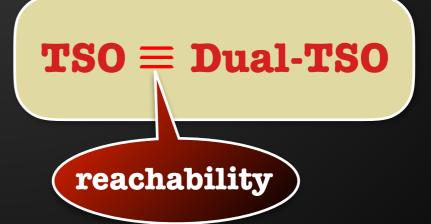
$$TSO \equiv Dual-TSO$$

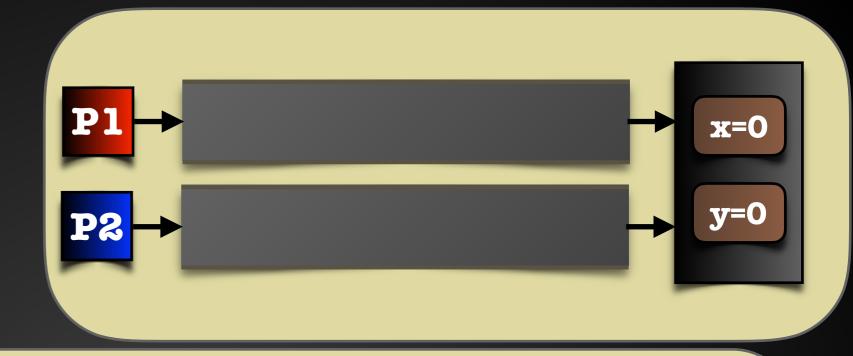
P1: write: x = 1

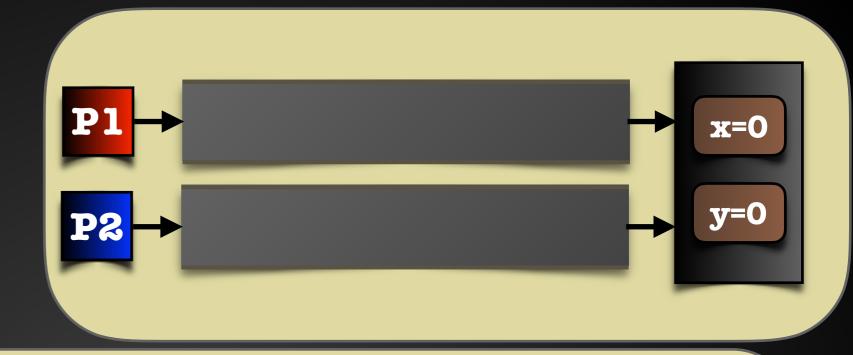
P1: read: x = 1



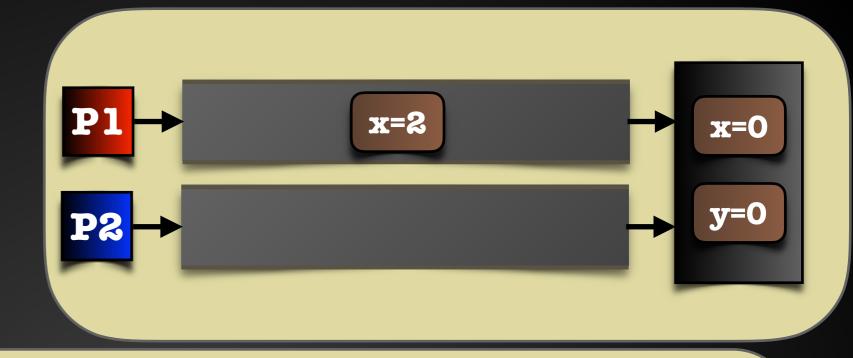
- write + self-propagation
- propagate from memory
- read own-writes
- read oldest write
- remove oldest write



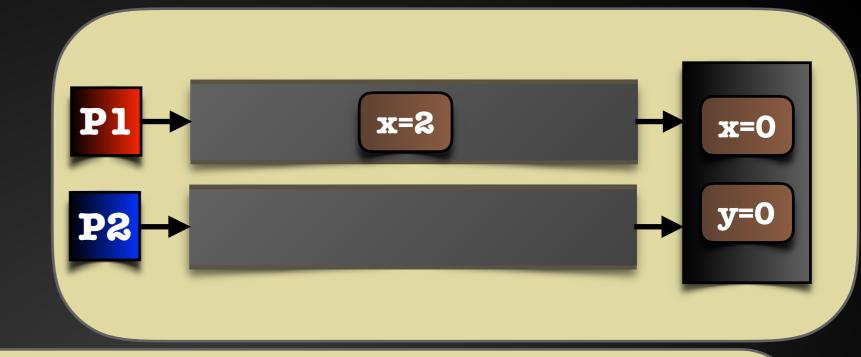




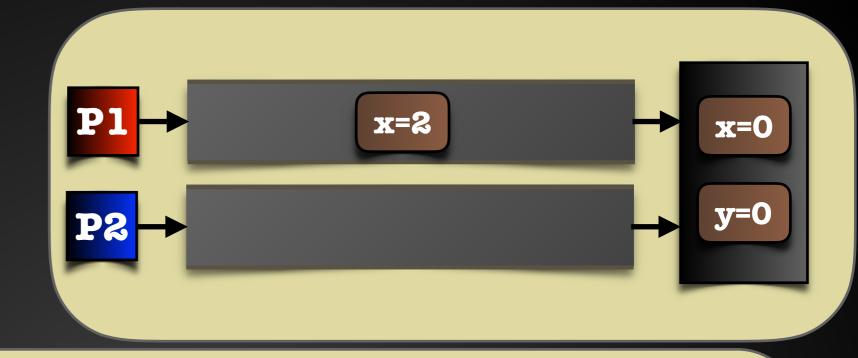
P1: w(x,2)



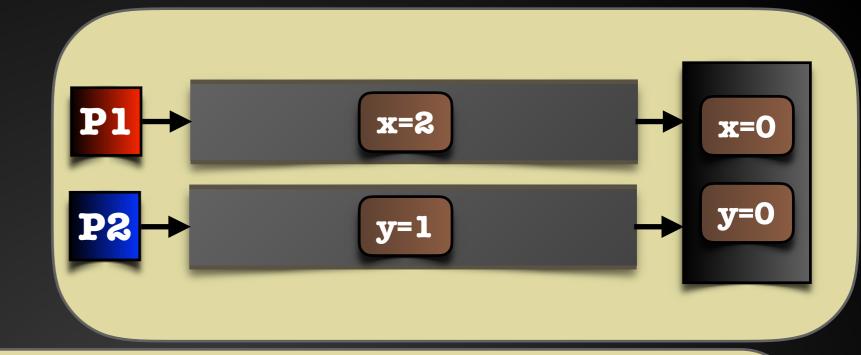
P1: w(x,2)



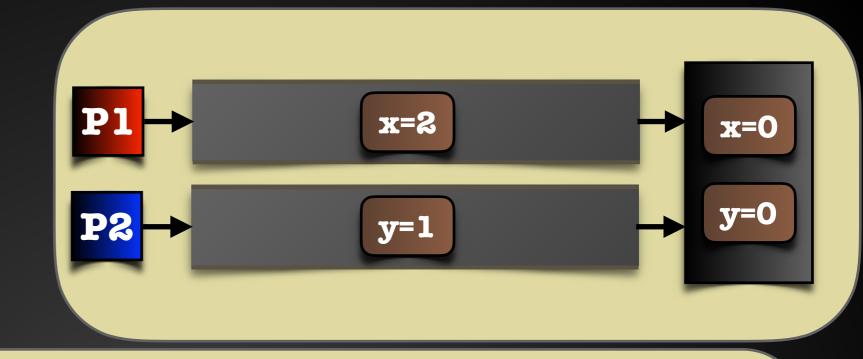
 $P1: w(x,2) \longrightarrow P1: r(y,0)$



$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1)$$



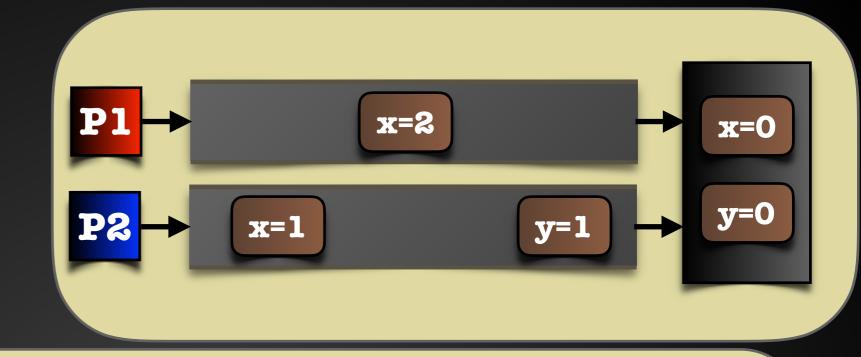




P1:
$$w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1)$$



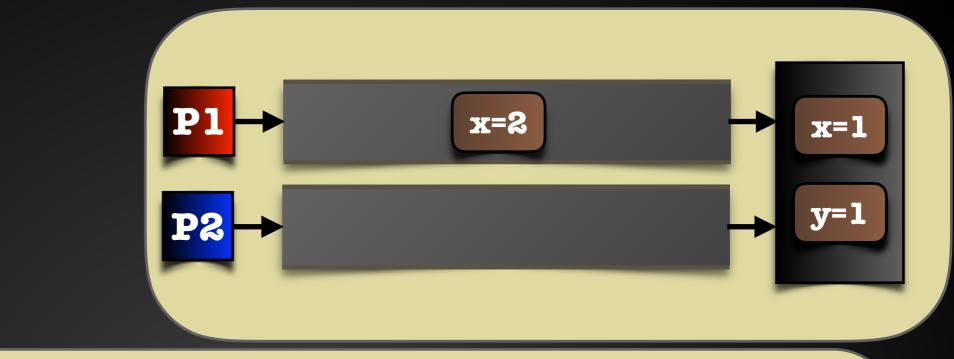
P1:
$$w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1)$$



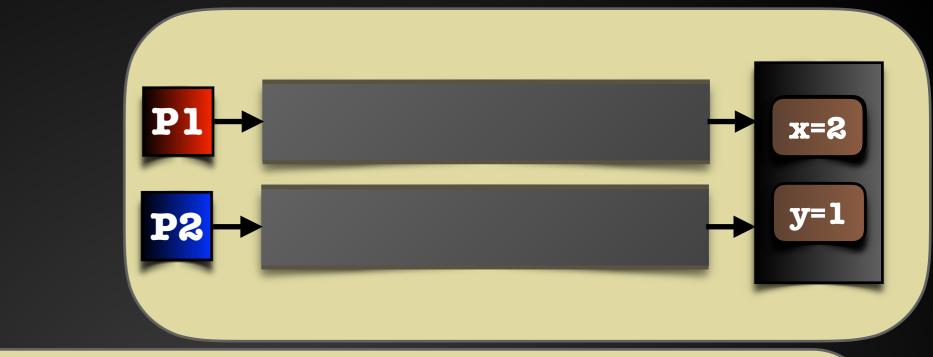
P1:
$$w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1)$$



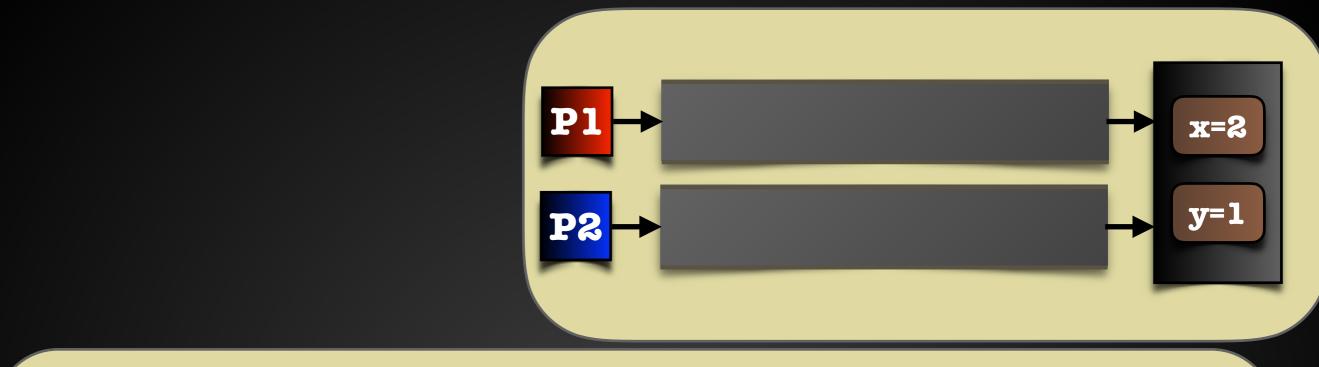
P1:
$$w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1)$$



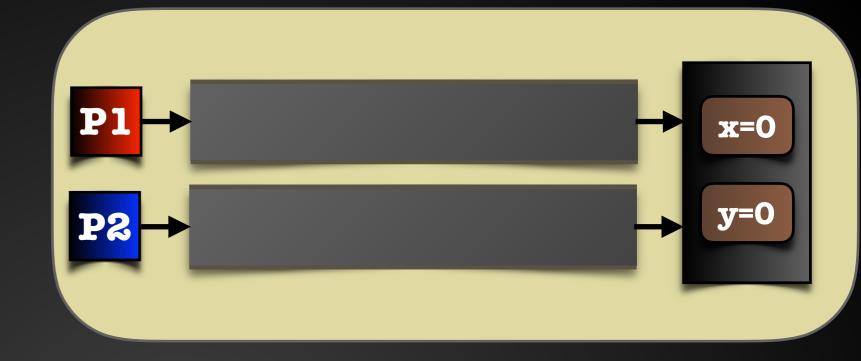
P1:
$$w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1)$$



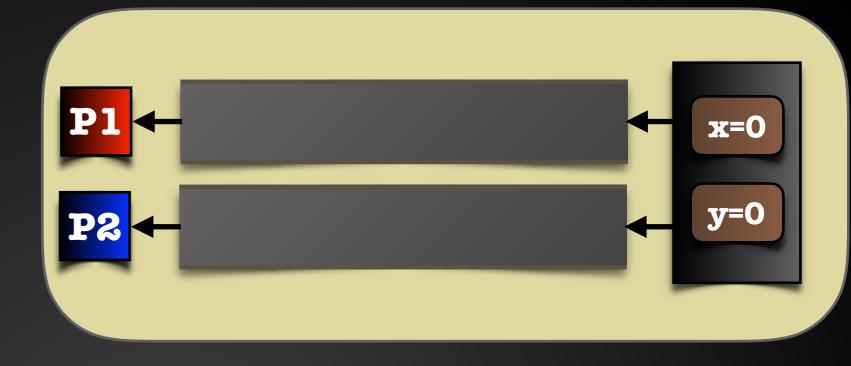




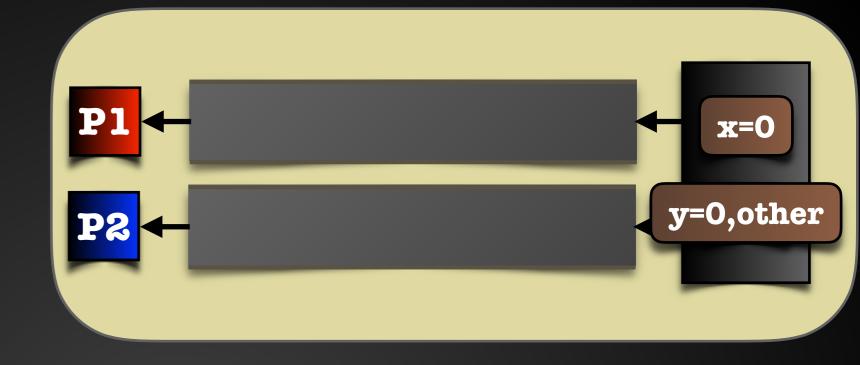




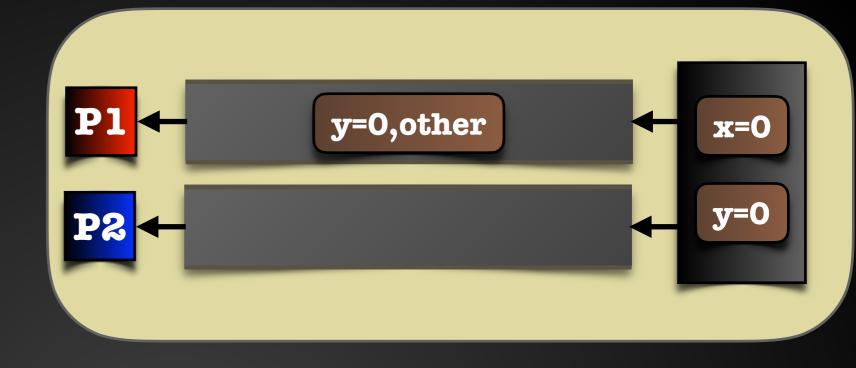




$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



P2: w(y,1)

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



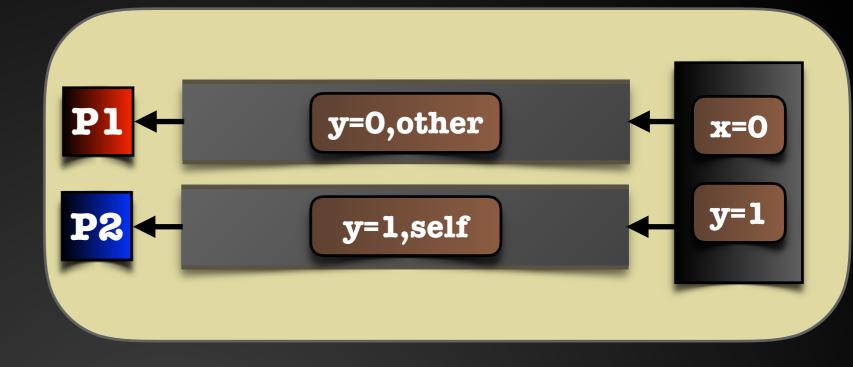
P2: w(y,1)

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



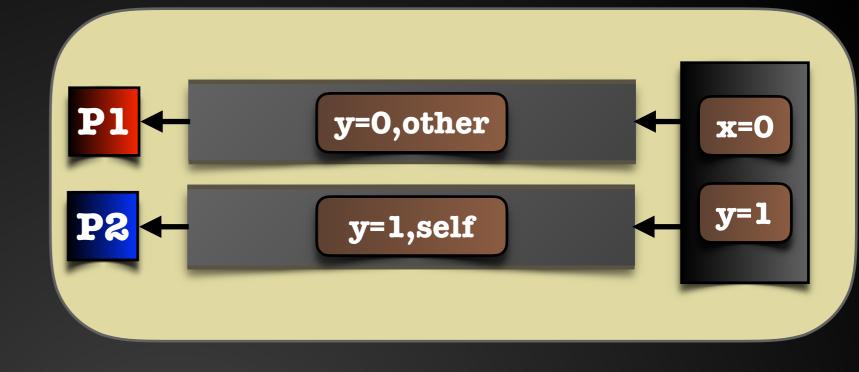
P2: w(y,1)

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



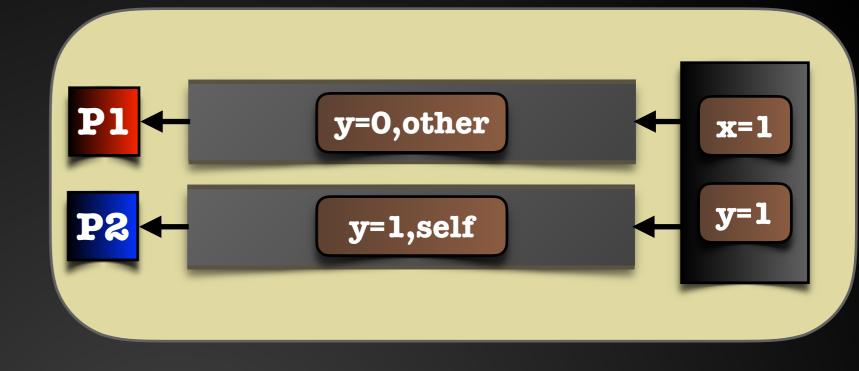
P2: w(y,1)

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



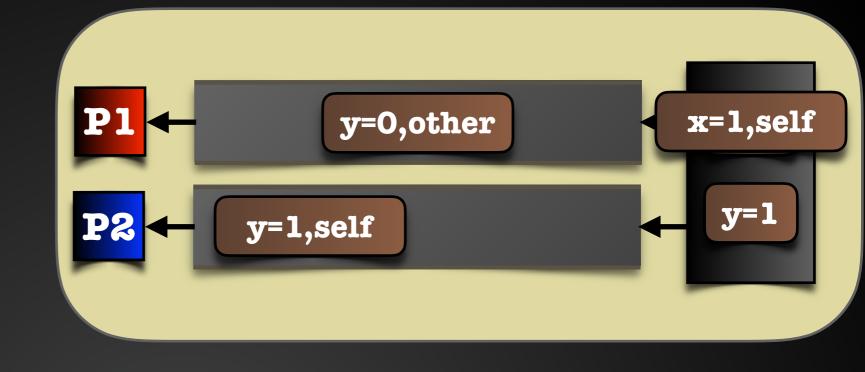
P2:
$$w(y,1) \rightarrow P2: w(x,1)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



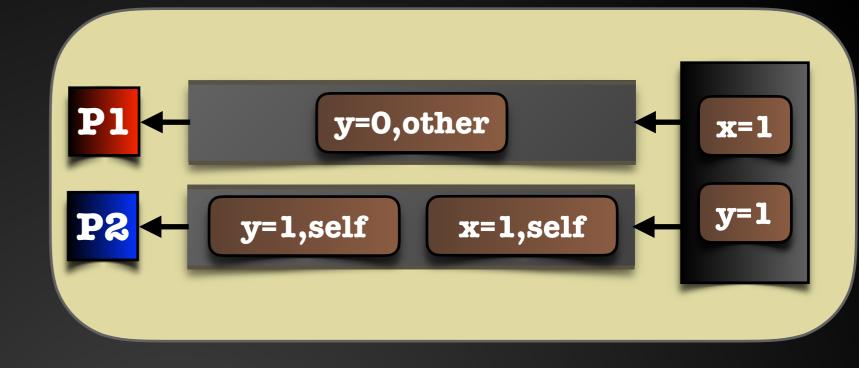
P2:
$$w(y,1) \rightarrow P2: w(x,1)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



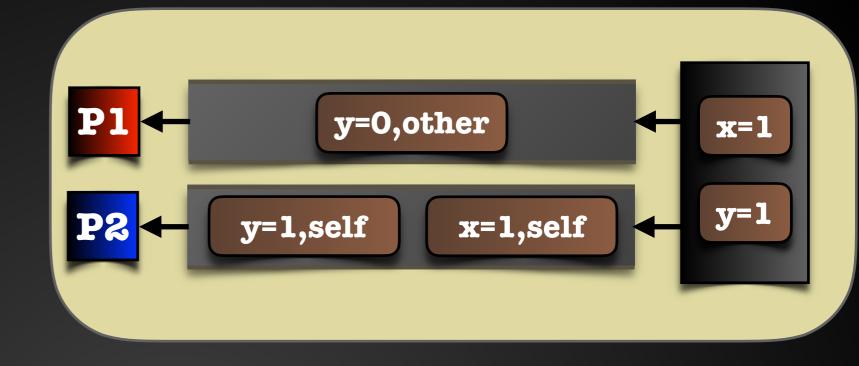
P2:
$$w(y,1) \rightarrow P2: w(x,1)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



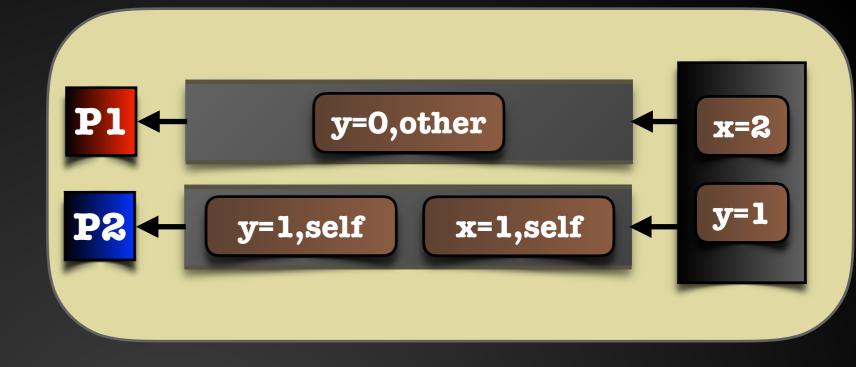
P2:
$$w(y,1) \rightarrow P2: w(x,1)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



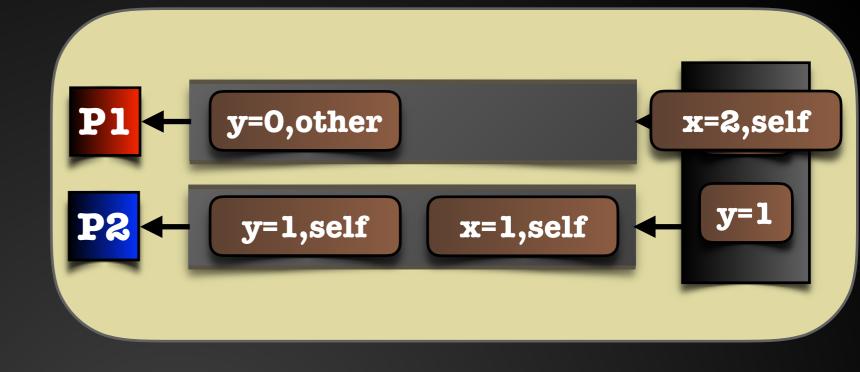
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



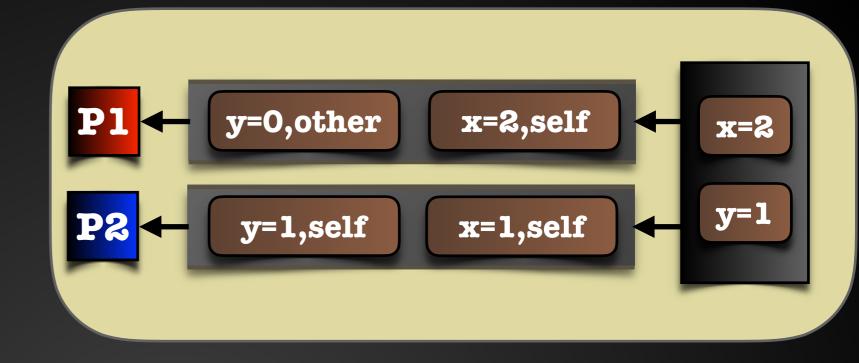
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



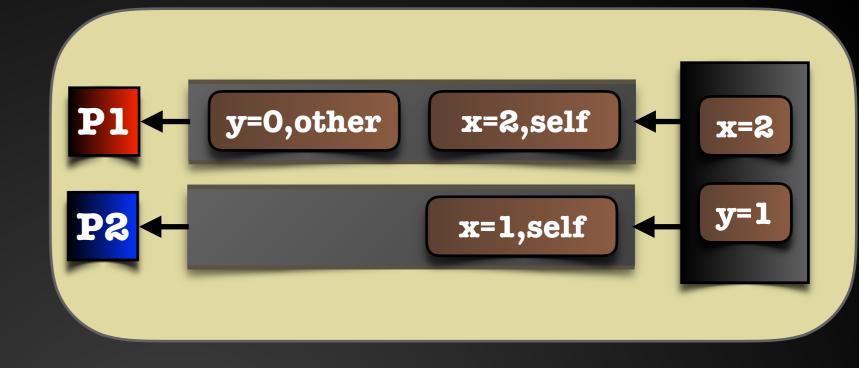
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



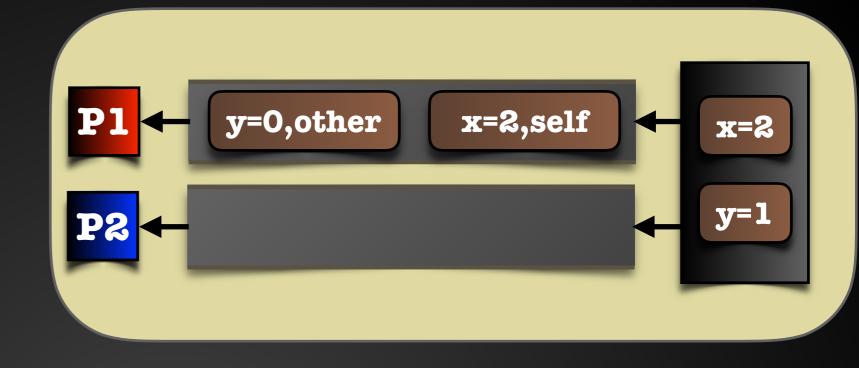
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



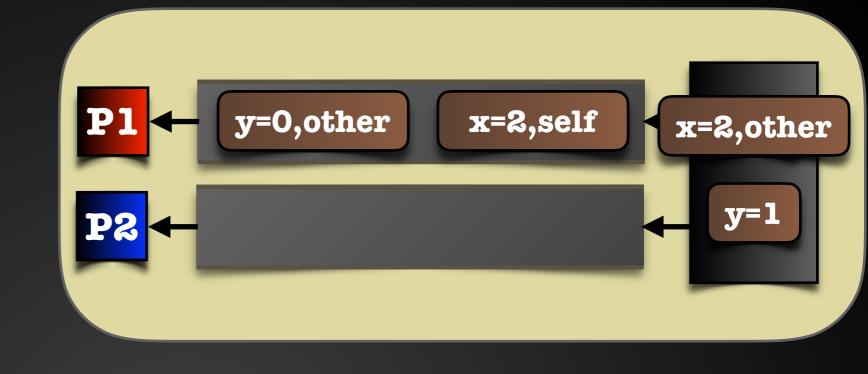
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



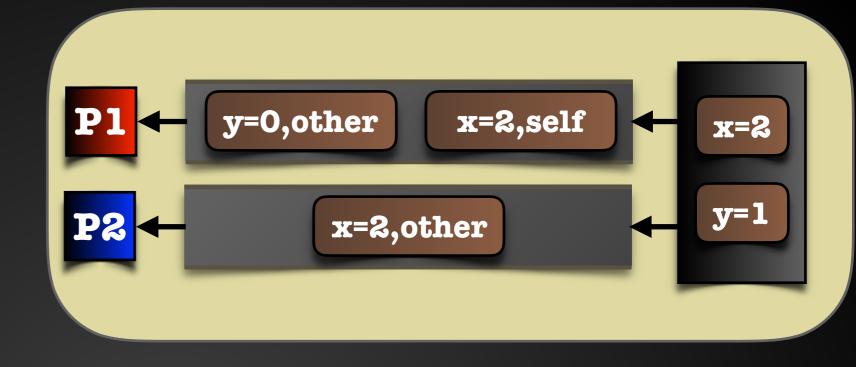
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



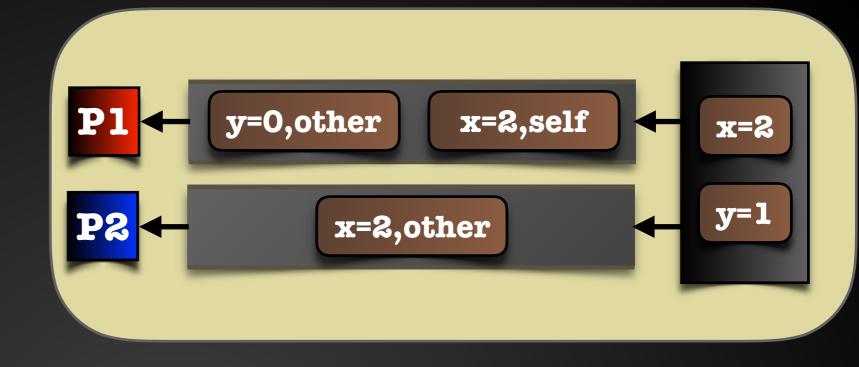
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$



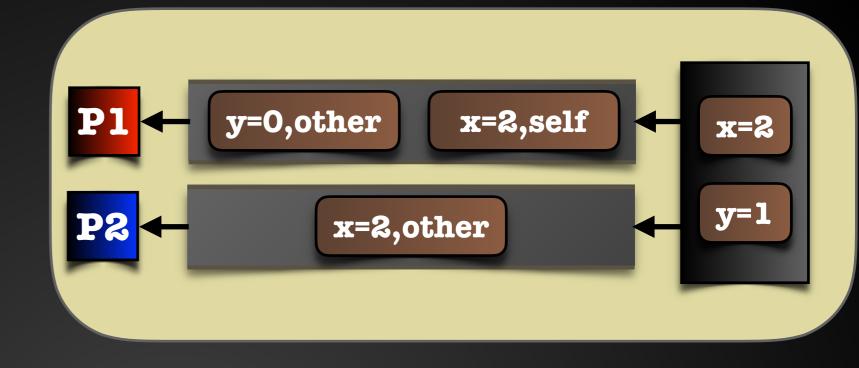
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



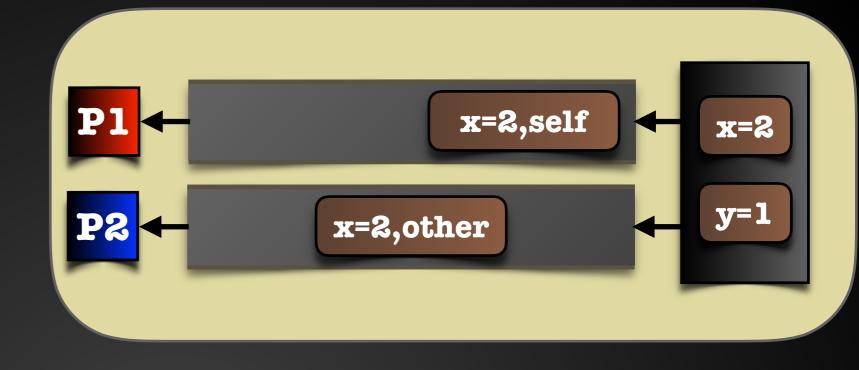
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2) \rightarrow P2: r(x,2)$$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$



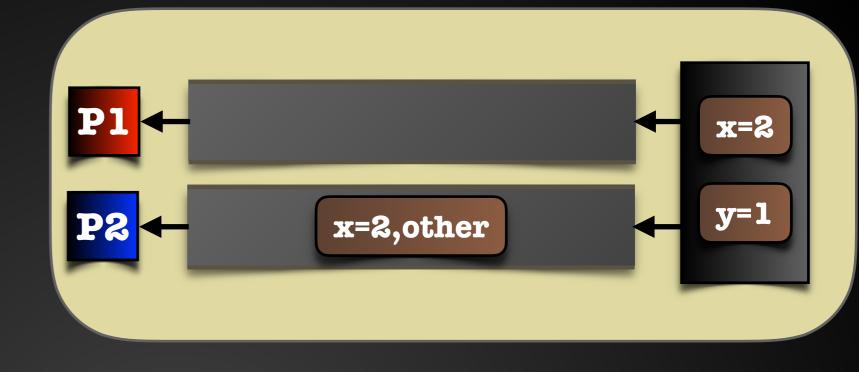
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2) \rightarrow P2: r(x,2) \rightarrow P1: r(y,0)$$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



P2:
$$w(y,1)$$
 \rightarrow P2: $w(x,1)$ \rightarrow P1: $w(x,2)$ \rightarrow P2: $r(x,2)$ \rightarrow P1: $r(y,0)$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$



P2:
$$w(y,1)$$
 \rightarrow P2: $w(x,1)$ \rightarrow P1: $w(x,2)$ \rightarrow P2: $r(x,2)$ \rightarrow P1: $r(y,0)$

$$P1: w(x,2) \rightarrow P1: r(y,0) \rightarrow P2: w(y,1) \rightarrow P2: w(x,1) \rightarrow P2: r(x,2)$$



P2:
$$w(y,1)$$
 \longrightarrow P2: $w(x,1)$ \longrightarrow P1: $w(x,2)$ \longrightarrow P2: $r(x,2)$ \longrightarrow P1: $r(y,0)$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$



P2:
$$w(y,1)$$
 \longrightarrow P2: $w(x,1)$ \longrightarrow P1: $w(x,2)$ \longrightarrow P2: $r(x,2)$ \longrightarrow P1: $r(y,0)$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$





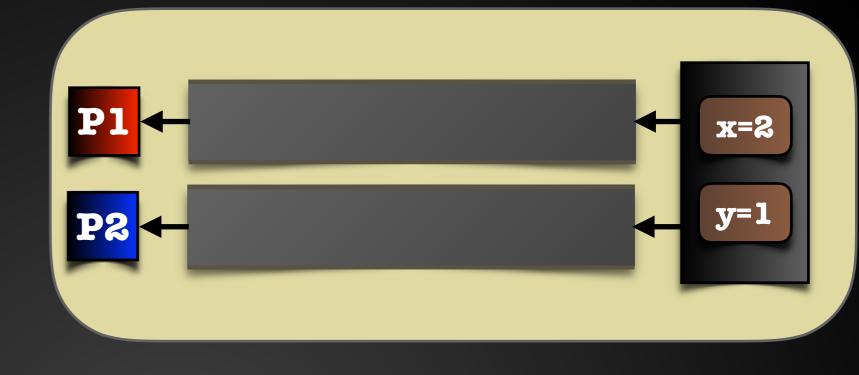
P2:
$$w(y,1) \rightarrow P2: w(x,1) \rightarrow P1: w(x,2) \rightarrow P2: r(x,2) \rightarrow P1: r(y,0)$$

P1:
$$w(x,2)$$
 \rightarrow P1: $r(y,0)$ \rightarrow P2: $w(y,1)$ \rightarrow P2: $w(x,1)$ \rightarrow P2: $r(x,2)$



P2:
$$w(y,1)$$
 \longrightarrow P2: $w(x,1)$ \longrightarrow P1: $w(x,2)$ \longrightarrow P2: $r(x,2)$ \longrightarrow P1: $r(y,0)$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$



P2:
$$w(y,1)$$
 \rightarrow P2: $w(x,1)$ \rightarrow P1: $w(x,2)$ \rightarrow P2: $r(x,2)$ \rightarrow P1: $r(y,0)$

$$P1: w(x,2) \longrightarrow P1: r(y,0) \longrightarrow P2: w(y,1) \longrightarrow P2: w(x,1) \longrightarrow P2: r(x,2)$$

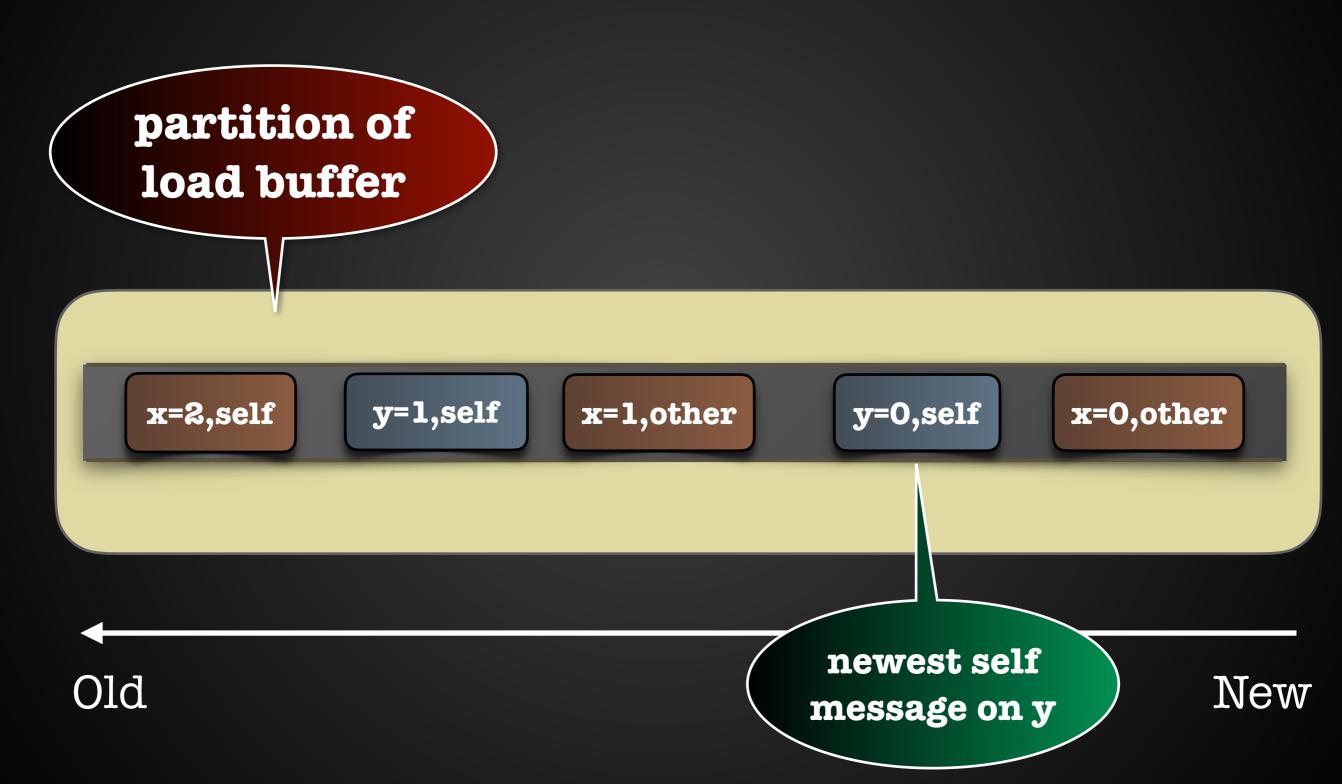
Outline

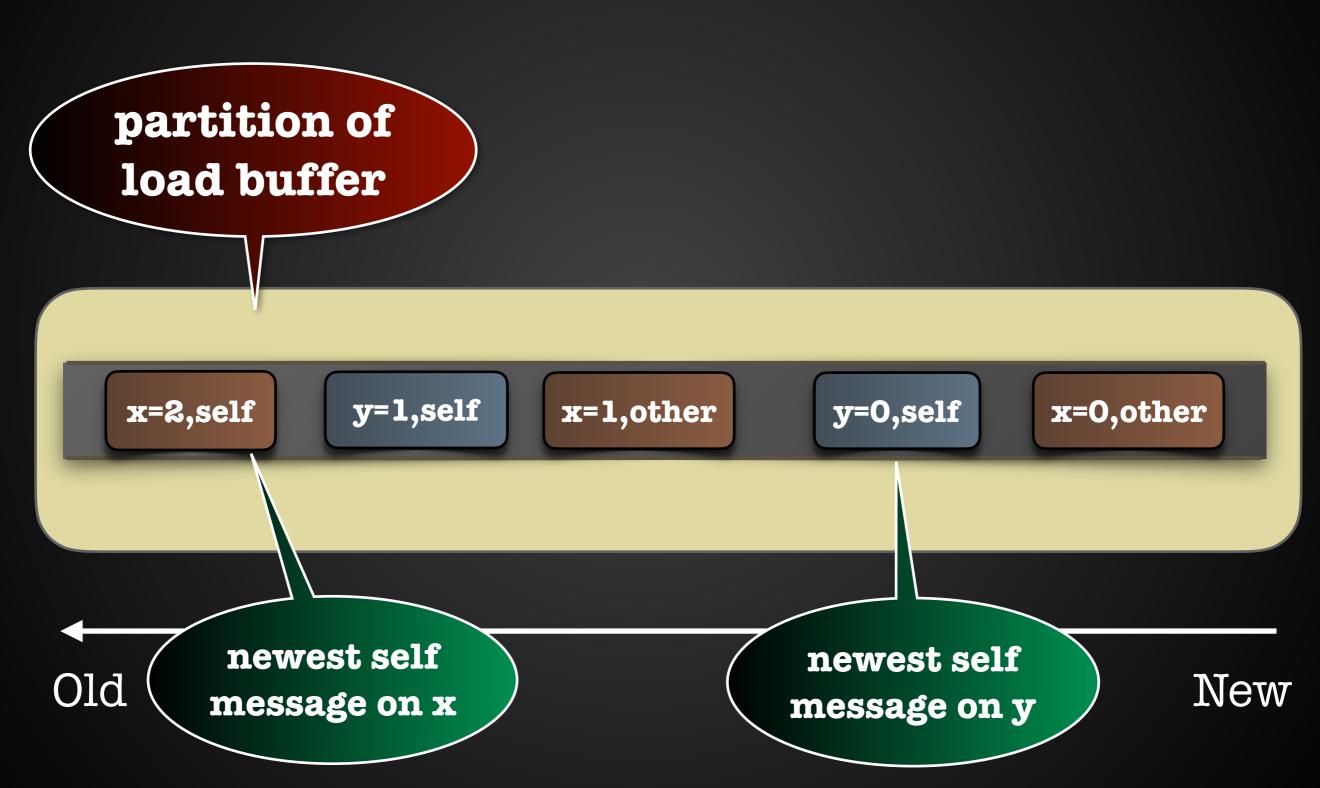
- Weak Consistency
- Total Store Order (TSO)
- Dual TSO
- Verification
- Specification
- Synthesis

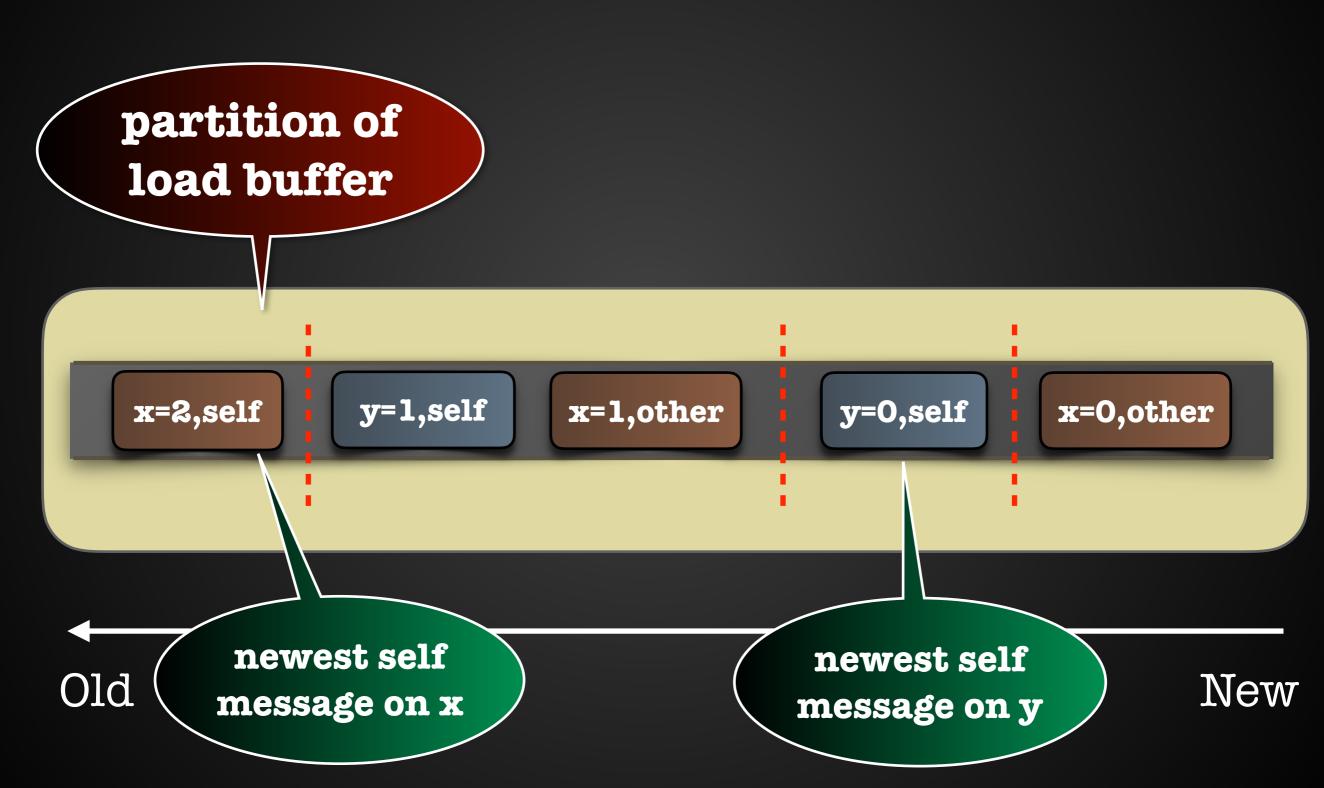


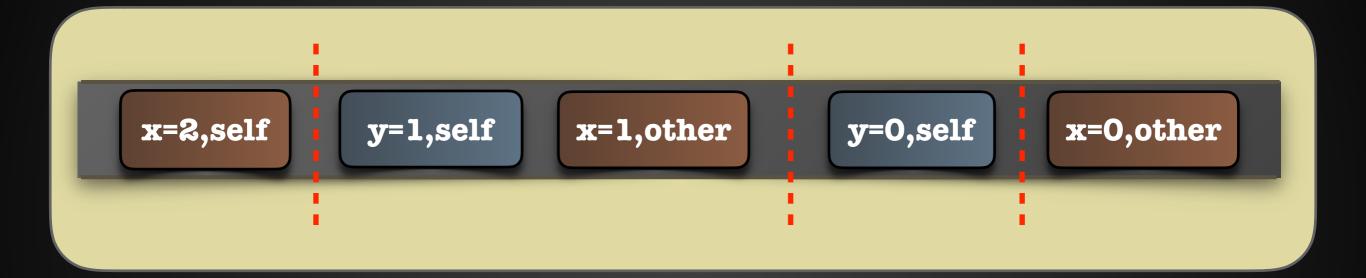
Old

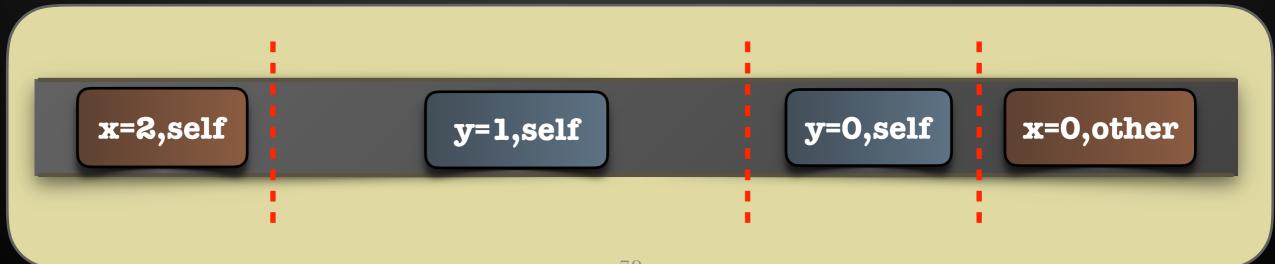
New

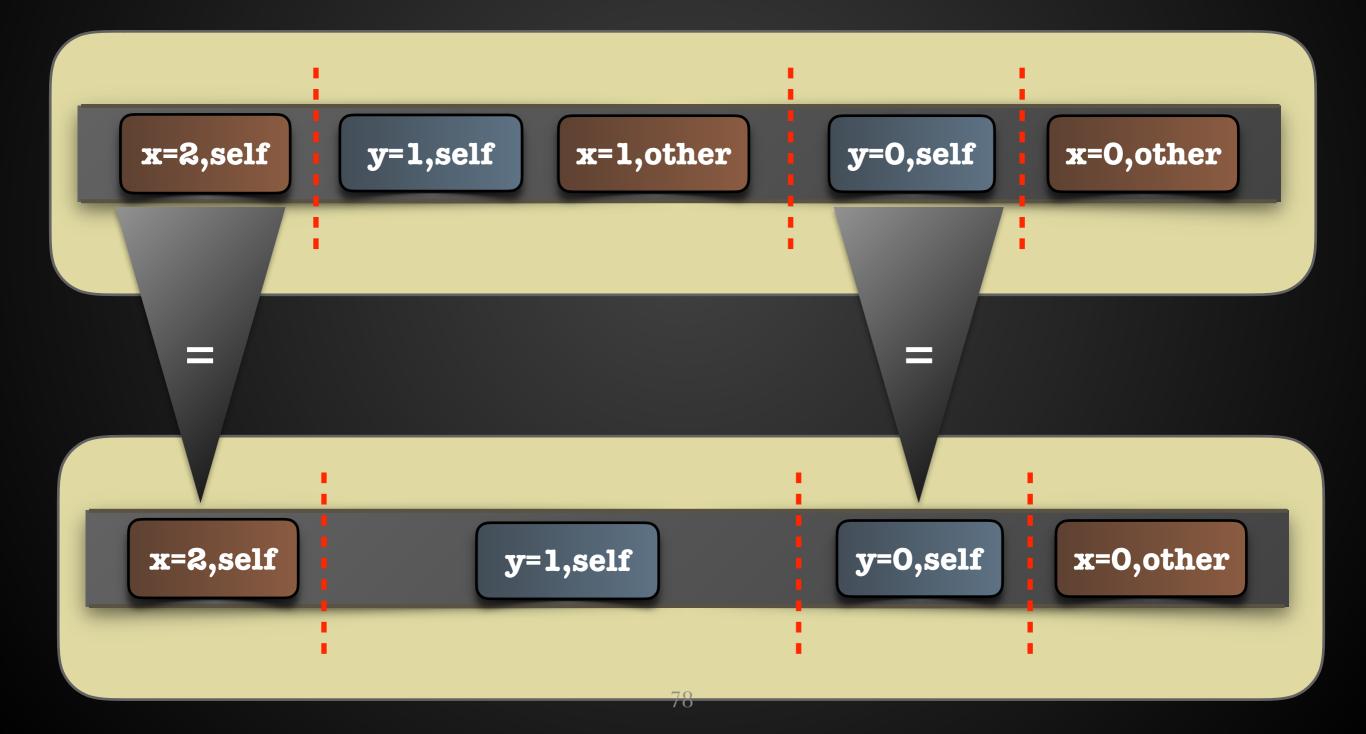


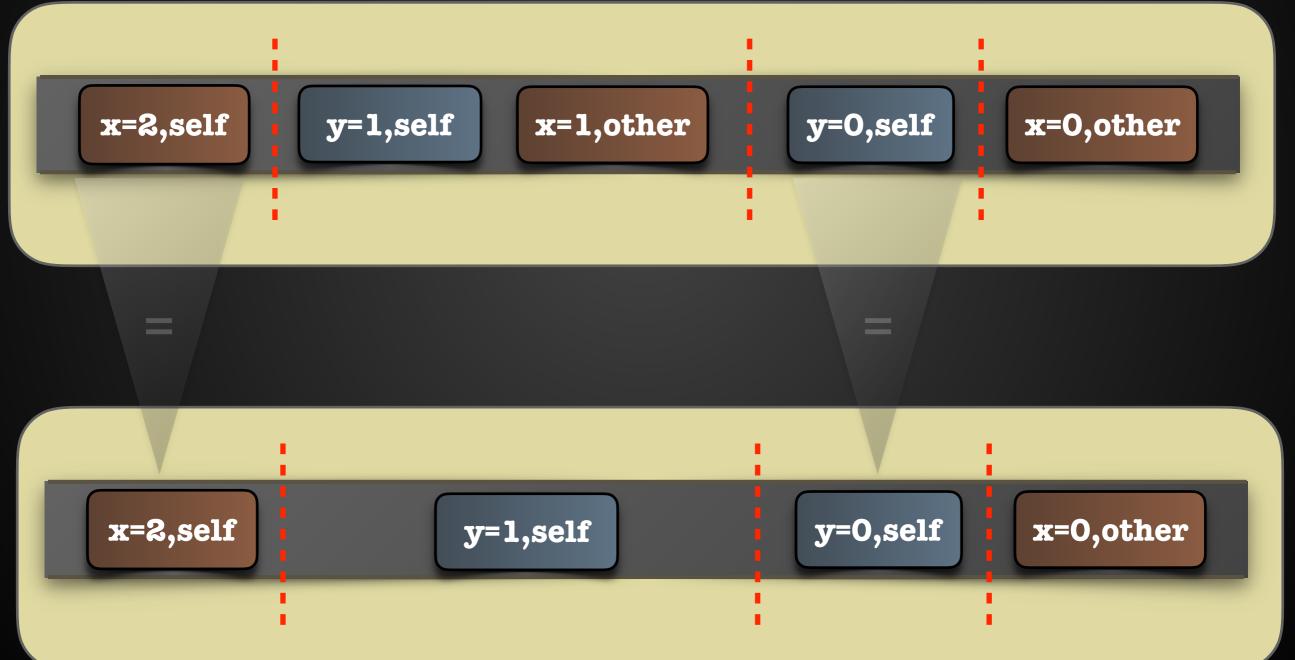


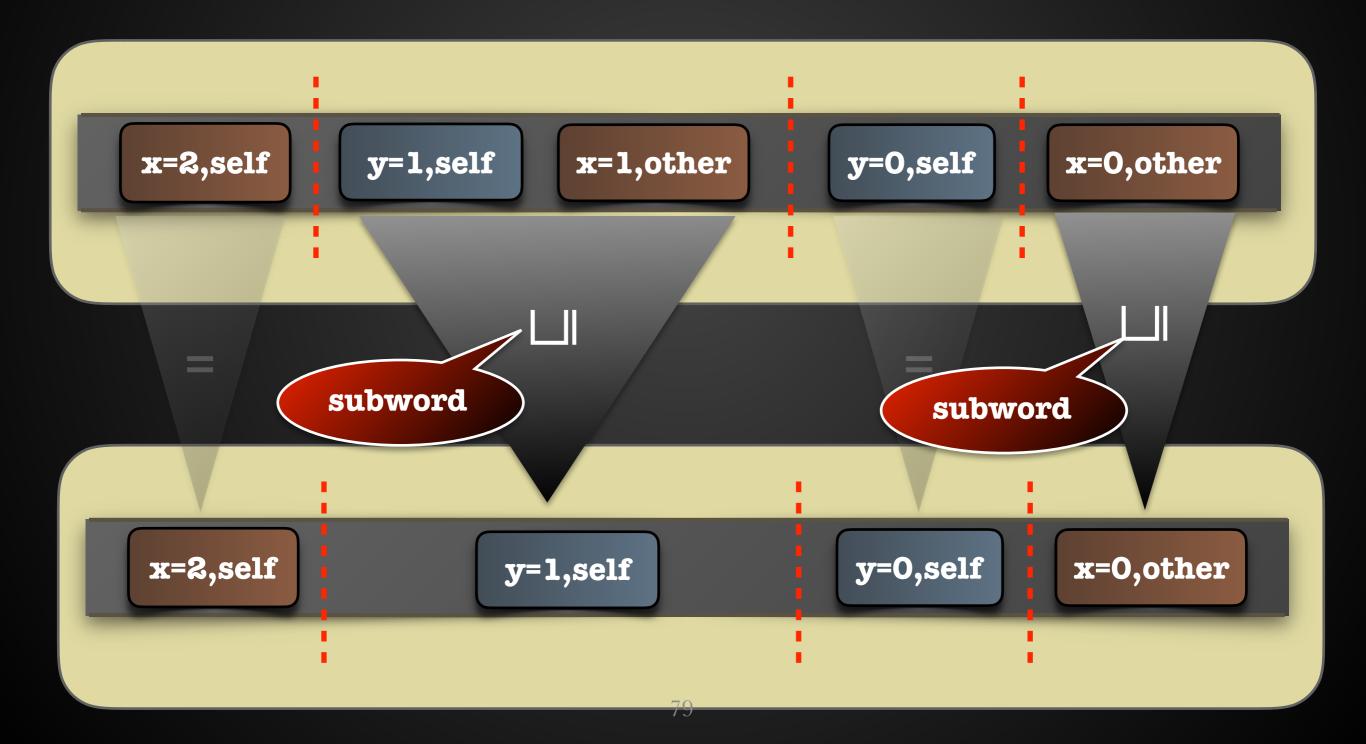




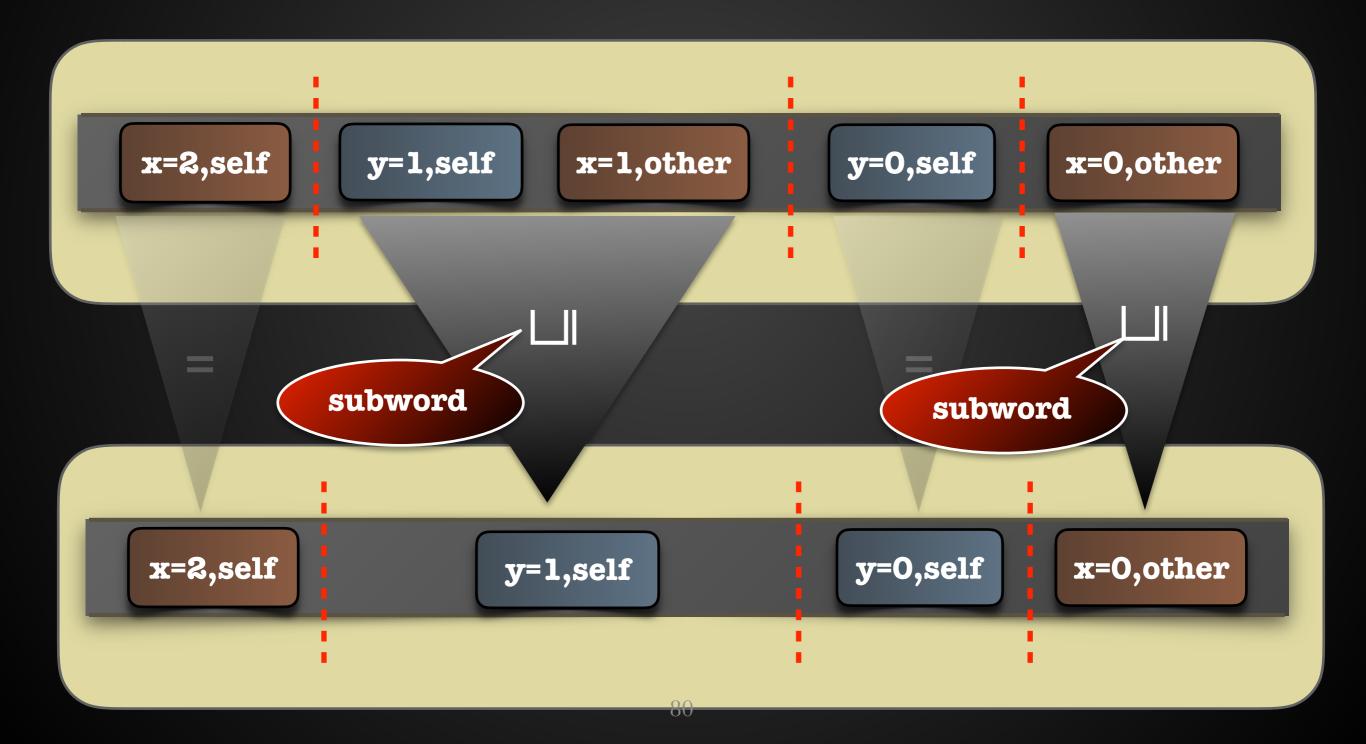






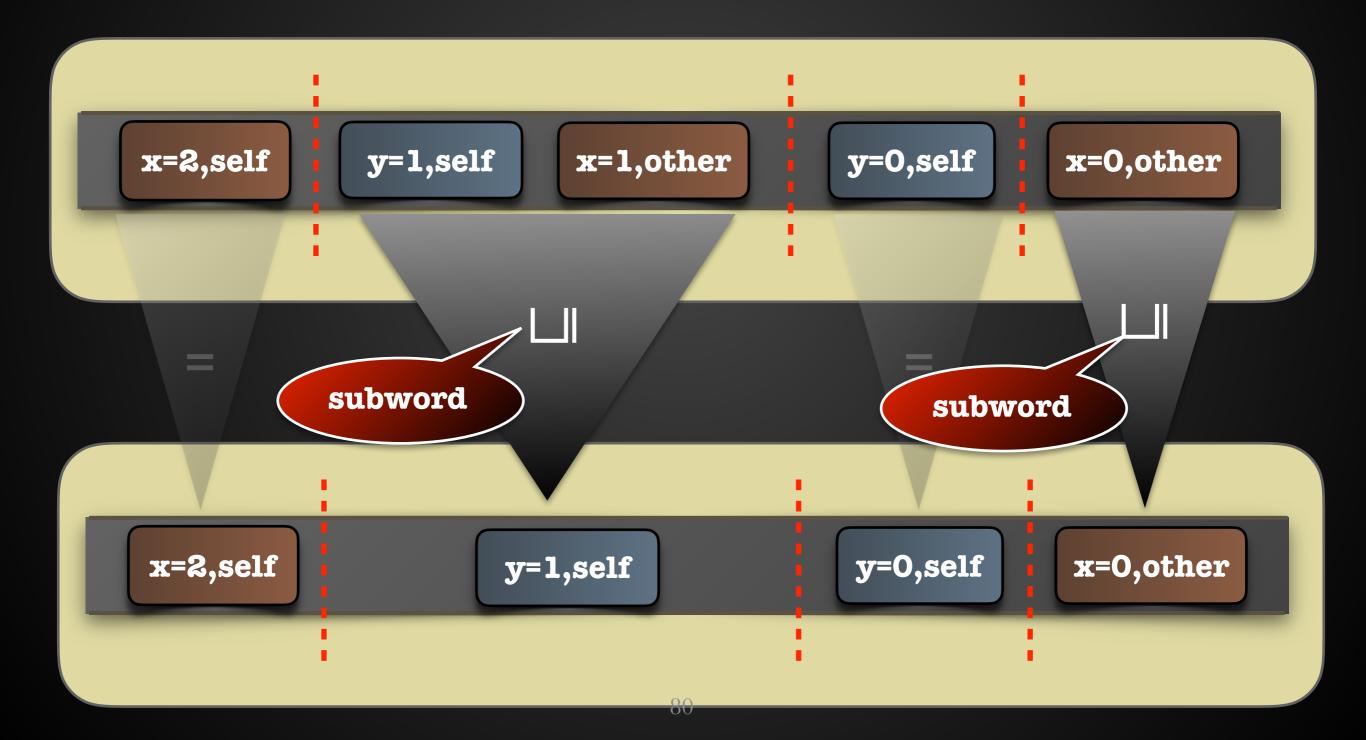


$ab \sqsubseteq xaybz$

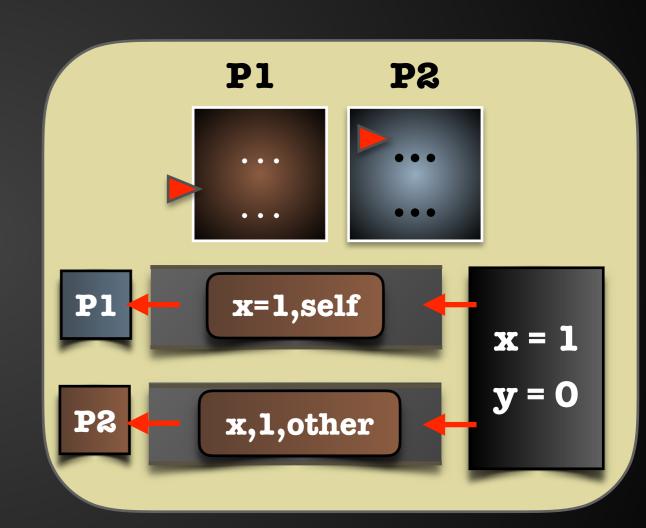




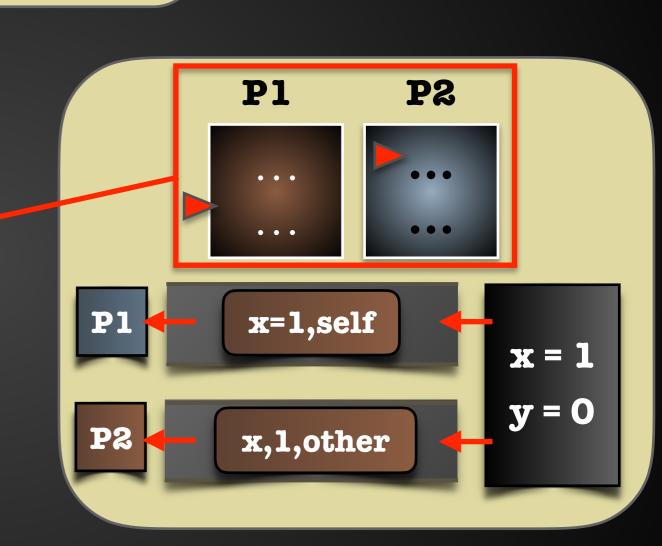
Ordering on Buffers



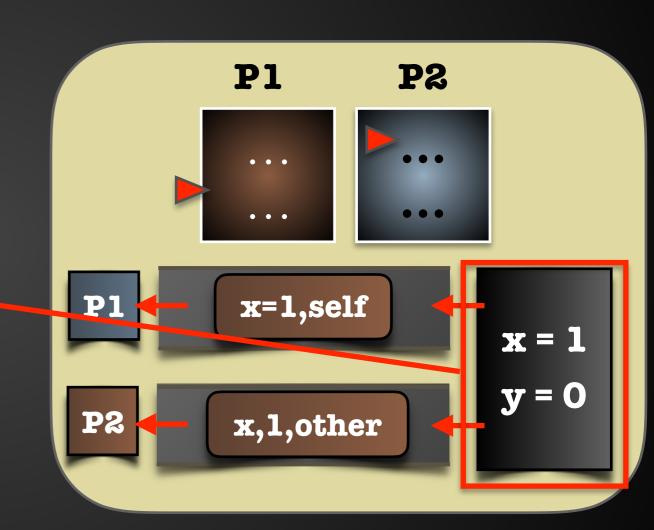
- identical process states
- identical memory state
- sub-word relation on buffers



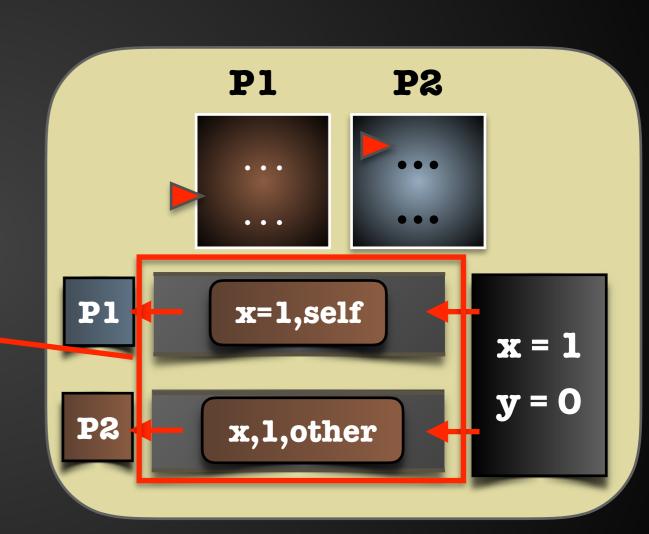
- identical process states
- identical memory state
- sub-word relation on buffers

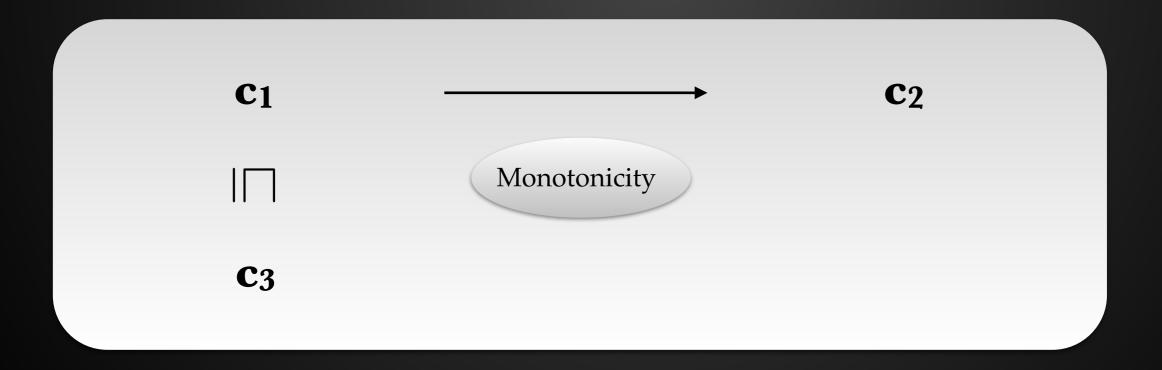


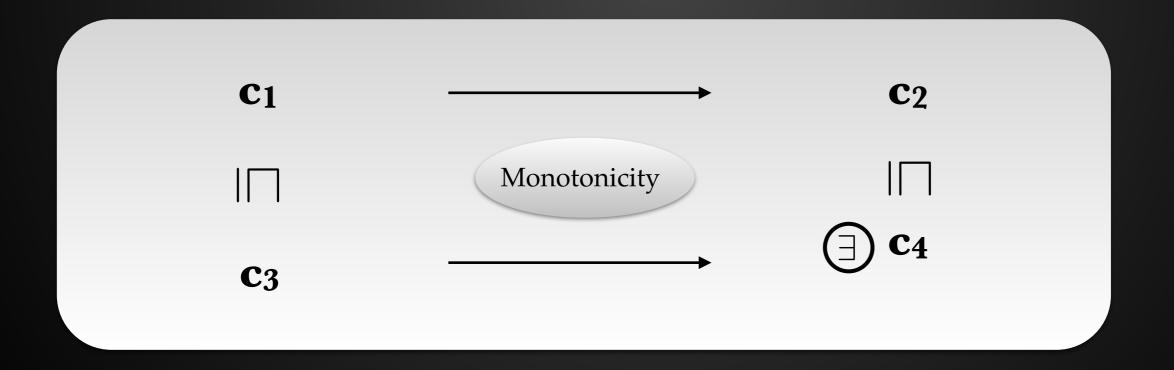
- identical process states
- identical memory state
- sub-word relation on buffers



- identical process states
- identical memory state
- sub-word relation on buffers







- finite-state programs running on TSO:
 - reachability analysis terminates
 - reachability decidable

Experimental Results

Tool:

Memorax

https://github.com/memorax/memorax

Experimental Results

time (secs)

generated configurations

Tool: Memorax

standard
benchmarks:
litmus tests and mutual
exclusion

Safe under #PProgram SC**TSO** #T#C $\overline{\mathrm{SB}}$ 10641 0.3 yes no LB 2048 0.0yes yes WRC 0.0 1507 yes yes ISA2 0.0 509 yes yes **RWC** 4277 0.1yes no W+RWC 0.0 1713 yes no **IRIW** 520 0.0yes yes MP 0.0883 yes yes Simple Dekker yes 0.098 no Dekker 5053 0.1yes no 0.15442 Peterson yes no 7632 Repeated Peterson 0.2ves no 82050 2.6 Bakery yes no Dijkstra 8324 0.2yes no Szymanski 29018 0.6 ves no Ticket Spin Lock 18963 0.9yes yes 17.7 Lamport's Fast Mutex 292543 yes no 2762578 Burns 124.3 yes no NBW-W-WR 222 0.0 yes yes 1704 Sense Reversing Barrier 0.1yes yes

Experimental Results

Tool:
Memorax

parameterized verification

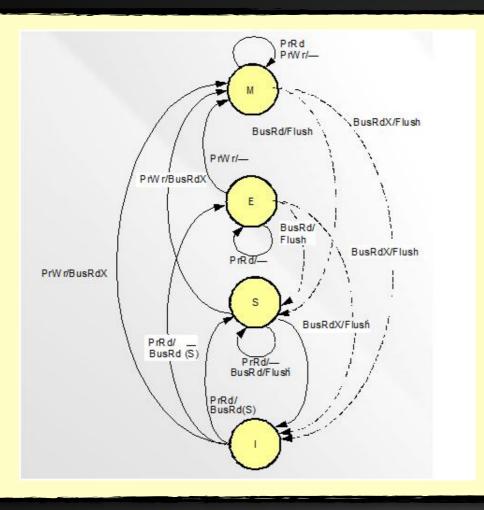
generated configurations

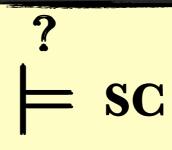
Program	#T	#C
SB	0.0	147
LB	0.6	1028
MP	0.0	149
WRC	0.8	618
ISA2	4.3	1539
RWC	0.2	293
W+RWC	1.5	828
IRIW	4.6	648

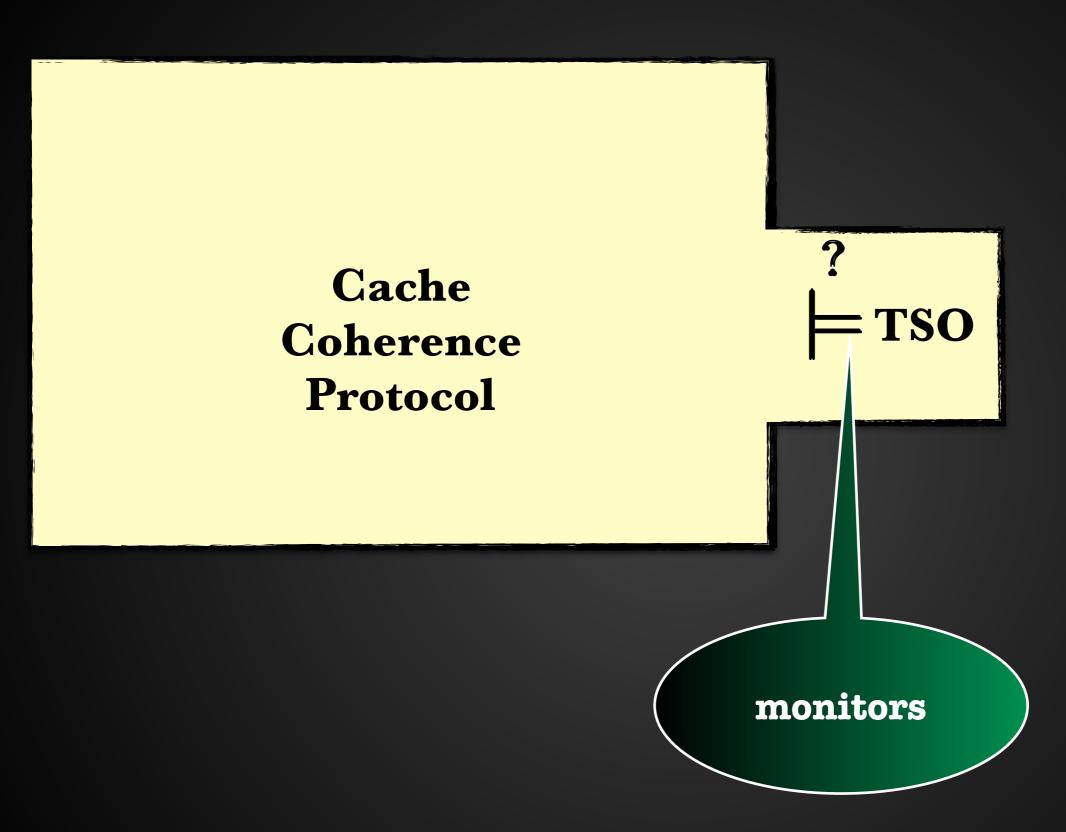
Outline

- Weak Consistency
- Total Store Order (TSO)
- Dual TSO
- Verification
- Specification
- Synthesis

Cache Coherence Protocol







TSO-CC: Consistency directed cache coherence for TSO

Marco Elver University of Edinburgh marco.elver@ed.ac.uk Vijay Nagarajan University of Edinburgh vijay.nagarajan@ed.ac.uk

Racer: TSO Consistency via Race Detection

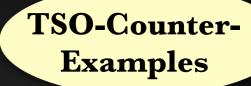
Alberto Ros
Department of Computer Engineering
Universidad de Murcia, Spain
aros@ditec.um.es

Stefanos Kaxiras

Department of Information Technology
Uppsala Universitet, Sweden
stefanos.kaxiras@it.uu.se

TSO

monitors



P1: w(x,1)

 $P1: w(x,1) \longrightarrow P2: r(x,1)$

$$P1: w(x,1) \longrightarrow P2: r(x,1) \longrightarrow P3: w(x,2)$$

P1:
$$w(x,1) \rightarrow P2: r(x,1) \rightarrow P3: w(x,2) \rightarrow P4: r(x,2)$$

$$P1: w(x,1) \rightarrow P2: r(x,1) \rightarrow P3: w(x,2) \rightarrow P4: r(x,2) \rightarrow P5: r(x,1)$$

$$P1: w(x,1) \rightarrow P2: r(x,1) \rightarrow P3: w(x,2) \rightarrow P4: r(x,2) \rightarrow P5: r(x,1)$$

P1:
$$w(x,1) \rightarrow P2: r(x,1) \rightarrow P3: w(x,2) \rightarrow P3: w(y,1) \rightarrow P4: r(y,1)$$
P5: $r(x,1)$

$$P1: w(x,1) \rightarrow P2: r(x,1) \rightarrow P3: w(x,2) \rightarrow P4: r(x,2) \rightarrow P5: r(x,1)$$

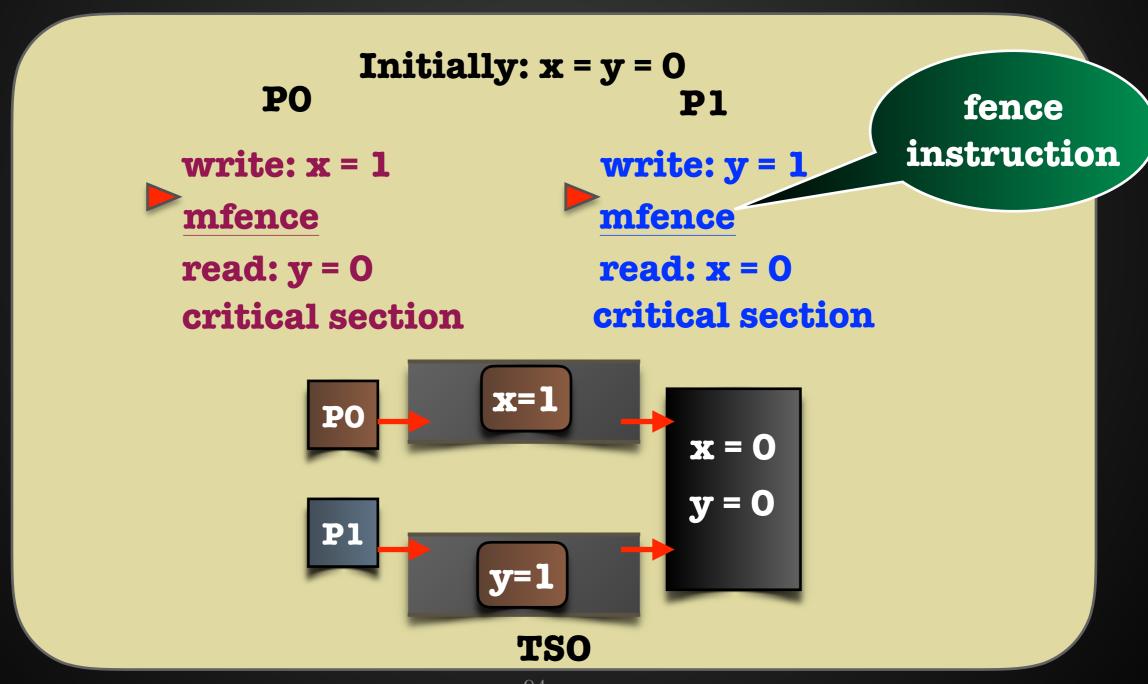
$$P1: w(x,1) \rightarrow P2: r(x,1) \rightarrow P3: w(x,2) \rightarrow P3: w(y,1) \rightarrow P4: r(y,1)$$

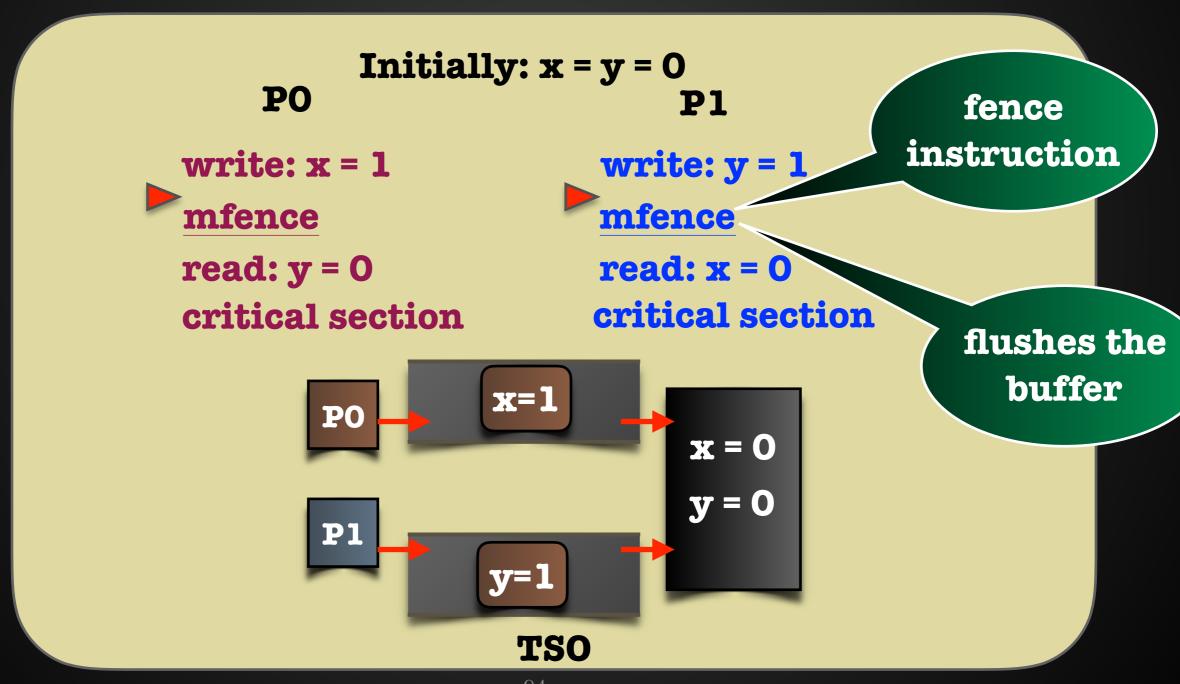
$$P5: r(x,1)$$

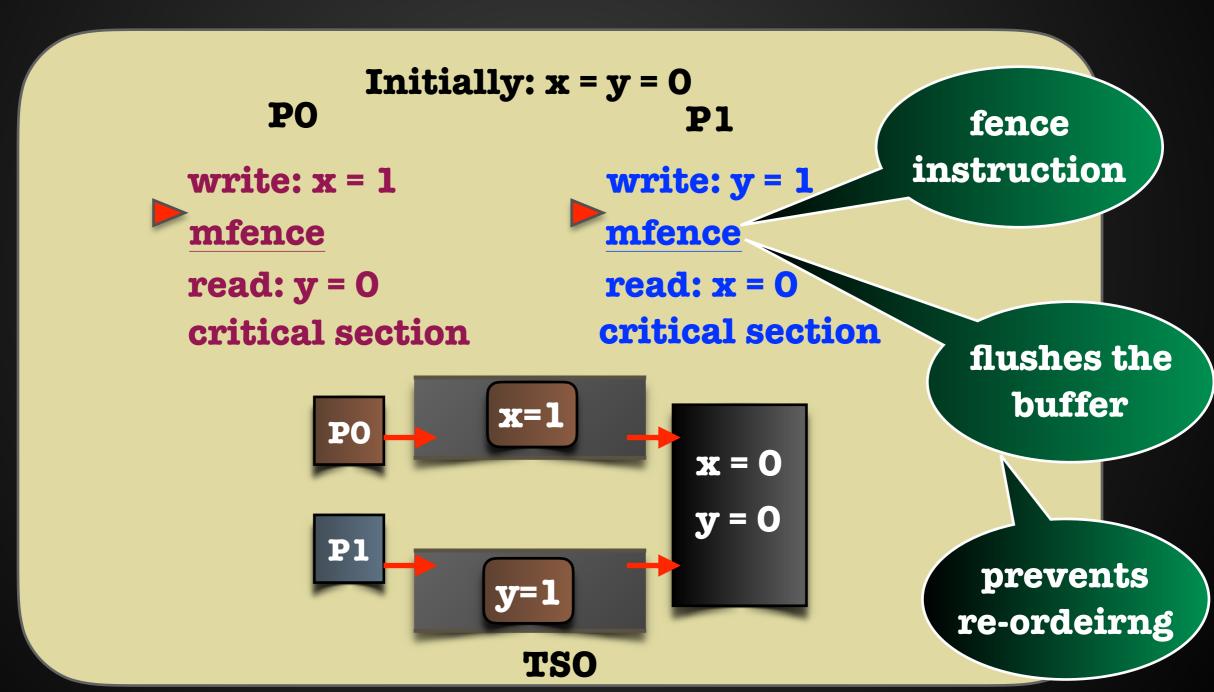
 $TSO \equiv 12 counter-examples$

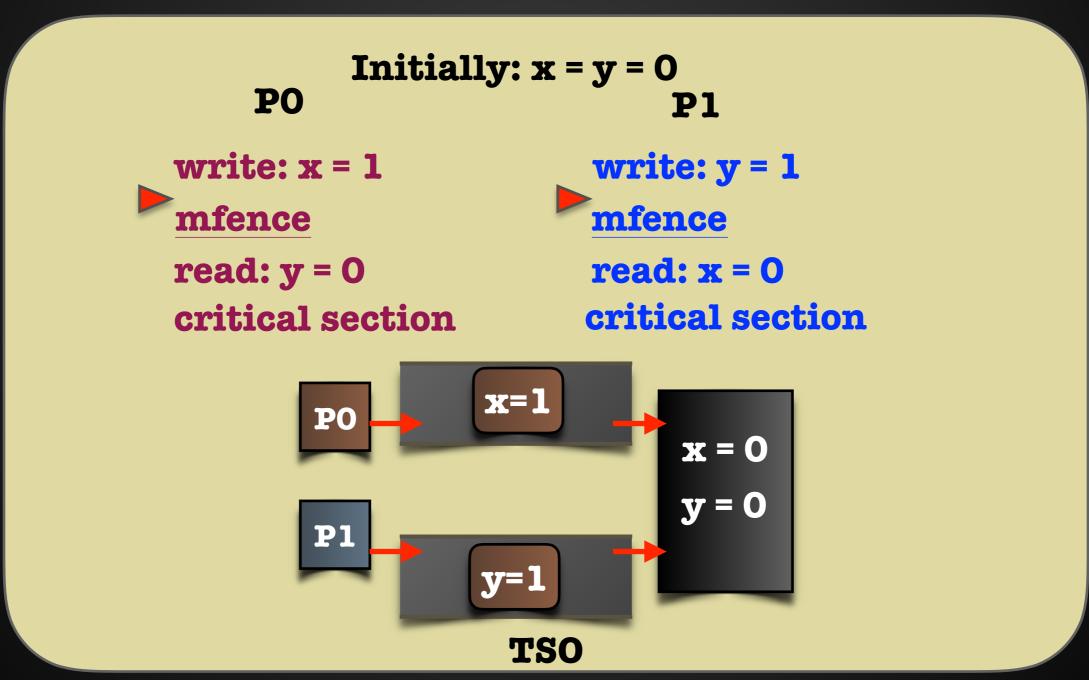
Outline

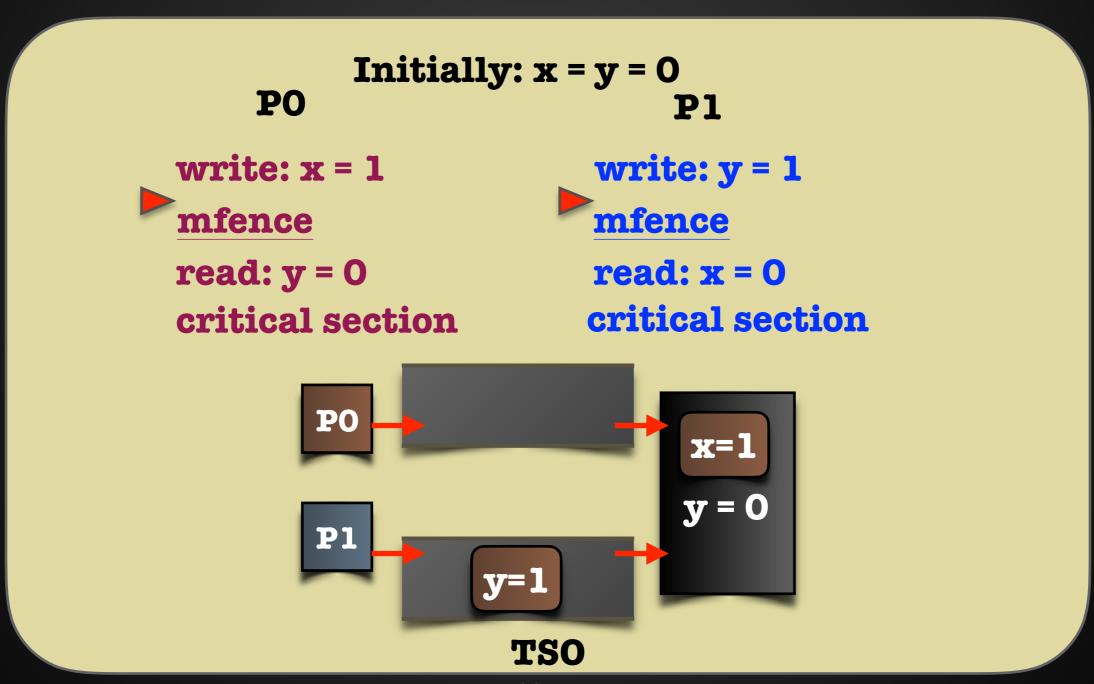
- Weak Consistency
- Total Store Order (TSO)
- Dual TSO
- Verification
- Specification
- Synthesis

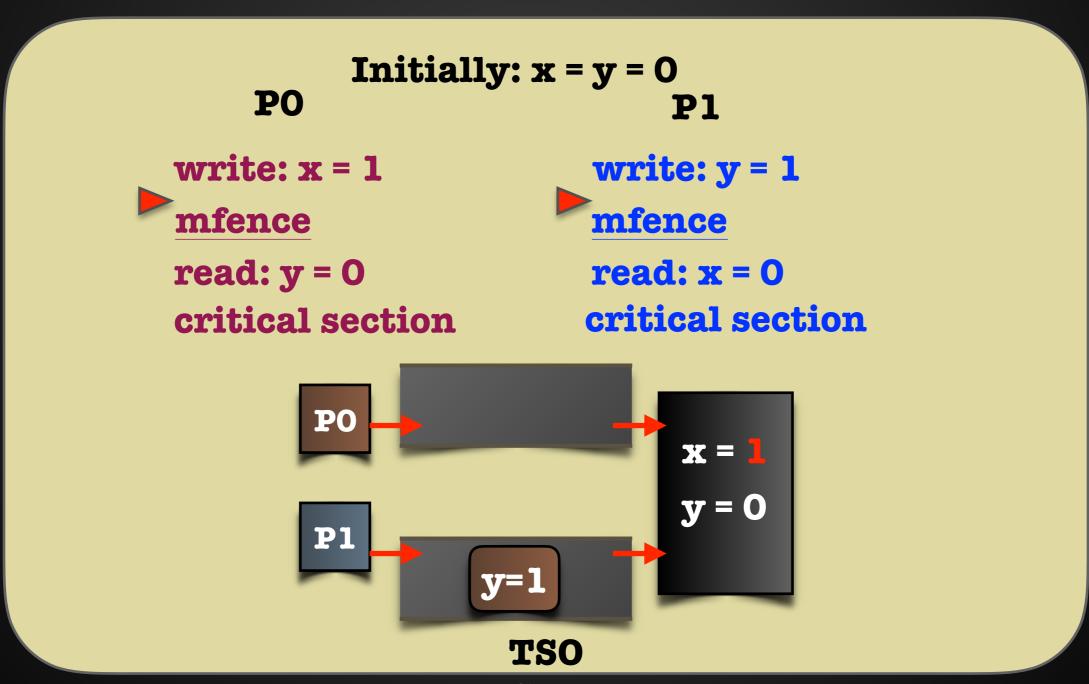


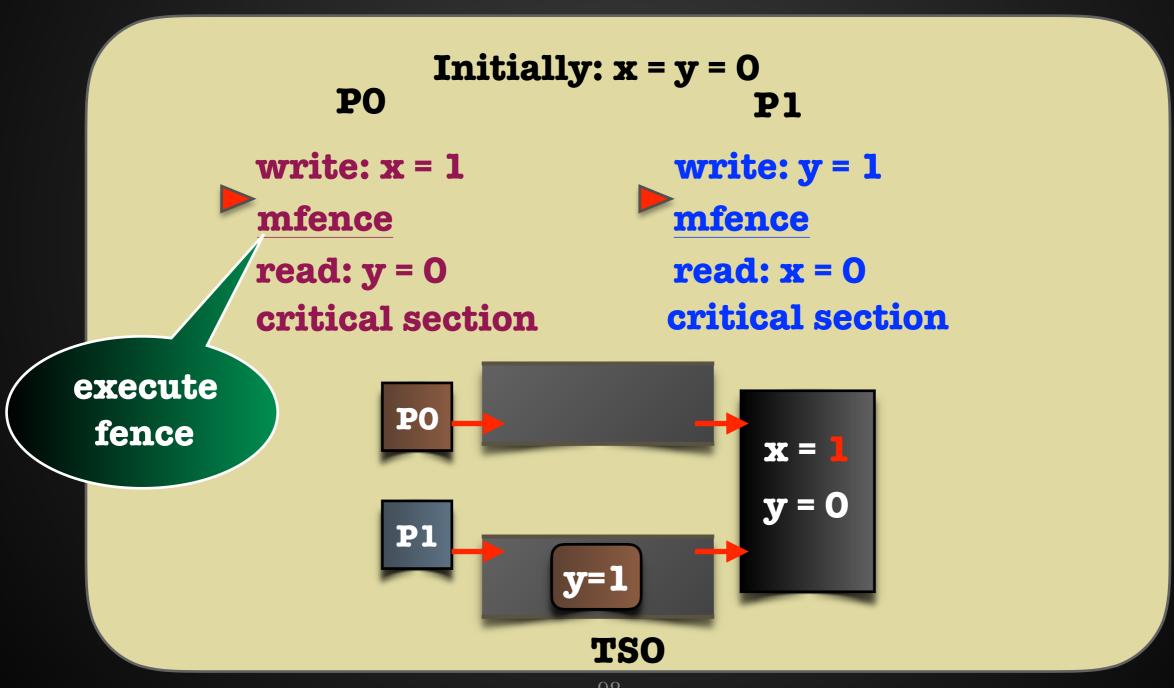


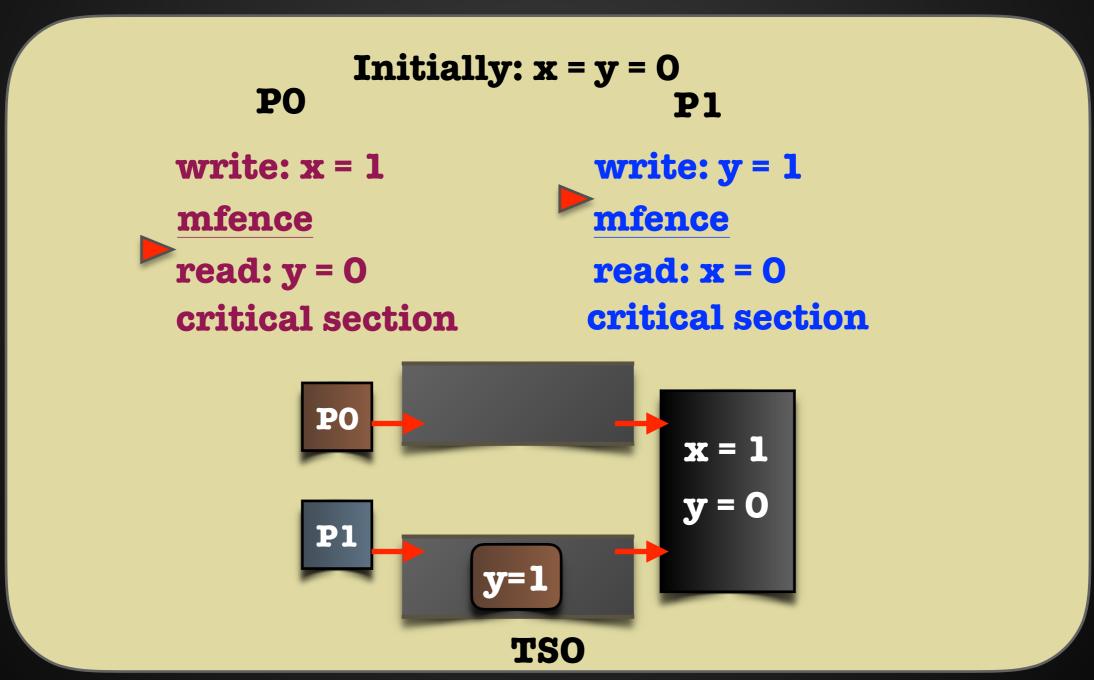


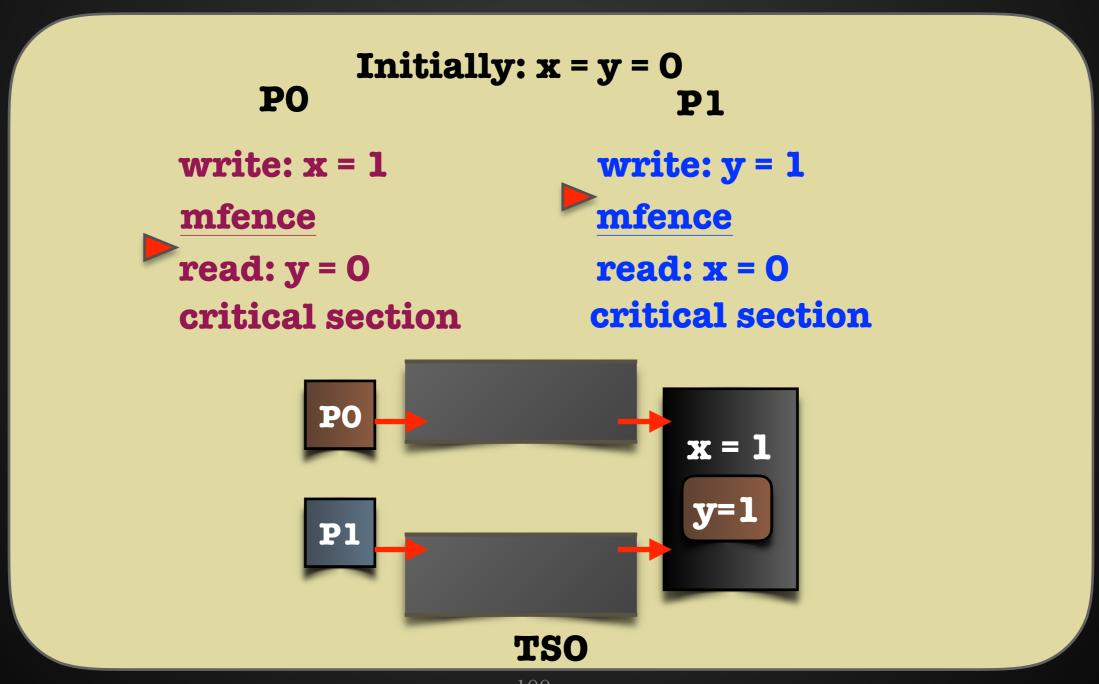


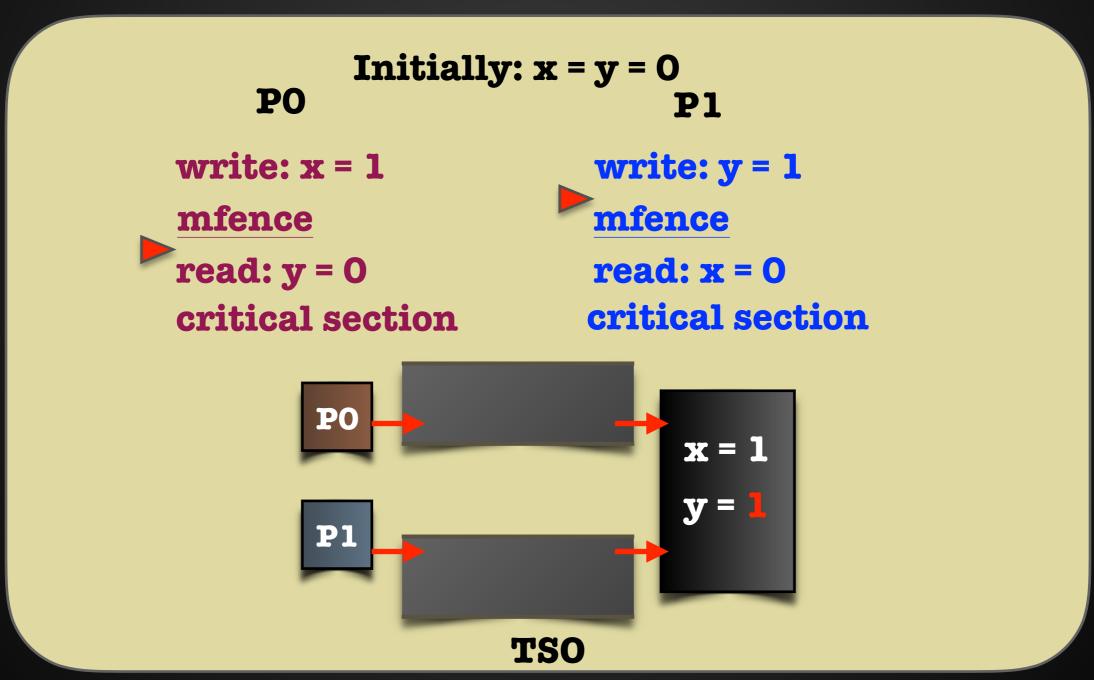


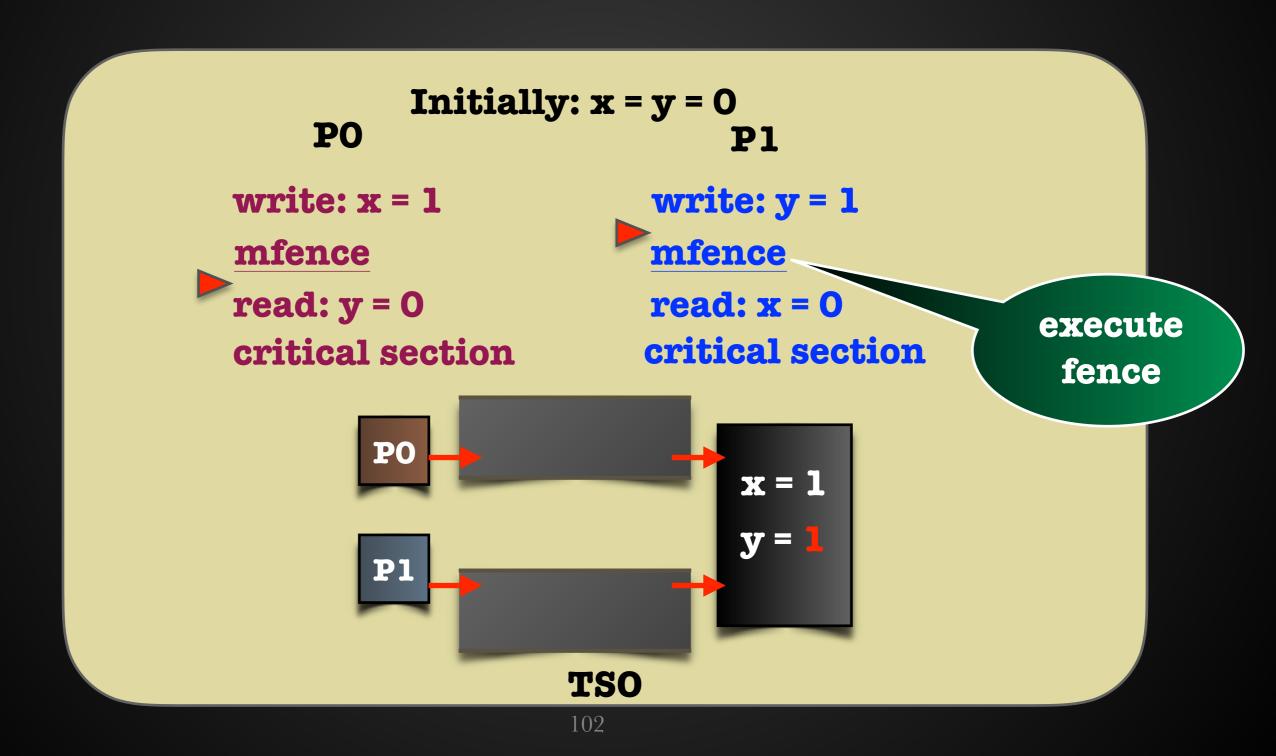


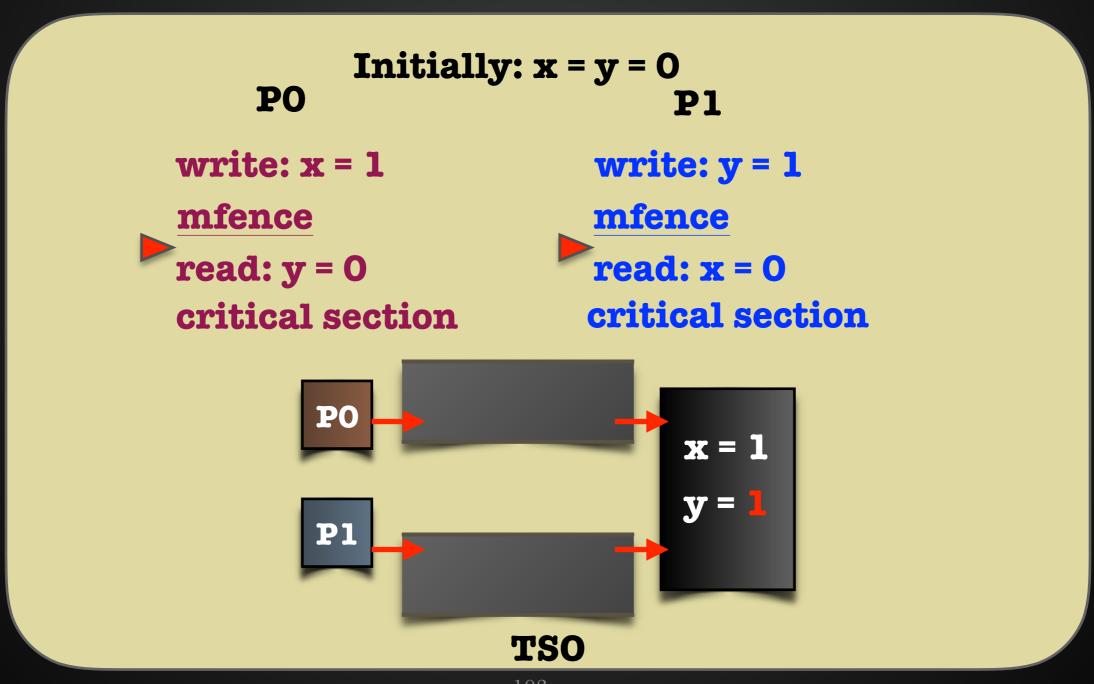


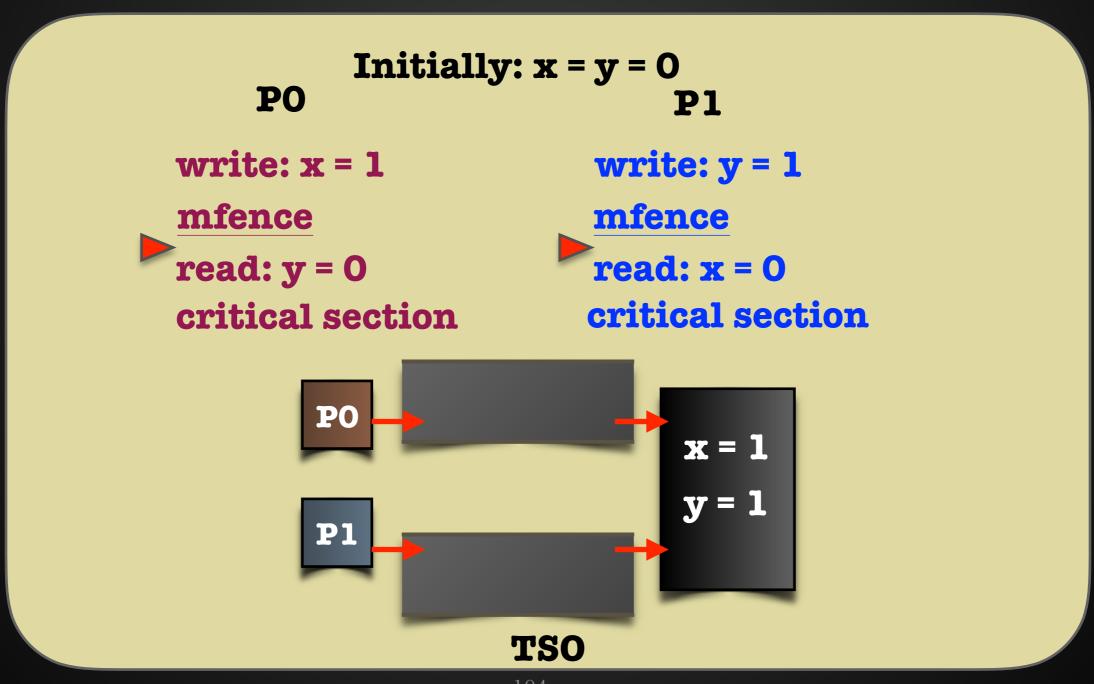


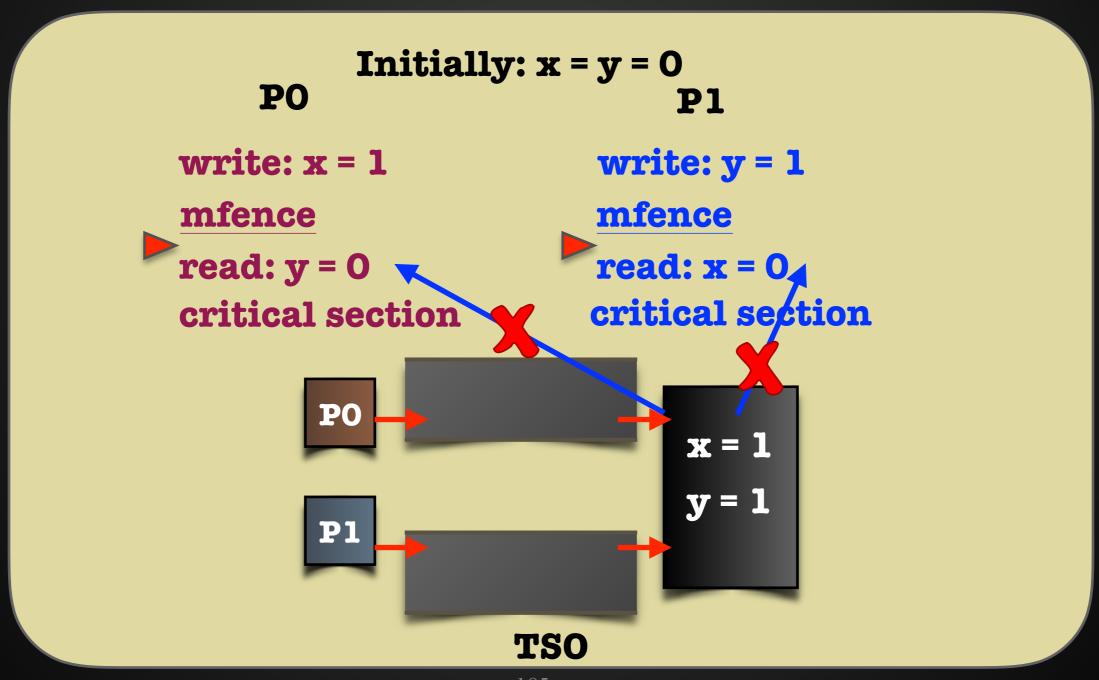


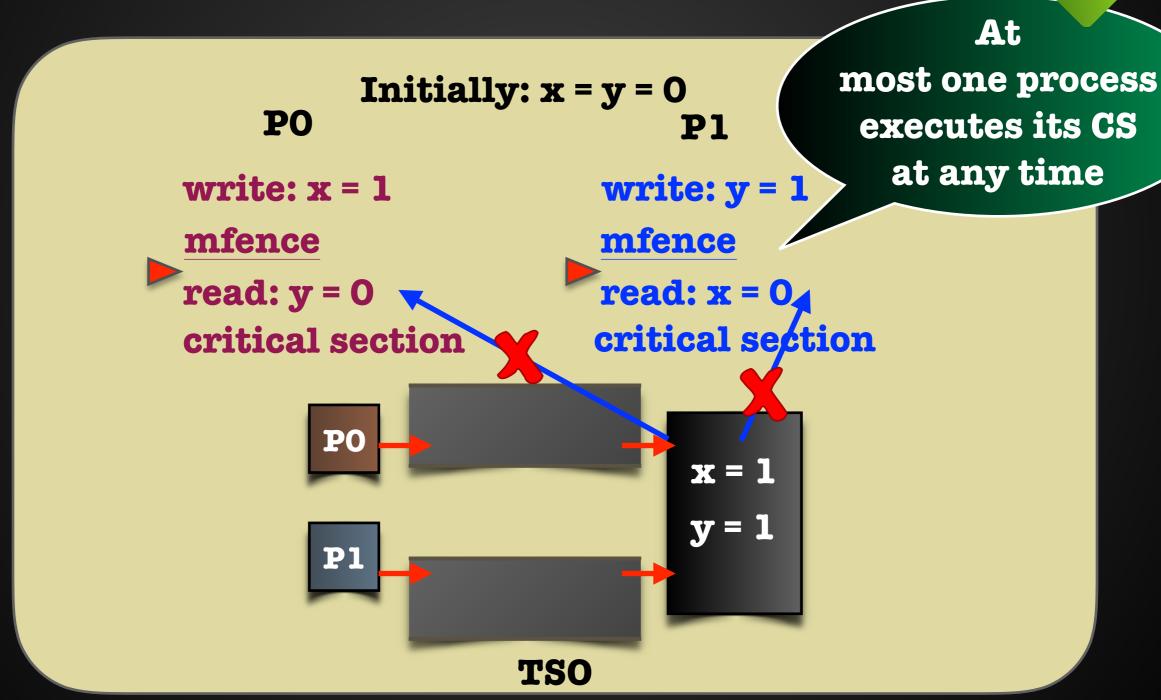


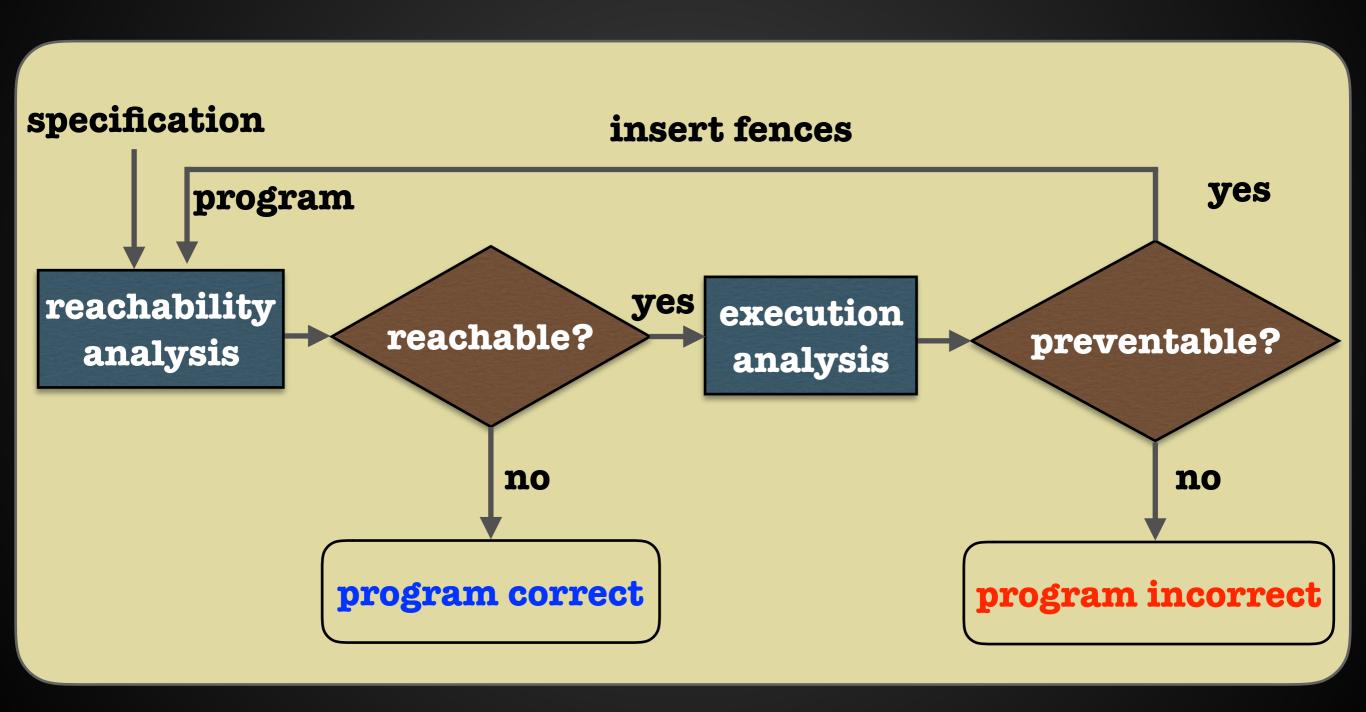


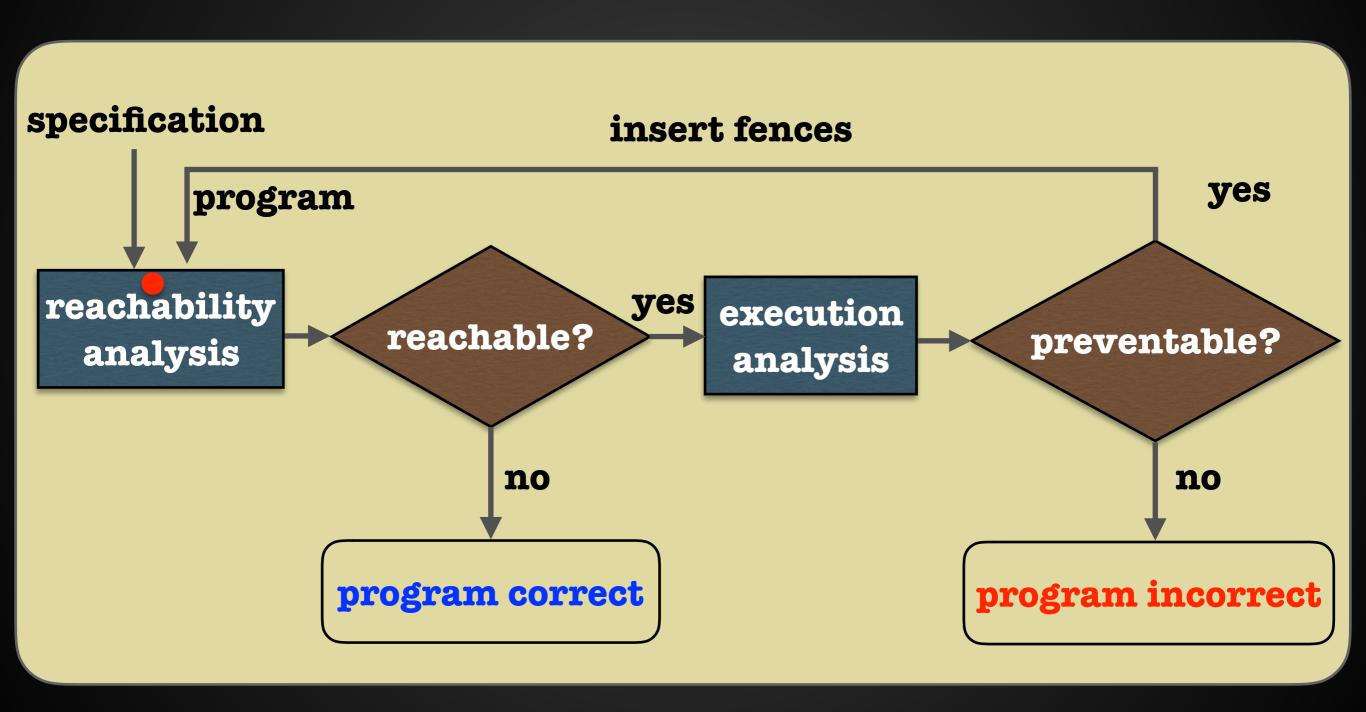


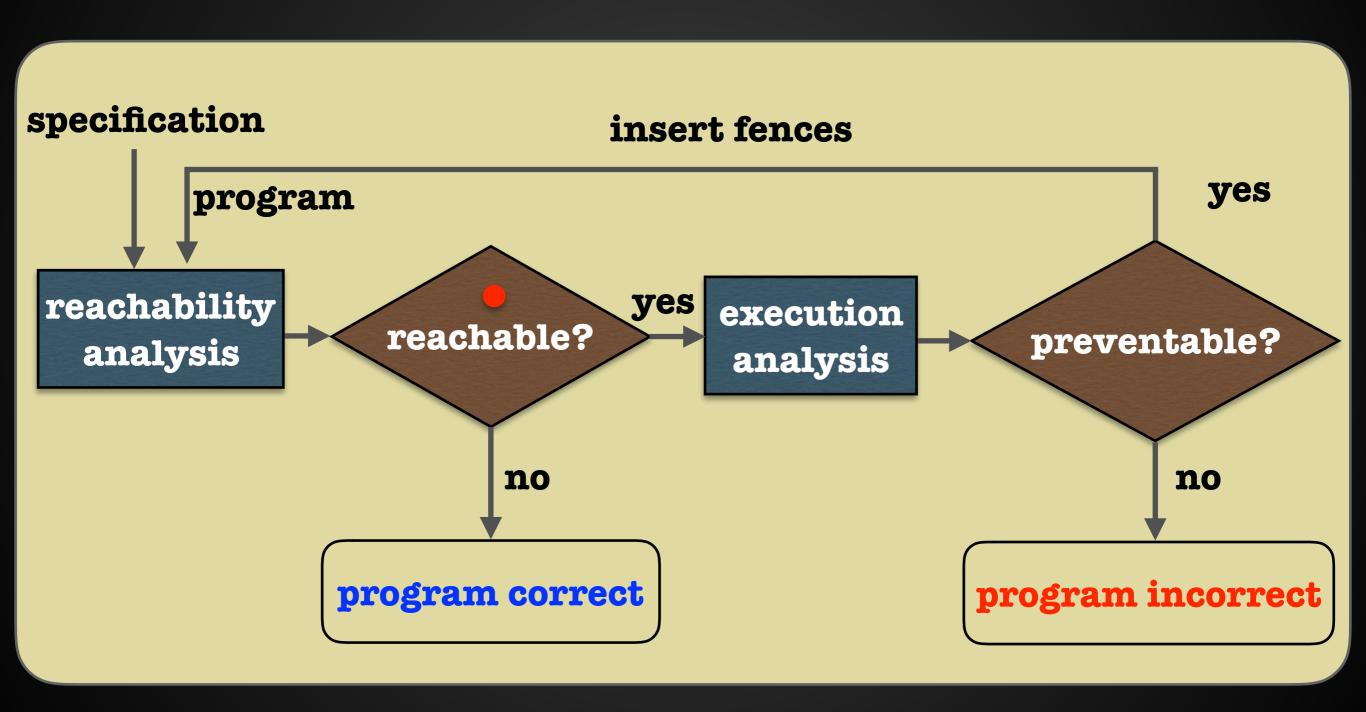


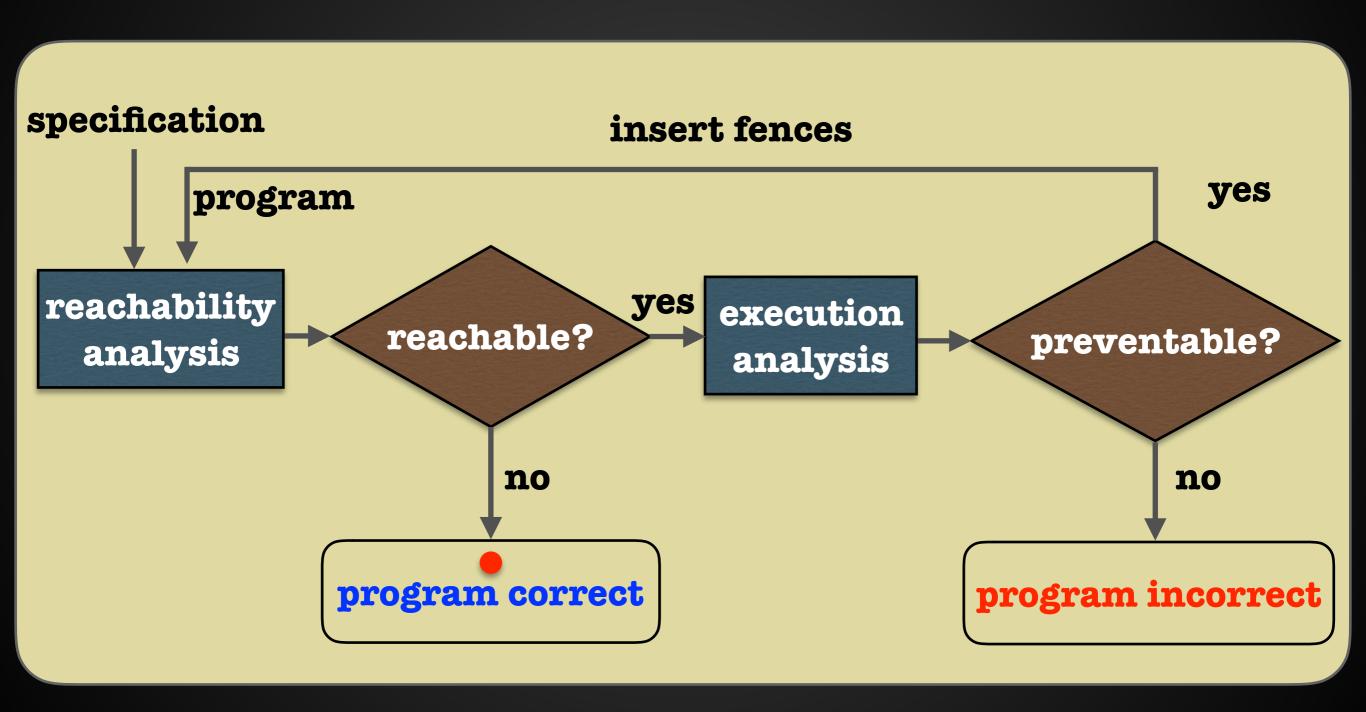


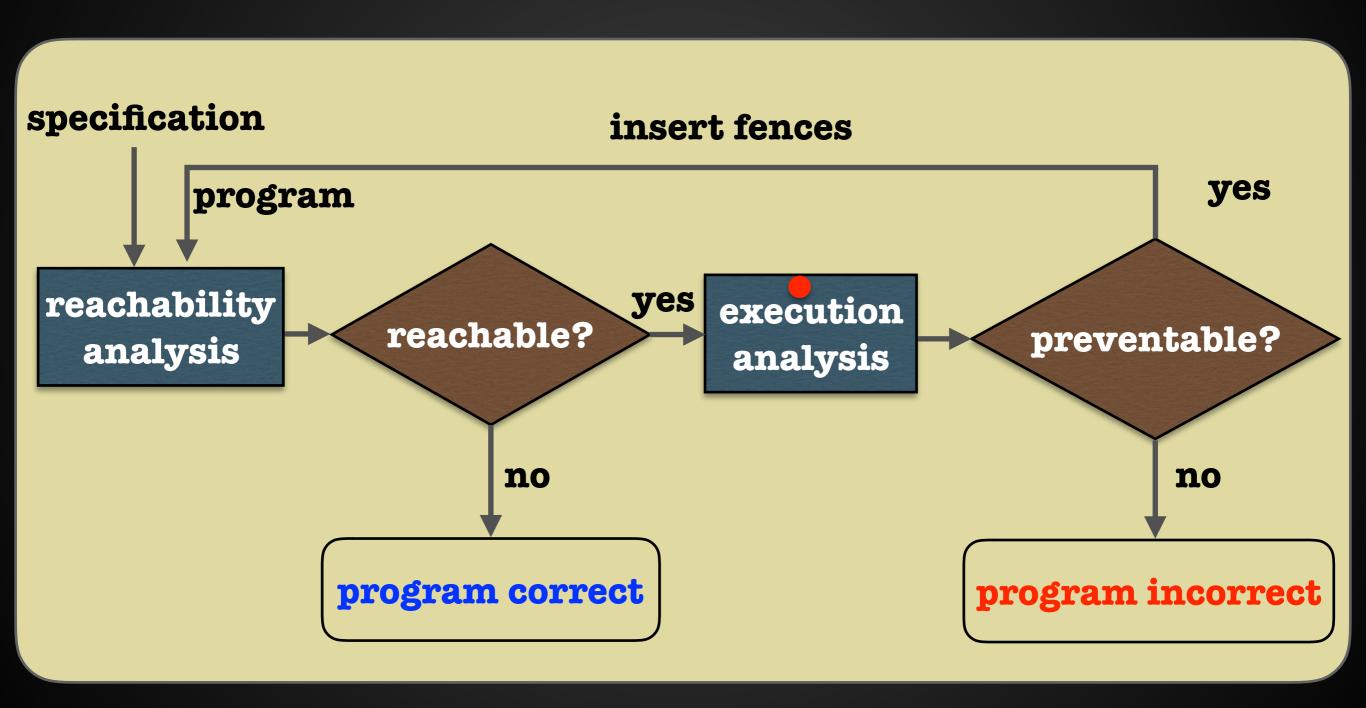


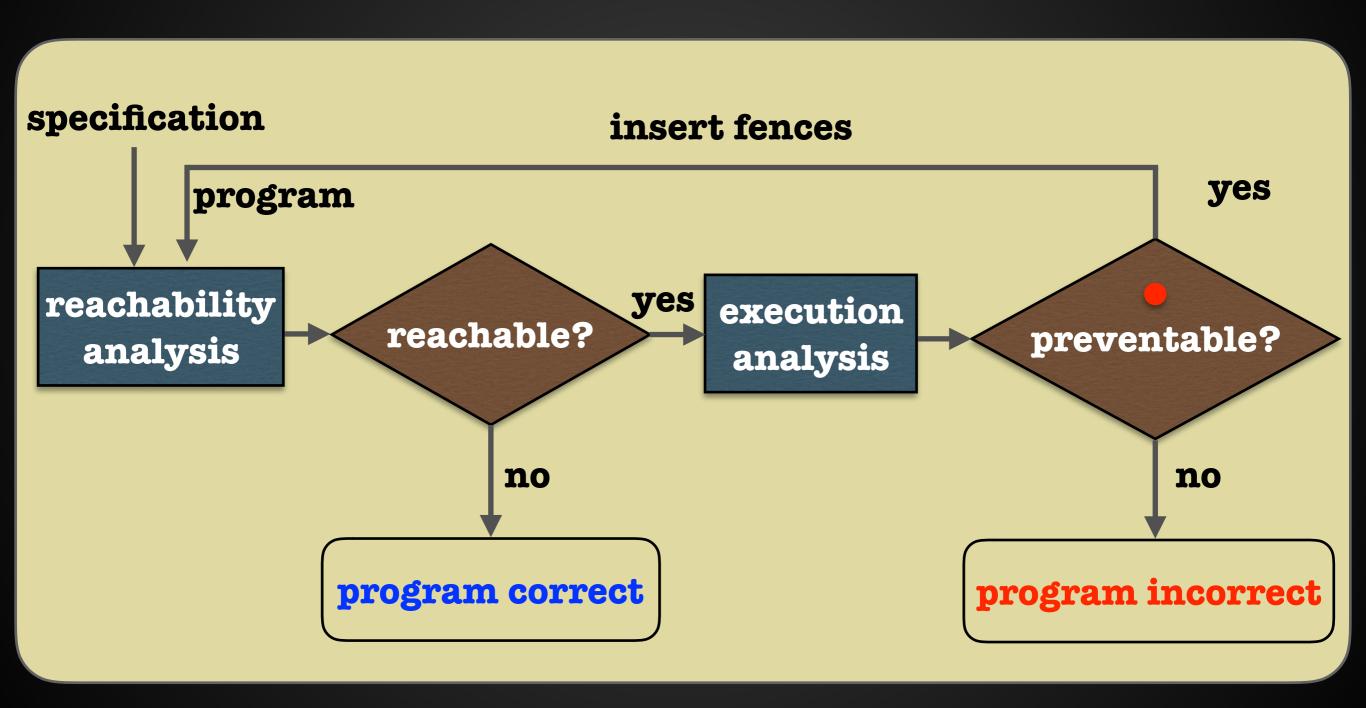


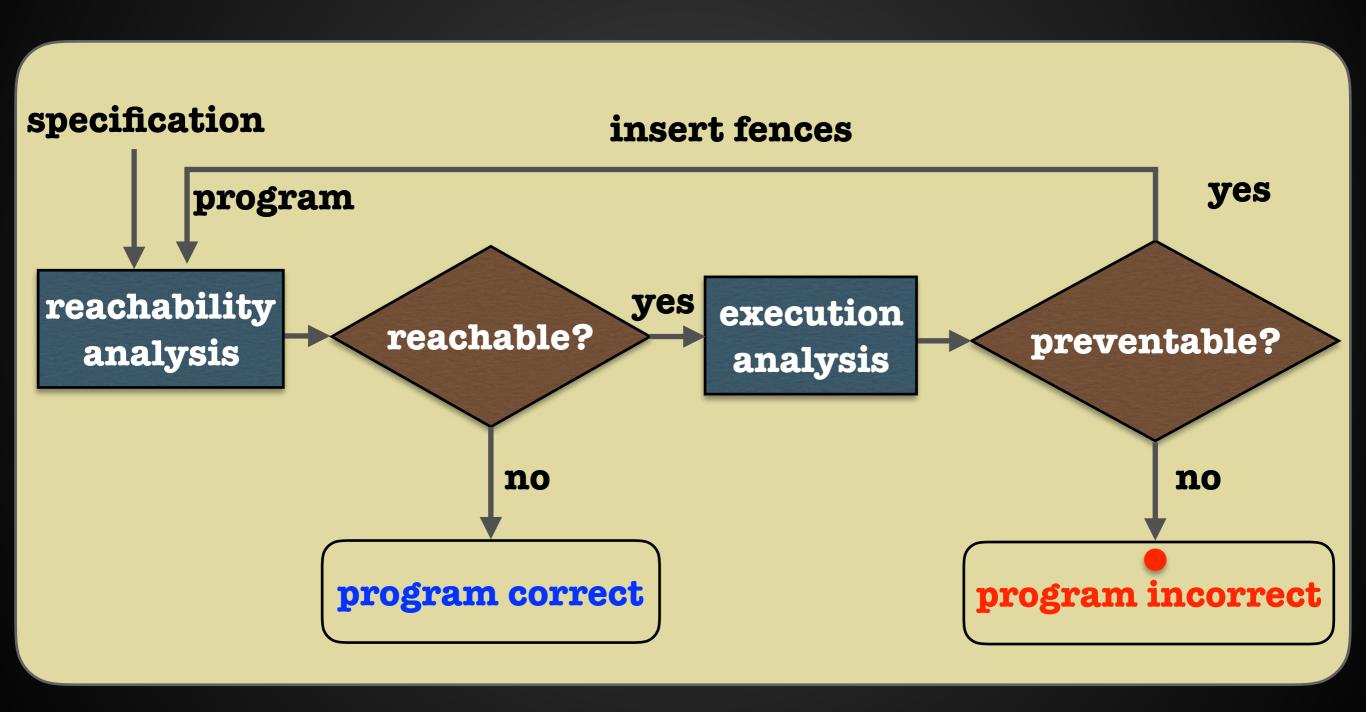


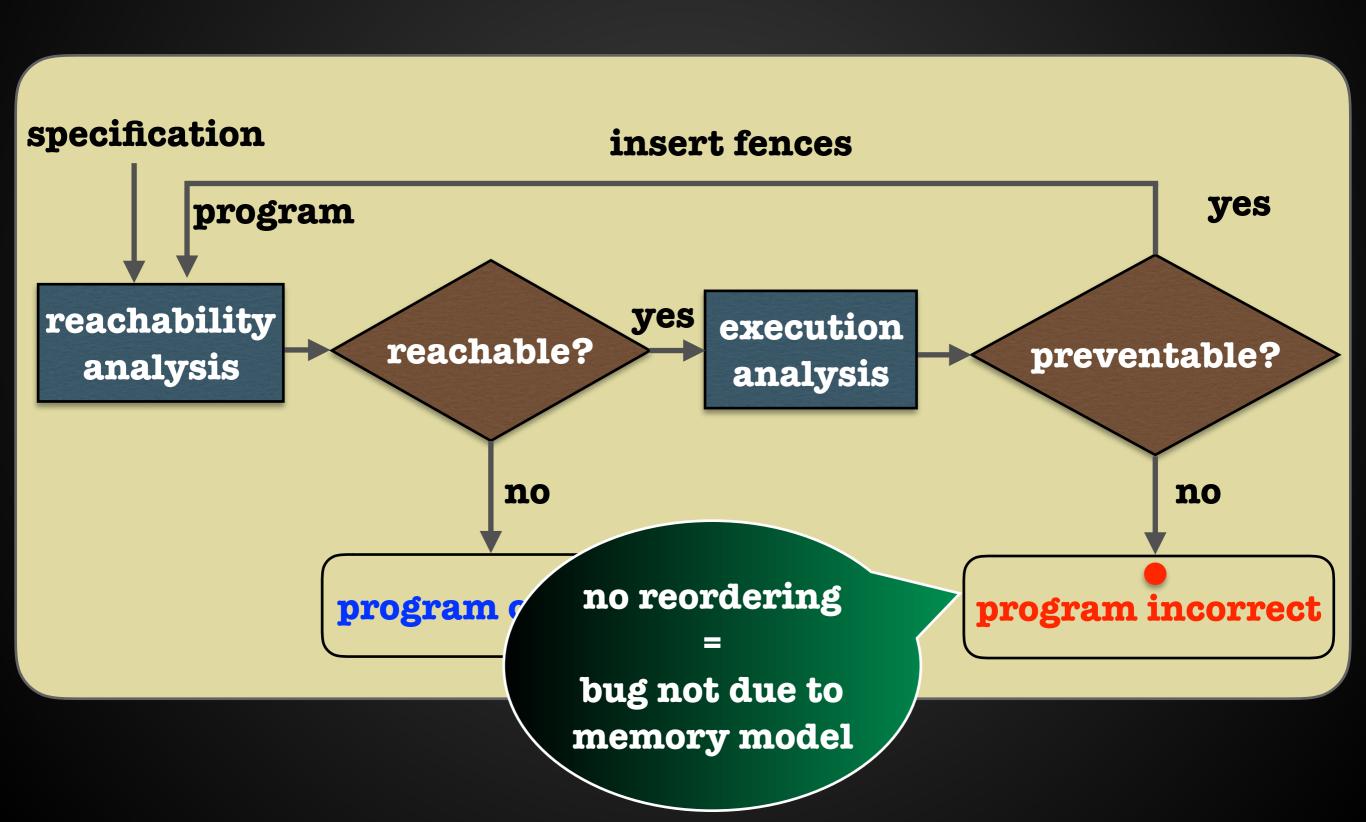


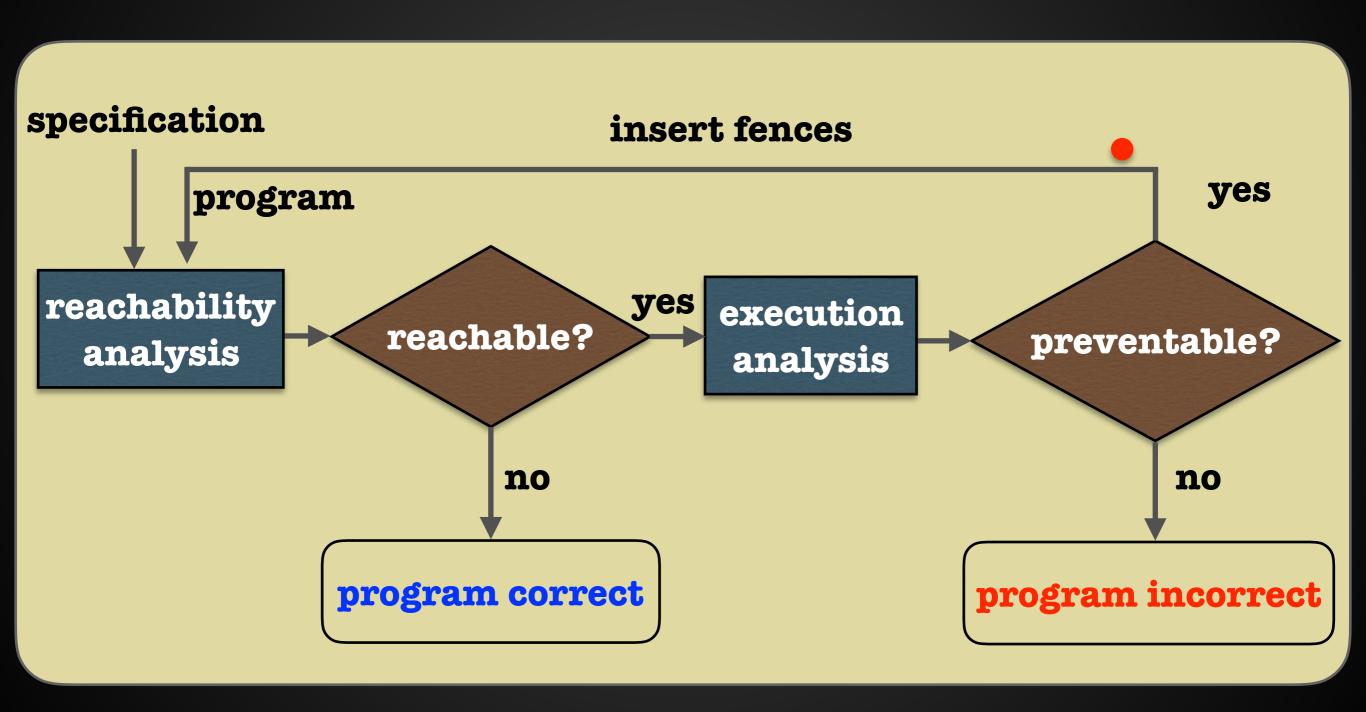


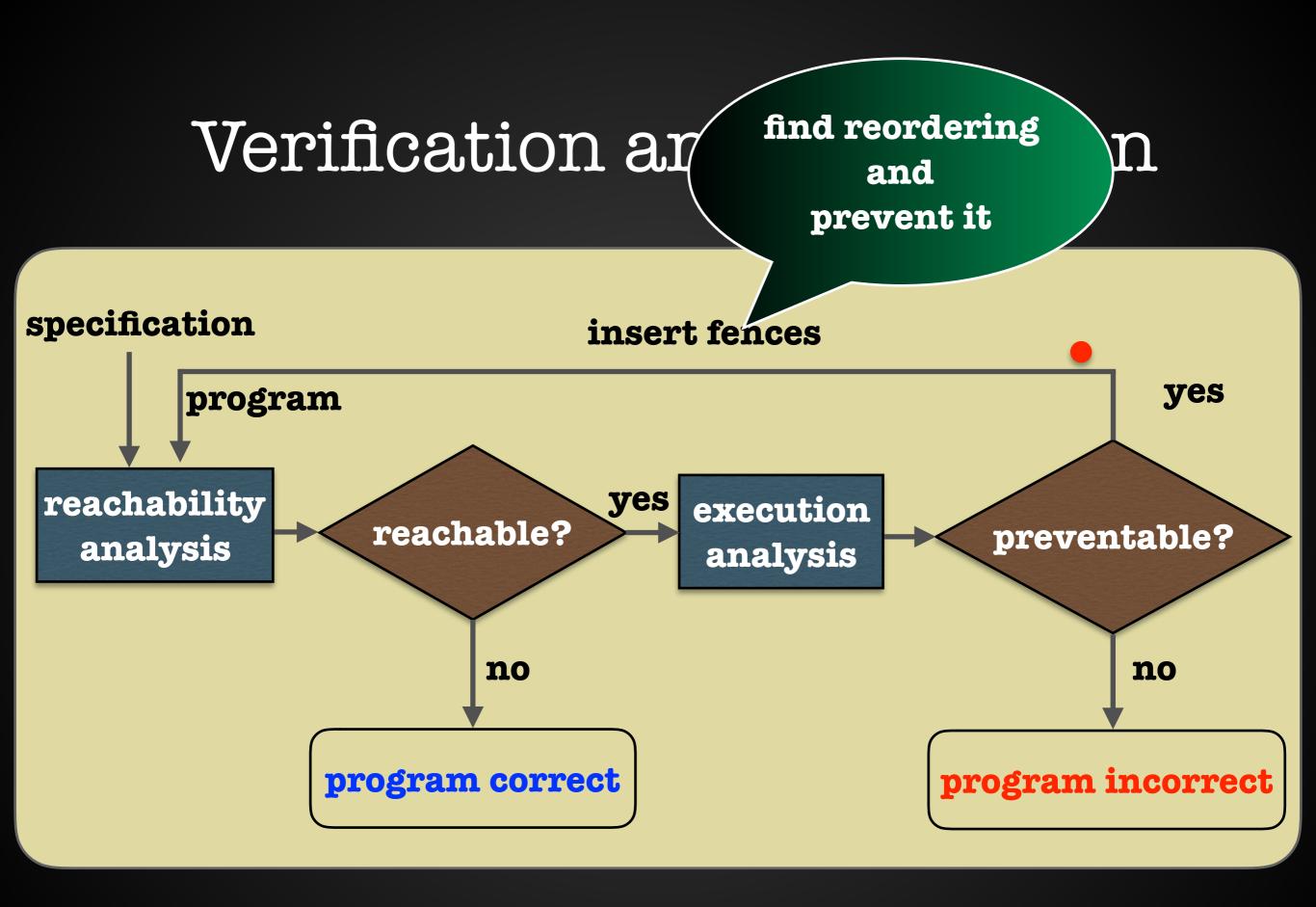


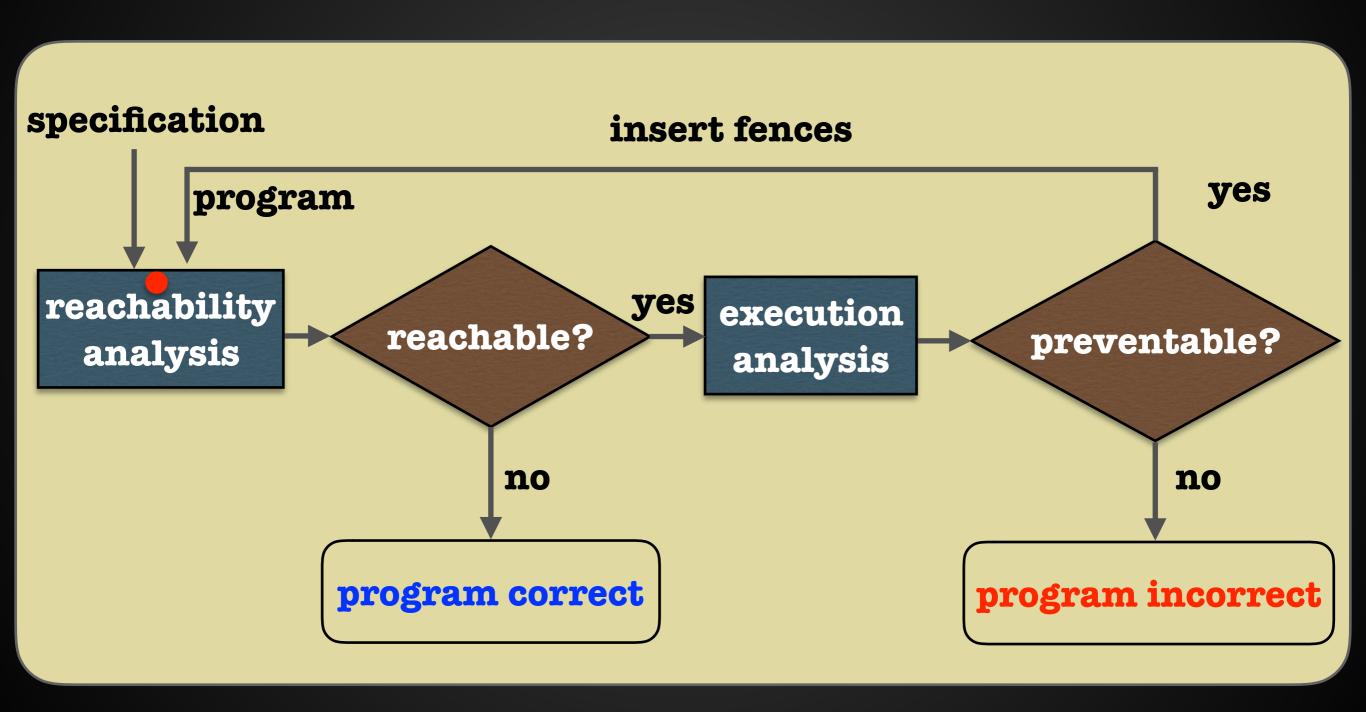


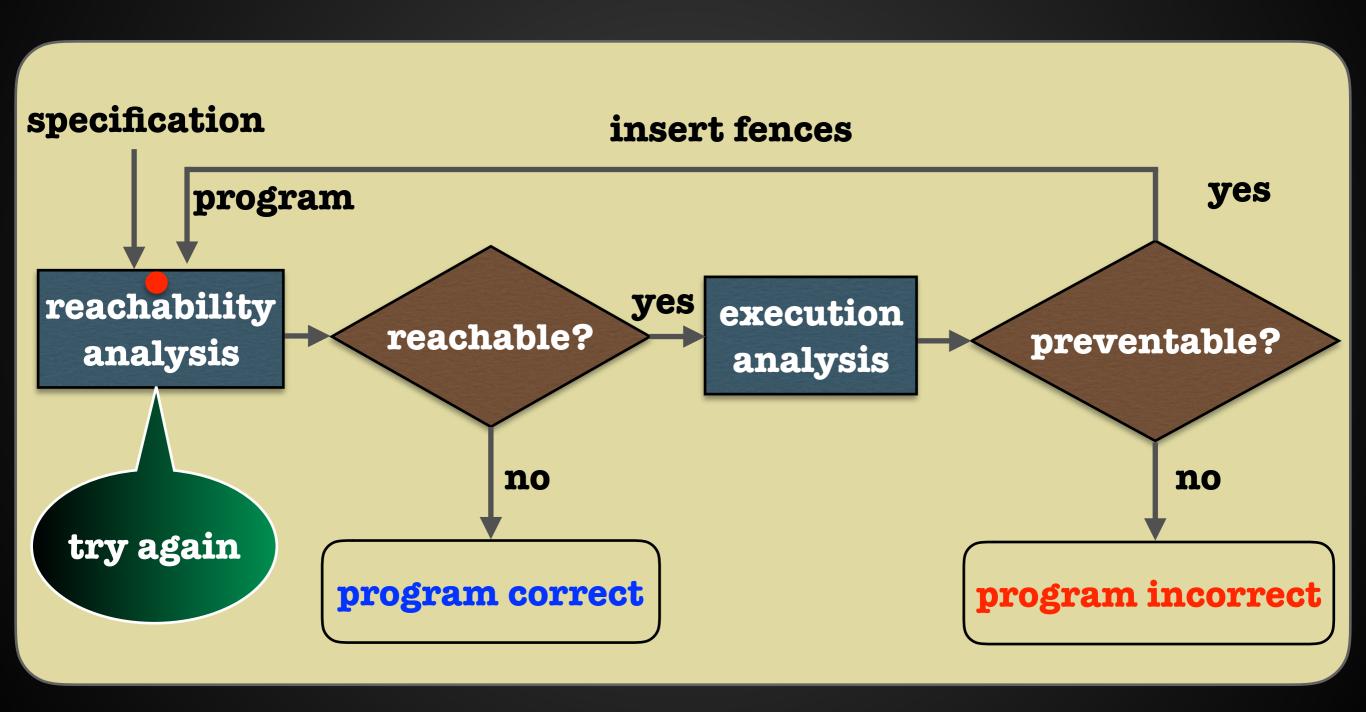


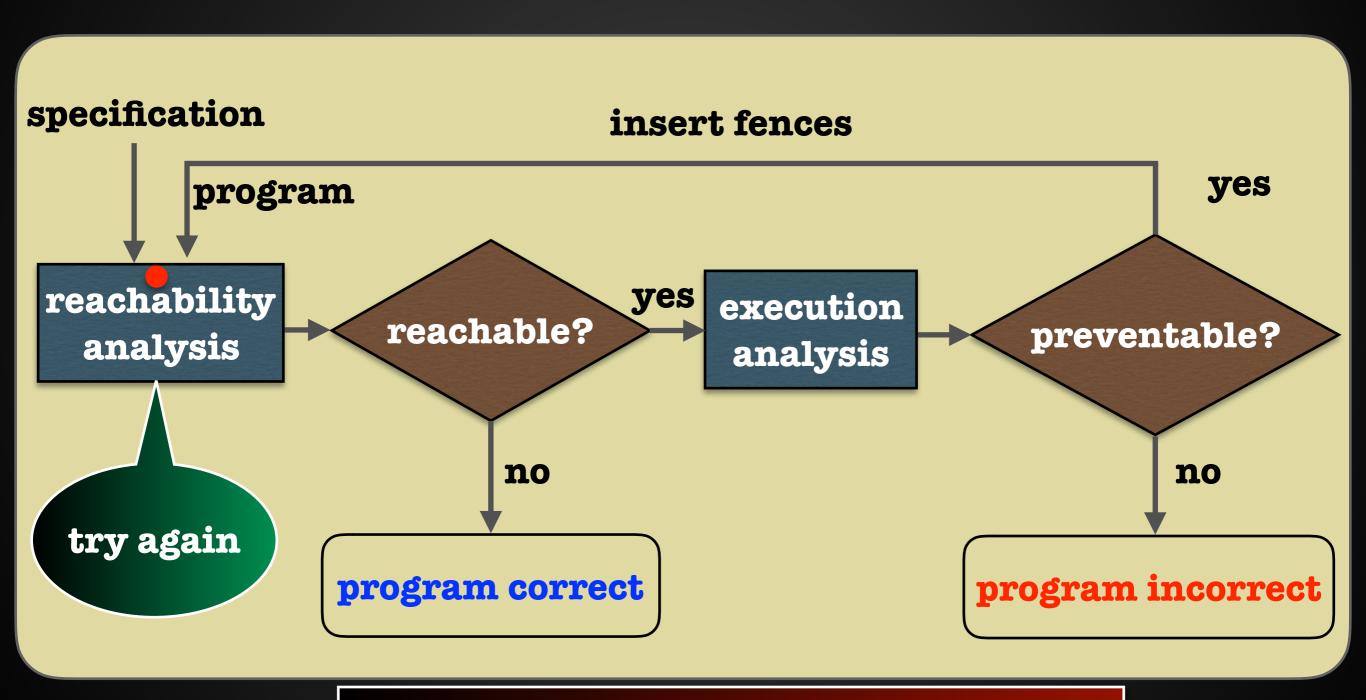












optimality = smallest set of fences needed for correctness

Conclusion

- Weak Consistency
- Total Store Order (TSO)
- Dual TSO

Current Work

- Weak Cache Verification
- Other memory models, e.g., POWER, ARM, C11
- Stateless Model Checking
- Monitor Design

Experimental Results

Dual-TSO vs Memorax

- Running time
- Memory consumption

Drogram	#P	Dua	al-TSO	Memorax	
Program	#-1	#T	#C	#T	#C
SB	5	0.3	10641	559.7	10515914
LB	3	0.0	2048	71.4	1499475
WRC	4	0.0	1507	63.3	1398393
ISA2	3	0.0	509	21.1	226519
RWC	5	0.1	4277	61.5	1196988
W+RWC	4	0.0	1713	83.6	1389009
IRIW	4	0.0	520	34.4	358057
Nbw_w_w	2	0.0	222	10.7	200844
$Sense_rev_bar$	2	0.1	1704	0.8	20577
Dekker	2	0.1	5053	1.1	19788
Dekker_simple	2	0.0	98	0.0	595
Peterson	2	0.1	5442	5.2	90301
Peterson_loop	2	0.2	7632	5.6	100082
Szymanski	2	0.6	29018	1.0	26003
MP	4	0.0	883	ТО	•
$Ticket_spin_lock$	3	0.9	18963	ТО	•
Bakery	2	2.6	82050	ТО	•
Dijkstra	2	0.2	8324	ТО	•
Lamport_fast	3	17.7	292543	ТО	•
Burns	4	124.3	2762578	ТО	•

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Experimental Results

Single buffer approach (exact method [TACAS12+13])

Dual-TSO vs Memorax

- Running time
- Memory consumption

Drogram	#P	Dua	al-TSO	Memorax	
Program	#-I	#T	#C	#T	#C
SB	5	0.3	10641	559.7	10515914
LB	3	0.0	2048	71.4	1499475
WRC	4	0.0	1507	63.3	1398393
ISA2	3	0.0	509	21.1	226519
RWC	5	0.1	4277	61.5	1196988
W+RWC	4	0.0	1713	83.6	1389009
IRIW	4	0.0	520	34.4	358057
Nbw_w_wr	2	0.0	222	10.7	200844
$Sense_rev_bar$	2	0.1	1704	0.8	20577
Dekker	2	0.1	5053	1.1	19788
Dekker_simple	2	0.0	98	0.0	595
Peterson	2	0.1	5442	5.2	90301
Peterson_loop	2	0.2	7632	5.6	100082
Szymanski	2	0.6	29018	1.0	26003
MP	4	0.0	883	ТО	•
$Ticket_spin_lock$	3	0.9	18963	ТО	•
Bakery	2	2.6	82050	ТО	•
Dijkstra	2	0.2	8324	ТО	•
Lamport_fast	3	17.7	292543	ТО	•
Burns	4	124.3	2762578	ТО	•

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Experimental Results

Dual-TSO vs Memorax

- Running time
- Memory consumption

standard
benchmarks:
litmus tests and mutual
exclusion algorithms

Drogram	#D	Dua	al-TSO	Me	emorax
Program	#P	#T	#C	#T	#C
SB	5	0.3	10641	559.7	10515914
LB	3	0.0	2048	71.4	1499475
WRC	4	0.0	1507	63.3	1398393
ISA2	3	0.0	509	21.1	226519
RWC	5	0.1	4277	61.5	1196988
W+RWC	4	0.0	1713	83.6	1389009
IRIW	4	0.0	520	34.4	358057
Nbw_w_wr	2	0.0	222	10.7	200844
Sense_rev_bar	2	0.1	1704	0.8	20577
Dekker	2	0.1	5053	1.1	19788
Dekker_simple	2	0.0	98	0.0	595
Peterson	2	0.1	5442	5.2	90301
Peterson_loop	2	0.2	7632	5.6	100082
Szymanski	2	0.6	29018	1.0	26003
MP	4	0.0	883	ТО	•
Ticket_spin_lock	3	0.9	18963	ТО	•
Bakery	2	2.6	82050	ТО	•
Dijkstra	2	0.2	8324	ТО	•
Lamport_fast	3	17.7	292543	ТО	•
Burns	4	124.3	2762578	ТО	•

Experimental R

running time in seconds

Dual-TSO vs Memorax

- Running time
- Memory consumption

Program	#P	Dua	I-TS	Memorax	
1 Togram	#1	#T	#C	#T	#C
SB	5	0.3	10641	559.7	10515914
LB	3	0.0	2048	71.4	1499475
WRC	4	0.0	1507	63.3	1398393
ISA2	3	0.0	509	21.1	226519
RWC	5	0.1	4277	61.5	1196988
W+RWC	4	0.0	1713	83.6	1389009
IRIW	4	0.0	520	34.4	358057
Nbw_w_wr	2	0.0	222	10.7	200844
$Sense_rev_bar$	2	0.1	1704	0.8	20577
Dekker	2	0.1	5053	1.1	19788
Dekker_simple	2	0.0	98	0.0	595
Peterson	2	0.1	5442	5.2	90301
Peterson_loop	2	0.2	7632	5.6	100082
Szymanski	2	0.6	29018	1.0	26003
MP	4	0.0	883	ТО	•
$Ticket_spin_lock$	3	0.9	18963	ТО	•
Bakery	2	2.6	82050	ТО	•
Dijkstra	2	0.2	8324	ТО	•
$Lamport_fast$	3	17.7	292543	ТО	•
Burns	4	124.3	2762578	ТО	•

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Experimental Res

generated configurations

Dual-TSO vs Memorax

- Running time
- Memory consumption

Drogram	#P	Dua	l-TSO	Memorax		
Program	// -Γ	#T	#C	#T	#C	
SB	5	0.3	10641	559.7	10515914	
LB	3	0.0	2048	71.4	1499475	
WRC	4	0.0	1507	63.3	1398393	
ISA2	3	0.0	509	21.1	226519	
RWC	5	0.1	4277	61.5	1196988	
W+RWC	4	0.0	1713	83.6	1389009	
IRIW	4	0.0	520	34.4	358057	
Nbw_w_w	2	0.0	222	10.7	200844	
$Sense_rev_bar$	2	0.1	1704	0.8	20577	
Dekker	2	0.1	5053	1.1	19788	
$Dekker_simple$	2	0.0	98	0.0	595	
Peterson	2	0.1	5442	5.2	90301	
Peterson_loop	2	0.2	7632	5.6	100082	
Szymanski	2	0.6	29018	1.0	26003	
MP	4	0.0	883	ТО		
$Ticket_spin_lock$	3	0.9	18963	ТО		
Bakery	2	2.6	82050	ТО		
Dijkstra	2	0.2	8324	ТО	•	
Lamport_fast	3	17.7	292543	ТО		
Burns	4	124.3	2762578	ТО	Ŀ	

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Experimental Res

generated configurations

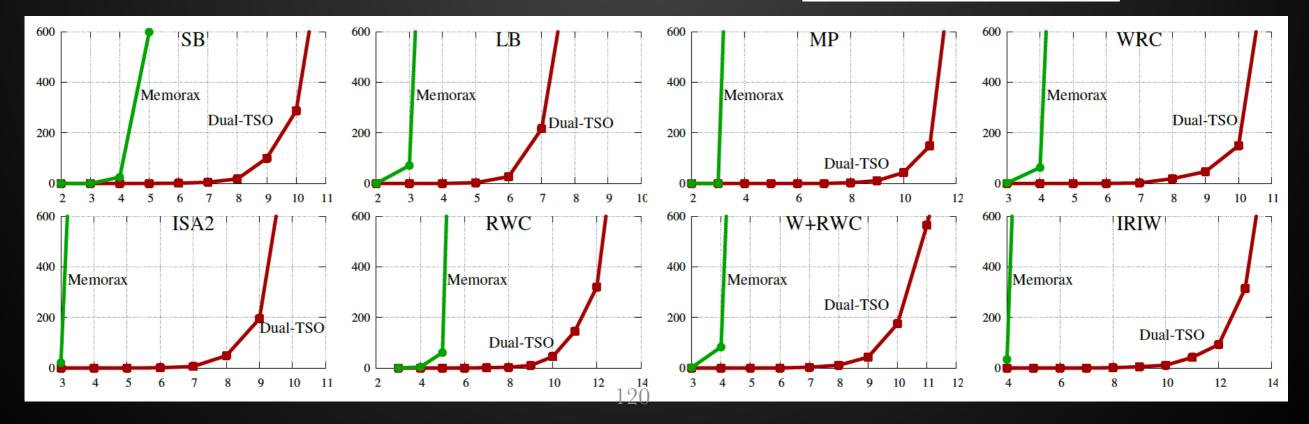
Dual-TSO vs Memorax

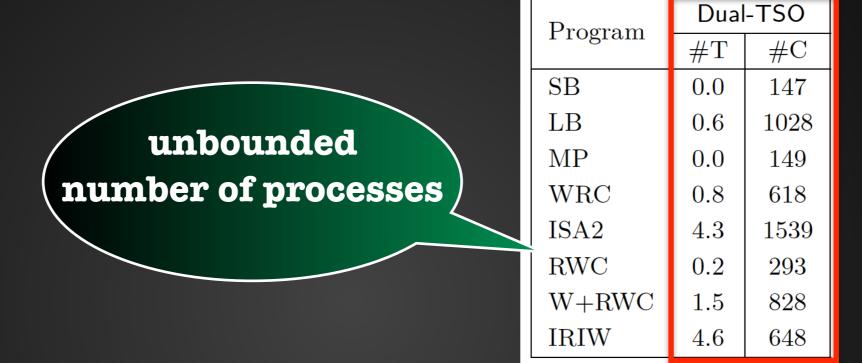
- Running time
- Memory consumption

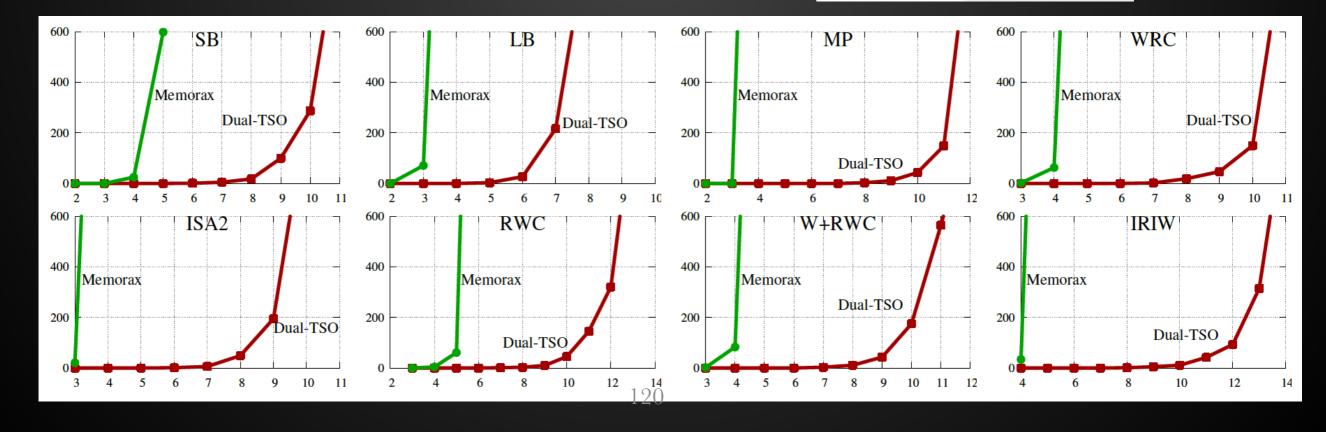
Dual-TSO is faster and uses less memory in most of examples

		Dua	l-TSO	Me	morax
Program	#P	#T	#C	#T	#C
SB	5	0.3	10641	559.7	10515914
LB	3	0.0	2048	71.4	1499475
WRC	4	0.0	1507	63.3	1398393
ISA2	3	0.0	509	21.1	226519
RWC	5	0.1	4277	61.5	1196988
W+RWC	4	0.0	1713	83.6	1389009
IRIW	4	0.0	520	34.4	358057
Nbw_w_wr	2	0.0	222	10.7	200844
Sense_rev_bar	2	0.1	1704	0.8	20577
Dekker	2	0.1	5053	1.1	19788
Dekker_simple	2	0.0	98	0.0	595
Peterson	2	0.1	5442	5.2	90301
Peterson_loop	2	0.2	7632	5.6	100082
Szymanski	2	0.6	29018	1.0	26003
MP	4	0.0	883	ТО	•
Ticket_spin_lock	3	0.9	18963	ТО	•
Bakery	2	2.6	82050	ТО	•
Dijkstra	2	0.2	8324	ТО	•
Lamport_fast	3	17.7	292543	ТО	•
Burns	4	124.3	2762578	ТО	ldot

Program	Dual-TSO			
Togram	#T	#C		
SB	0.0	147		
LB	0.6	1028		
MP	0.0	149		
WRC	0.8	618		
ISA2	4.3	1539		
RWC	0.2	293		
W+RWC	1.5	828		
IRIW	4.6	648		

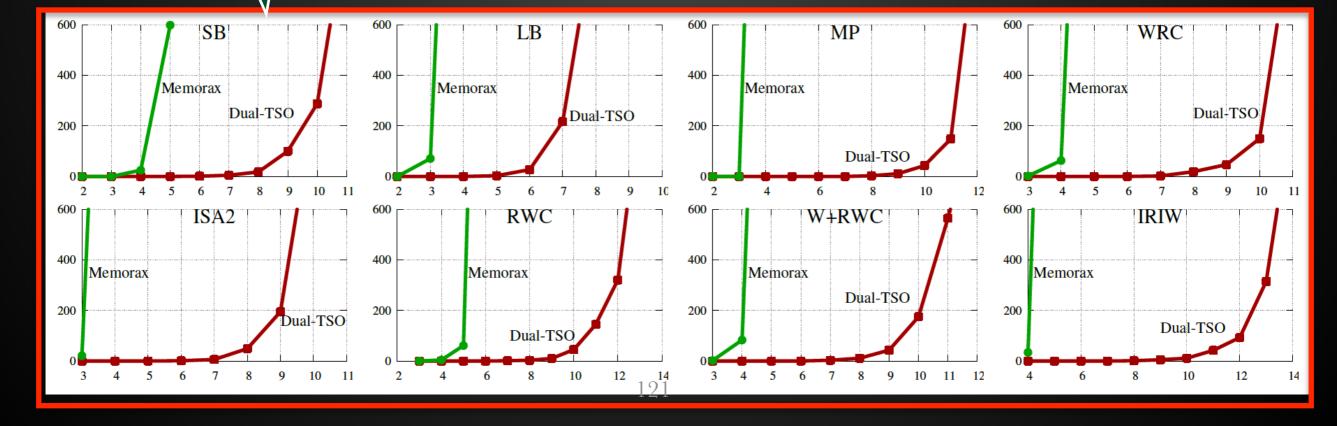


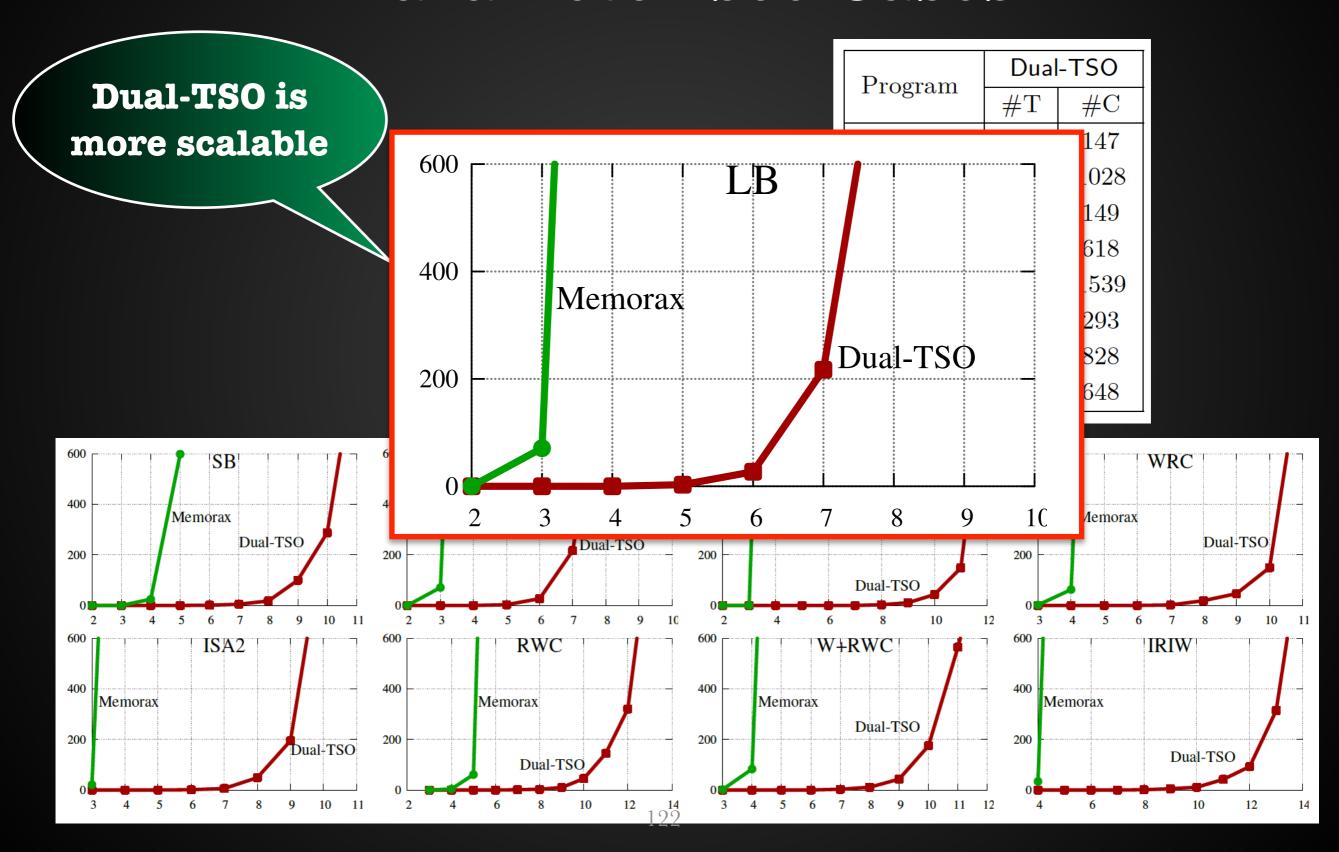




increasing the number of processes

Program	Dual-TSO			
Trogram	#T	#C		
SB	0.0	147		
LB	0.6	1028		
MP	0.0	149		
WRC	0.8	618		
ISA2	4.3	1539		
RWC	0.2	293		
W+RWC	1.5	828		
IRIW	4.6	648		





Dual-TSO is more efficient and scalable

Program	Dual-TSO			
Trogram	#T	#C		
SB	0.0	147		
LB	0.6	1028		
MP	0.0	149		
WRC	0.8	618		
ISA2	4.3	1539		
RWC	0.2	293		
W+RWC	1.5	828		
IRIW	4.6	648		

