

Optimal Dynamic Partial Order Reduction with Observers

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Abstract. Dynamic partial order reduction (DPOR) algorithms are used in stateless model checking (SMC) to combat the combinatorial explosion in the number of schedulings that need to be explored to guarantee soundness. The most effective of them, the Optimal DPOR algorithm, is optimal in the sense that it explores only one scheduling per Mazurkiewicz trace. In this paper, we enhance DPOR with the notion of *observability*, which makes dependencies between operations conditional on the existence of future operations, called *observers*. Observers naturally lead to a lazy construction of dependencies. This requires significant changes in the core of POR algorithms (and Optimal DPOR in particular), but also makes the resulting algorithm, Optimal DPOR with Observers, super-optimal in the sense that it explores exponentially less schedulings than Mazurkiewicz traces in some cases. We argue that observers come naturally in many concurrency models, and demonstrate the performance benefits that Optimal DPOR with Observers achieves in both an SMC tool for shared memory concurrency and a tool for concurrency via message passing, using both synthetic and actual programs as benchmarks.

1 Introduction

Testing and verification of concurrent programs is hard, as it requires reasoning about all the ways in which operations executed by different processes (or threads) can interfere. *Stateless model checking (SMC)* [12] is a technique with low memory requirements that can be effective in finding concurrency errors or proving that a program cannot reach an error state by systematically exploring all the ways in which such operations can be interleaved. The technique requires taking control of the scheduler and subsequently executing the program multiple times, each time imposing a different scheduling of the processes. By considering every process at every execution step, however, the number of possible schedulings grows exponentially w.r.t. the total length of program execution. *Partial order reduction (POR)* techniques [9, 11, 20, 22] address this problem by prescribing the exploration of only a subset of schedulings, albeit a subset that is sufficient to cover all behaviours. POR techniques take advantage of the fact that most pairs of operations by different processes in typical concurrent programs are not interfering. As a result, a scheduling E that can be obtained from another scheduling E' by swapping adjacent but non-interfering (independent) execution steps will make the program behave in exactly the same way as E' ; such schedulings have the same partial order of interfering operations and belong to the same equivalence class, called a *Mazurkiewicz trace* [19]. It is sufficient for SMC algorithms to explore only one scheduling in each such equivalence class.

POR algorithms operate by examining pairs of interfering operations. If it is possible to execute such operations in the reverse order, then their partial order will be different, and a scheduling from the relevant equivalence class must also be explored. For soundness, POR techniques need to be conservative, treating operations as interfering even in cases where they are not. Increasing the accuracy of interference detection can therefore significantly improve the effectiveness of any POR technique. In early POR techniques, interference was determined statically, leading to over-approximations and limiting the achievable reduction. The efficiency of POR was later increased using semantic information to decide which operations interfere [13]. *Dynamic Partial Order Reduction* (DPOR) [10] further improved the effectiveness of POR algorithms by allowing interference to be determined from data obtained during the program’s execution.

In this paper, we introduce the notion of *observability* of operations, allowing *observer* operations that appear later in a scheduling to be used when deciding whether earlier operations are interfering. We start by explaining observers with a series of examples (Sect. 2), and continue by presenting key notions of DPOR and explaining why using observers in DPOR algorithms is challenging (Sect. 3). We then present a formal framework (Sect. 4) and describe an extension to the Optimal DPOR algorithm [2] that enables use of observers (Sect. 5). The extension is generic in the sense that it can be applied to several models of concurrency, such as shared memory and message passing. We demonstrate this claim by two implementations: one in an SMC tool for C/C++ programs with pthreads and one in an SMC tool for Erlang programs (Sect. 6). Finally, in Sect. 7 we evaluate our implementations and show that Optimal DPOR with Observers can achieve significantly better reduction in both synthetic and ‘real’ programs.

2 DPOR and Observers by Example

Consider the program shown in Fig. 1 in which a *main* process spawns two concurrent processes, *p* and *q*, which issue write operations on two different shared variables *x* and *y*. After *p* and *q* finish their execution, the *main* process reads the values of *x* and *y* and checks a correctness property. A DPOR algorithm will begin exploring this program by executing an arbitrary scheduling; see Fig. 1 (middle). Nodes show the values of the shared variables and each transition consists of an execution step. By inspecting the operations in this scheduling, the algorithm sees that if the second step of *q* is scheduled before the second step of *p*, the partial order of the writes to the *y* variable is different. It therefore plans to execute a scheduling in which the second step of *p* happens after

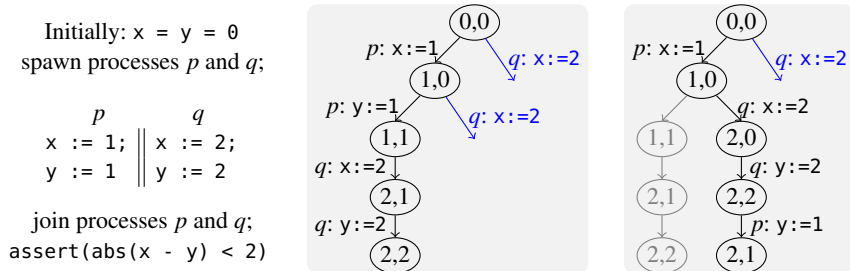


Fig. 1: Writers program (its correctness property as assertion) and two of its schedulings.

the one from q . The start of this scheduling can be denoted as $p.q$. Similarly, the order of the writes on x can be reversed, by executing q 's first step before the first step of p . Therefore, a scheduling starting with q should also be explored. In Optimal DPOR [2], future explorations are added as partial schedulings, forming *wakeup trees* (shown in blue). These trees are quite trivial in this example, each consisting of a single path.

The algorithm continues exploration from the “deepest” point where a new scheduling should be tried; in the example, this is the (1,0) node. A second scheduling is explored with the intention to execute some operation before the second step of p . Without any other constraint, a non-optimal DPOR algorithm could execute p 's second step immediately after the first step of q , ending up in a state identical with the previously explored (2,1) and then again in (2,2). The *sleep sets* technique [11] can be used to avoid or stop such redundant explorations. Sleep sets retain information from already explored earlier process steps that have not yet been ‘overtaken’ by some step in the current exploration. In our example, information about p 's second step is retained in the sleep set until some other interfering operation (here q 's second step) has been executed. Moreover, sleep sets can be used to infer that swapping (again) the second step of p and the second step of q (based on their interference in the second scheduling) is redundant. Any DPOR algorithm using sleep sets will explore four schedulings for this program (instead of the six ones possible). Each of these four schedulings leads to a different final state. Notice that two writes on the same variable were always deemed as interfering.

Consider now the program shown on the right.
 The shared variable x (whose initial value is 0) is $x := 1 \parallel x := 2 \parallel \text{assert}(x < 3)$
 accessed by processes p, q and r . Here, the correctness property is checked by process r . If interference is decided using the same criteria as a *data race* (i.e., two operations interfere if they access the same memory location and at least one of them is a write), then all three operations interfere with each other. As a result, each of the $3! = 6$ possible interleavings has a different partial order and therefore belongs to a different Mazurkiewicz trace that should be explored by a DPOR algorithm. In schedulings starting with r , however, the order of the execution of p and q is irrelevant (if one does not care about the final contents of the memory), as the values written by these operations will never be read. A DPOR algorithm could detect that the written values are *not observed* and consider the write operations as non-interfering.

Taking this idea further, consider a next example,
 shown on the right. Here, N processes write on the $x := 1 \parallel x := 2 \parallel \dots \parallel x := N$
 shared variable x , and as a result there exist $N!$ schedul- $\text{join processes } p_1, p_2, \dots, p_N;$
 ings. In each such scheduling, however, only the last $\text{assert}(x > 0)$
 written value will be read. A DPOR algorithm could consider write operations that are not subsequently observed as independent and therefore explore just N instead of $N!$ schedulings, thereby achieving an exponential reduction.

In the last two examples, better reduction could be obtained if the interference of write operations, which are conservatively considered as “always interfering”, was characterized more accurately by looking at complete executions and taking observability by “future” operations into account. This idea is applicable not only in shared memory but also in other models of concurrency. In the next message passing program, processes p and q each send a different message to the mailbox of process r using the send operator “!”.

Process r uses a receive operation to retrieve a message p q r and store it in a (local) variable x . If we assume that $r ! M_1 \parallel r ! M_2 \parallel \text{receive } x$ receive operations pick and return the oldest message in the mailbox or return null if no message exists, send operations can interfere (the order of delivery is significant) and so can send and receive operations (an empty mailbox can yield a different value). As a result, six schedulings are possible. However, only three schedulings need to really be explored: the receive operation interferes only with the earliest send operation and cannot be affected by a later send; moreover, if the receive operation is executed first, the order of the send operations is irrelevant.

If we instead assume that receive operations *block* if no matching message exists, only *two* schedulings need to be explored, as r can receive either M_1 or M_2 . Again, if we generalize the example to N processes instead of just two, the behaviour is similar to the program with N writes: only N schedulings (instead of $N!$) are relevant, each determined by the first message delivered; the remaining message deliveries are not observable. Note that, in this concurrency model, we are interested in the observability of the *first* instead of the last operation in an execution sequence.

In some message-passing concurrency models (e.g., in Erlang programs [4]), it is further possible to use *selective* receive operations instead, which also block when no message can be selected. Using this feature, the previous program can be generalized and rewritten so that r is explicitly picking messages in order, using pattern matching. Such a program is shown on the right.

Here r wants to pick up the N messages $r ! M_1 \parallel r ! M_2 \parallel \dots \parallel r ! M_N \parallel \text{receive } M_1; \text{receive } M_2; \dots; \text{receive } M_N$ in order: first M_1 , then M_2 , etc. Thus, the order of delivery of messages is irrelevant. A DPOR algorithm could take advantage of the additional information provided by

the selective receive operations, notice that the messages from $p_{i+1} \dots p_N$ cannot be selected before the message from p_i , and therefore determine that the N sends are independent. A *single* scheduling is enough to explore all behaviours of the program!

Having explained the concept of *observability* of operations by examples, let us see how it can be combined with the Optimal DPOR algorithm and achieve such reductions.

3 Using Observers in a DPOR Algorithm

Our objective is to construct a DPOR algorithm that *lazily* considers interferences based on the existence of *later* operations, called *observers*. In the simplest case, operations that would be conservatively considered interfering are treated as independent in the absence of an observer. Examples in Sect. 2 included write operations whose values were never read, or cases where the order of message deliveries does not affect the order in which the messages are received.

The intuition behind such an SMC approach comes from the fact that it is only operations that *observe* a value (e.g., assertions, receive statements, etc.) that can influence the control flow and lead to erroneous or generally unexpected behaviour. Other operations (e.g., writes, sends, etc.) cannot affect program behaviour if no future operation observes their effects. In such cases, interference between those other operations can be ignored.

3.1 POR Concepts and Optimal DPOR

The goal of POR techniques is the exploration of only a (small) subset of the possible schedulings of a concurrent program which is *sound*; that is, a subset that includes at least one scheduling from each Mazurkiewicz trace. DPOR algorithms perform a depth-first exploration of the tree of all possible schedulings. Reduction is achieved by exploring only a sound subset of all scheduling choices that are possible at each point in the tree. Such subsets are formed on the basis of two complementary techniques.

- Each point in the tree is associated with a *sleep set*, which contains a set of processes whose exploration would be redundant. More precisely, a process p is in the sleep set after a sequence of form $E.v$ if p has previously been explored after E , and furthermore p does not interfere with v . Thus, exploring $E.v.p$ is redundant, since it was previously explored after $E.p$ (as $E.p.v$).
- From each point in the tree, the set of explored processes must form a *source set* [2]. (Some DPOR algorithms employ persistent or stubborn sets, which are subsumed by source sets.) Source sets have the property that for any extension which forms a complete (aka *maximal*) scheduling, there is an equivalent extension in which the next step is taken by a process in the source set. A source set is constructed incrementally during the exploration by inspecting encountered races: whenever a scheduling of form $E.p.v$ is explored, in which the step of p is in a race with some step in v , then the reversal of that race will be explored in some other scheduling, where some process q in v is scheduled immediately after E : this is achieved by adding q to the source set after E .

Most existing DPOR algorithms prescribe that from each point in the tree (i) all processes in a source set should be explored, and (ii) no process in the sleep set should be explored. However, these principles are not sufficient to avoid redundant exploration [2]. The reason is that the reversal of a race in $E.p.v$ may happen only by exploring a particular subsequence of v ; since a source set can only contain the first step in such a sequence, it can not prevent continued exploration beyond that first step from being redundant. Optimal DPOR improves on earlier techniques by using *wakeup trees* [2] in addition to sleep sets. Wakeup trees are composed of partial execution sequences (called *wakeup sequences*) that (a) reverse the order of the interfering operations, and (b) are provably non-redundant. Optimal DPOR, currently the state-of-the-art DPOR algorithm, always uses wakeup sequences to explore new schedulings. As a result, Optimal DPOR does not even initiate redundant exploration, and can achieve exponential reduction over e.g., the original [10] or the Source DPOR [2] algorithm.

3.2 Observers and Sleep Sets

The use of sleep sets is not trivial when using observers, because interference between events can often not be determined when they occur, but only later in the scheduling. Let us illustrate using an example. In the next program, three processes (p , q and s) send tagged messages (with tags A and B) to a receiver process r , which uses selective receive to read matching messages from its mailbox. Each message also contains the process identifier of the sender.

$$\begin{array}{c}
p \\
r ! \{B, p\}; \\
r ! \{A, p\}
\end{array}
\parallel
\begin{array}{c}
q \\
r ! \{A, q\};
\end{array}
\parallel
\begin{array}{c}
s \\
r ! \{B, s\};
\end{array}
\parallel
\begin{array}{c}
r \\
\text{receive } \{A, x\}; \\
\text{if } (x == p) \\
\text{receive } \{B, y\}
\end{array}$$

In standard DPOR, the sends are interfering, since the order of delivery can affect the values assigned to the x and y variables in r . Using observers, sends are interfering only if justified by an observing receive operation. Assume that the first explored scheduling is $p.p.q.s.r.r$. Here, the second send by p (sending the message tagged with A) interferes with the send by q , since their order is observed by the first receive of r (if the message from q had been delivered first, it would have been the one picked instead). Furthermore, the first send by p (sending the message tagged with B) interferes with the message send by s , since they have the second receive of r as observer. In order to explore the reversal of the race between the first send of p and that of s , the algorithm needs to explore a scheduling in which p 's first send is executed after s . Such a scheduling must clearly start with s . The rules for sleep sets prescribe that p should be in the sleep set at the start of this exploration, and that p should be removed from the sleep set after executing s if p and s interfere. However, this interference is visible only later, making it unclear what to do. On the one hand, removing p from the sleep set on the grounds that it “might” interfere with s risks to explore redundant schedulings and defeats the purpose of observers. On the other hand, keeping p in the sleep set and “see what happens” prevents exploring the effects of the race reversal, since that requires the second send of p to be explored before q , which is forbidden if p remains in the sleep set. Thus, sleep sets are not a sufficiently precise mechanism for avoiding redundant exploration without missing non-redundant schedulings.

3.3 Introducing Observers to Optimal DPOR

We will now explain how Optimal DPOR can be adapted to work with observers. There are two main challenges: (1) we need to address the fact that, in the presence of observers, interference is conditional, and (2) we also need a suitable replacement for sleep sets, since we can no longer use them to guarantee that there is no redundant exploration.

In Optimal DPOR, it is assumed that operations that are interfering in some execution sequence remain interfering in any prefix of that sequence. This is no longer true when we determine interference by the existence of observing operations. If an observer is not included in a prefix of an execution sequence in which two operations were observably interfering, the same two operations will be independent. To address challenge 1 in Optimal DPOR with observers, we need to extend the wakeup sequences constructed for reversing the order of interfering operations that require an observer, with a suffix that includes the observer. It is allowed for this suffix to include operations happening after the interfering operations (even in program order); any such operations will behave identically in the reversal because in the original scheduling the observer was the first event that could be affected by the ordering of the interfering operations. To address challenge 2, we can build on the intuition behind sleep sets and assert that when our algorithm is done with a particular state, it has explored all schedulings that can start with the step that led to that state. When the algorithm considers a new scheduling (based

on a wakeup sequence), information about observers in that scheduling needs to be recalculated from the operations in the sequence. The algorithm can then perform an exhaustive test, that ensures that each step previously explored from any point in the execution is overtaken by some other step in the wakeup sequence under consideration.

4 Framework

We consider a concurrent system composed of a finite set of *processes* (or threads). Each process executes a deterministic program, in which statements act on the global *state* of the system. Processes can interact via shared variables, messages, etc. We assume that the state space does not contain cycles, and that executions have bounded length. A step of a process may not disable another process.

Formally, let Σ be the set of states of a concurrent system and $s_0 \in \Sigma$ be the unique *initial state*. The partial function $execute_p : \Sigma \mapsto \Sigma$ describes execution, representing an atomic *execution step* of process p , which may depend on and affect the state. An *execution sequence* E of the system is a finite sequence of execution steps of its processes that is performed from the initial state. We use $\langle \rangle$ to denote the empty sequence and $.$ to denote concatenation of sequences of process steps (e.g., $p.p.q$ denotes the execution sequence where first p performs two steps, followed by a step of q). The sequence of process steps in E also uniquely determine the state of the system after E , which is denoted $s_{[E]}$. For a state s , let $enabled(s)$ denote the set of processes p that are enabled in s (i.e., for which $execute_p(s)$ is defined). If $p \in enabled(s_{[E]})$, then $E.p$ is an execution sequence. A sequence E is *maximal* if $enabled(s_{[E]}) = \emptyset$, i.e., no process is enabled after E . An *event* $\langle p, i \rangle$ of E is a particular occurrence of a process in E , representing the i -th occurrence of process p in the execution sequence. We use w, w', \dots to range over sequences, e, e', \dots to range over events, as well as:

- $E \vdash w$ to denote that $E.w$ is an execution sequence.
- $w \setminus p$ to denote the sequence w with its first occurrence of p removed.
- $dom(E)$ to denote the set of events $\langle p, i \rangle$ which are in E .
- $dom_{[E]}(w)$ to denote $dom(E.w) \setminus dom(E)$, i.e., the events in $E.w$ which are in w .
- $next_{[E]}(p)$ to denote $dom_{[E]}(p)$ as a special case.
- \hat{e} to denote the process p of an event $e = \langle p, i \rangle$.
- $e <_E e'$ to denote that e occurs before e' in E , i.e., $<_E$ is the total order of events.
- $E' \leq E$ to denote that the sequence E' is a prefix of the sequence E .

We assume a function which assigns a *happens-before relation* [15] to any execution sequence E , denoted as \rightarrow_E .

We will keep the general approach of Optimal DPOR and require the happens-before relation to satisfy a set of properties, collected in Definition 1. These properties are the first point where we diverge from the underlying model for Optimal DPOR [2, Definition 3.2]. In that definition, Properties (3) and (5) need to be weakened, Property (6) needs to be replaced, whereas Property (7) was only required for Source DPOR and is thus dropped.

Definition 1 (Properties of valid happens-before relations). *A happens-before assignment, which assigns a unique happens-before relation \rightarrow_E to any execution sequence E , is valid if it satisfies the following properties for all execution sequences E :*

1. \rightarrow_E is an irreflexive partial order on $\text{dom}(E)$, which is included in $<_E$.
2. The execution steps of each process are totally ordered, i.e., $\langle p, i \rangle \rightarrow_E \langle p, i+1 \rangle$ whenever $\langle p, i+1 \rangle \in \text{dom}(E)$.
3. Given an execution sequence E and a process p s.t. $E \vdash p$, then for all events $e, e' \in \text{dom}(E)$, if $e \rightarrow_E e'$ then $e \rightarrow_{E,p} e'$.
4. Any linearization E' of \rightarrow_E on $\text{dom}(E)$ is an execution sequence which has exactly the same “happens-before” relation $\rightarrow_{E'}$ as \rightarrow_E . This means that the relation \rightarrow_E induces a set of equivalent execution sequences, all containing the same set of events, and with the same “happens-before” relation. We use:
 - $E \simeq E'$ to denote that $\text{dom}(E) = \text{dom}(E')$ and that E and E' are linearizations of the same “happens-before” relation, and
 - $[E]_{\simeq}$ to denote the equivalence class of E .
5. If $E \simeq E'$, then $\text{enabled}(s_{[E]}) = \text{enabled}(s_{[E']})$.

For the last property, we need to introduce a few definitions. Given \rightarrow_E , if $e, e' \in \text{dom}(E)$ and $e <_E e'$, define

- $e \triangleleft_E e'$ (read as e is in a race with e') to denote that $e \rightarrow_E e'$ and $\widehat{e} \neq \widehat{e}'$ and there is no event $e'' \in \text{dom}(E)$, different from e' and e , such that $e \rightarrow_E e'' \rightarrow_E e'$.
- $e \lesssim_E e'$ (read as e is in a reversible race with e') to denote that $e \triangleleft_E e'$ and in any equivalent execution sequence $E' \simeq E$ where e occurs immediately before e' , \widehat{e}' is not blocked before the occurrence of e .

Now we continue listing properties of valid happens-before relations.

6. Given an execution sequence E , then for all events $e, e' \in \text{dom}(E)$ where $e \triangleleft_E e'$, there exists a set $O = \text{observers}(e, e', E) \subseteq \text{dom}(E)$ such that:
 - (a) For all $o \in O$, it holds that $e \rightarrow_E o$, $o \neq e'$, and $o \not\rightarrow_E e'$.
 - (b) For all $o, o' \in O$ it holds that $o \not\rightarrow_E o'$.
 - (c) If $E' \simeq E$ then $O' = \text{observers}(e, e', E') = O$.
 - (d) For every prefix $E' < E$ of E such that $e, e' \in \text{dom}(E')$:
 - If O is empty, then $e \rightarrow_{E'} e'$.
 - If O is nonempty, then $e \rightarrow_{E'} e'$ iff $\text{dom}(E') \cap O \neq \emptyset$.
 - (e) If $e \lesssim_E e'$, then for all sequences w such that $E \vdash w$ and all events $e'' \in \text{dom}(E)$:
 - If $e \not\rightarrow_E e''$, then $e \not\lesssim_{E,w} e''$.
 - If $e'' \not\rightarrow_E e'$, then $e'' \not\lesssim_{E,w} e'$.
 - (f) For all $e'' \in \text{dom}(E)$ such that $e' \rightarrow_E e''$ it holds that $O \cap \text{observers}(e', e'', E) = \emptyset$.
 - (g) If $O = \{o\}$ and $E = E'.\widehat{o}$ for some o and E' , then for any $E'' \simeq E'$, either $e \rightarrow_{E''} \widehat{o}e'$ or $e' \rightarrow_{E''} \widehat{o}e$.

We give some intuition for the changed properties. Property 3 requires the happens-before assignment to maintain edges in extensions, but allows having fewer edges in prefixes. Property 5 allows execution sequences that reach different states (due to unobserved races) to be considered equivalent. Property 6 summarizes properties for races that require observers. Most requirements are intuitive. Property 6.(d) clarifies property 3: an “observed” race is included in a sequence only if some observers of the race are also included. Property 6.(e) prevents extensions to an execution sequence

from adding edges to the events of a reversible race in such a way that the race can not be reversed. Property 6.(f) prohibits an observer from creating “dependency chains”. Finally, property 6.(g) requires that an observer observes a fixed set of pairs of events in each execution sequence; a consequence of this is that whether or not some particular race is observed never depends on the ordering of some other pair of events observed by the same observer. All these properties are satisfied by “natural” happens-before assignments for events in message passing programs and most shared memory programs. Limitations include e.g., models in which the written memory regions of two write operations may overlap without being equal; such pairs of operations need to be treated as unconditionally racing.

5 Optimal DPOR with Observers

We now present a DPOR algorithm with observers that achieves optimal reduction.

In Sect. 3.2 we explained why sleep sets are not suitable when observers are used. We instead introduce a notion of redundancy based solely on the set of explored steps from each state. We will base this notion on a concept defined in Optimal DPOR.

Definition 2 (Initials and Weak Initials [2]). For an execution sequence $E.w$, the set $I_{[E]}(w)$ of processes that are initials and the set $WI_{[E]}(w)$ of processes that are weak initials are defined as follows:

1. $p \in I_{[E]}(w)$ iff there is a sequence w' such that $E.w \simeq E.p.w'$
2. $p \in WI_{[E]}(w)$ iff there are sequences w' and v such that $E.w.v \simeq E.p.w'$

Definition 3 (Redundant Sequences). For an execution sequence E and a function $done$ from prefixes of E to sets of processes, the set of sequences $redundant(E, done)$ is defined such that $v \in redundant(E, done)$ iff $E.v$ is an execution sequence and there is a partitioning $E = w.w'$ of E such that some process $p \in done(w)$ is also in $p \in WI_{[w]}(w'.v)$.

The intuition is that if $v \in redundant(E, done)$, then the execution sequence $E.v$ is equivalent to a previously explored execution sequence. In the special case where races do not need observers (i.e., the set of observers for each race is empty), we can define sleep sets in the classical sense by letting $p \in sleep(E)$ denote that E is of form $E'.v$ for some v such that $p \in done(E')$ and p and v are independent. Then $sleep(E)$ will consist of all single-process sequences in $redundant(E, done)$, and $v \in redundant(E, done)$ is equivalent to $sleep(E) \cap WI_{[E]}(v) \neq \emptyset$.

If E is an execution sequence, and v and w are sequences of processes, let:

- $v \sqsubseteq_{[E]} w$ denote that there is a sequence v' such that $E.v.v'$ and $E.w$ are execution sequences with $E.v.v' \simeq E.w$. Intuitively, $v \sqsubseteq_{[E]} w$ if, after E , the sequence v is a possible way to start an execution that is equivalent to w .
- $v \sim_{[E]} w$ denote that there are sequences v' and w' such that $E.v.v'$ and $E.w.w'$ are execution sequences with $E.v.v' \simeq E.w.w'$. Intuitively, $v \sim_{[E]} w$ if, after E , the sequence v is a possible way to start an execution that is equivalent to an execution sequence of form $E.w.w'$, and vice versa.

Let us define an *ordered tree* as a pair $\langle B, \prec \rangle$, where B (the set of *nodes*) is a finite prefix-closed set of sequences of processes, with the empty sequence $\langle \rangle$ being the root. The children of a node w , of form $w.p$ for some set of processes p , are ordered by \prec . In $\langle B, \prec \rangle$, such an ordering between children has been extended to the total order \prec on B by letting \prec be the induced post-order relation between the nodes in B . This means that if the children $w.p_1$ and $w.p_2$ are ordered as $w.p_1 \prec w.p_2$, then $w.p_1 \prec w.p_2 \prec w$ in the induced post-order.

Definition 4 (Wakeup Tree). *Let E be an execution sequence, and $done$ be a function from prefixes of E to sets of processes. A wakeup tree after $\langle E, done \rangle$ is an ordered tree $\langle B, \prec \rangle$, such that the following properties hold*

1. *No leaf w of B is redundant after E , i.e., $w \notin \text{redundant}(E, done)$;*
2. *whenever $u.p$ and $u.w$ are nodes in B with $u.p \prec u.w$, and $u.w$ is a leaf, then $p \notin WI_{[E.u]}(w)$.*

Property (2) is the same as Optimal DPOR; Property (1) has been modified.

Regarding inserting sequences in a wakeup tree, let $\langle B, \prec \rangle$ be a wakeup tree after $\langle E, done \rangle$. For any sequence w such that $w \notin \text{redundant}(E, done)$ we need an operation $insert_{[E]}(w, \langle B, \prec \rangle)$ that satisfies the following properties:

1. $insert_{[E]}(w, \langle B, \prec \rangle)$ is also a wakeup tree after $\langle E, done \rangle$,
2. any leaf of $\langle B, \prec \rangle$ remains a leaf of $insert_{[E]}(w, \langle B, \prec \rangle)$, and
3. $insert_{[E]}(w, \langle B, \prec \rangle)$ contains a leaf u with $u \sim_{[E]} w$.

The $insert_{[E]}(w, \langle B, \prec \rangle)$ operation can be implemented as follows. Let v be the smallest (w.r.t. to \prec) sequence in B such that $v \sim_{[E]} w$. If v is a leaf, $insert_{[E]}(w, \langle B, \prec \rangle)$ can leave the tree unmodified. Otherwise, let w' be a shortest sequence such that $w \sqsubseteq_{[E]} v.w'$, and add $v.w'$ as a new leaf, ordered after all already existing nodes in B of form $v.w''$.

5.1 Algorithm

Algorithm 1 is a modified and extended version of the plain Optimal DPOR algorithm [2], so that it supports observers. Since sleep sets is no longer an applicable mechanism for avoiding redundant exploration, the algorithm accepts only two arguments, E , the prefix to explore, and WuT , the initial wakeup tree after E . It keeps two global variables, wut , a mapping from execution sequences to wakeup trees, and $done$, a mapping from execution sequences to sets of processes. For a pair of events $e, e' \in \text{dom}(E)$ that are in a reversible race ($e \lesssim_E e'$) in E , the algorithm employs the following notation:

- $pre(E, e)$ denotes the prefix of E up to, but not including, the event e ,
- $notdep(e, E)$ denotes the sub-sequence of E consisting of the events that occur after e but do not “happen after” e (i.e., the events e' that occur after e such that $e \not\rightarrow_E e'$).
- $notobs(e, e', E)$ denotes the sub-sequence of E containing the events that “happen after” e , but are not observers $o \in O = \text{observers}(e, e', E)$ of the race $e \rightarrow_E e'$, nor “happen after” any such o : $notobs(e, e', E) = \langle q \parallel q \in E, e \rightarrow_E q, q \notin O, \nexists o \in O. o \rightarrow_E q \rangle$.

The first change compared to Optimal DPOR is in lines 6 to 8 which describe how to construct a wakeup sequence for an observed race, including an observer operation. Second, the test $v \in \text{redundant}(E, done)$ on line 11 replaces the test $sleep(E') \cap WI_{[E]}(v) \neq \emptyset$ at the corresponding place in Optimal DPOR. The rest of the algorithm is essentially the same, with initialization, update and propagation of sleep sets removed.

Algorithm 1: Optimal DPOR with Observers.

Initial call: $Explore(\langle \rangle, \{\langle \rangle\}, \emptyset)$

```

1 Explore(E, WuT)
2   done(E) := ∅;
3   if enabled(s[E]) = ∅ then
4     foreach e, e' ∈ dom(E) such that (e ≲E e') do
5       let E' = pre(E, e);
6       if observers(e, e', E) ≠ ∅ then
7         choose o ∈ observers(e, e', E);
8         let v = notdep(e, E). $\hat{e}$ . $\hat{e}$ .( $\text{notobs}(e, e', E) \setminus \hat{e}$ );
9       else
10        let v = notdep(e, E). $\hat{e}$ ;
11        if v ∉ redundant(E', done) then
12          wut(E') := insert[E'](v, wut(E'));
13    else
14      if WuT ≠ ⟨{⟨⟩}, ∅⟩ then
15        wut(E) := WuT;
16      else
17        choose p ∈ enabled(s[E]);
18        wut(E) := ⟨{⟨⟩, p}, {⟨p, ⟨⟩}⟩;
19      while ∃ p ∈ wut(E) do
20        let p = min⊆{p ∈ wut(E)};
21        let WuT' = subtree(wut(E), p);
22        Explore(E, p, WuT');
23        remove all sequences of form p.w from wut(E);
24        add p to done(E);

```

5.2 Correctness and Optimality

The correctness and optimality of Algorithm 1 are stated in the following theorems.

Theorem 1 (Correctness of Optimal DPOR with Observers). *Whenever a call to $Explore(E, WuT)$ returns during Algorithm 1, then for all maximal execution sequences $E.w$, the algorithm has explored some execution sequence E' which is in $[E.w]_{\simeq}$.*

Since the initial call to the algorithm uses the arguments $Explore(\langle \rangle, \{\langle \rangle\}, \emptyset)$, Theorem 1 implies that for all maximal execution sequences E the algorithm explores some execution sequence E' which is in $[E]_{\simeq}$ (Appendix A contains the full proof).

Theorem 2 (Optimality of Optimal DPOR with Observers). *Algorithm 1 never explores two maximal execution sequences which are equivalent.*

If Algorithm 1 is not at the end of a maximal sequence, it will continue exploring the scheduling either by using information from a wakeup tree (line 15) or by choosing an arbitrary enabled process (line 18). Theorem 2 ensures that all maximal execution sequences reached are non redundant (Appendix B contains the proof).

6 Implementations

We have implemented Algorithm 1 in two SMC tools: Nidhugg and Concuerror.

Observers in Nidhugg. Nidhugg [1] is a stateless model checking tool for shared-memory pthreads programs written in C or C++ that operates by interpreting LLVM IR. Nidhugg can test programs also under relaxed memory models (TSO, PSO, and Power), but in this paper we will limit ourselves to testing programs under Sequential Consistency.

In the context of shared memory, the observers extension was used to make races between writes to the same memory location conditional on the existence of a read of that memory location that “observes” those writes. In order to add the observers extension to Nidhugg, the tool was first extended to support Optimal DPOR, as it previously only implemented Source DPOR, which is not easily extended with observers, as discussed in Sect. 3.2. The tool now records symbolic representations of program events that contain enough information to reconstruct the happens-before relation induced by a particular execution. For Source DPOR, these symbolic events are unnecessary if the happens-before relation is stored in vector clocks [18], as it is in Nidhugg. For Optimal DPOR, symbolic events are the most reasonable way to implement tests that check whether a given process is a weak initial of some sequence, which is needed for both the redundancy check and wakeup tree insertion.

To extend this implementation with observers, symbolic events for writes were extended with an “observed”-flag, which is unset until a read that reads the value written by that write is executed. At the end of the execution, we compute the vector clocks of the happens-before relation, only considering two write events to the same memory location as interfering if at least one of them has the “observed”-flag set. Then, Optimal DPOR was modified as described in Sect. 5.1. The check whether a wakeup sequence is redundant on line 11 is implemented using sleep sets extended with processes conditionally sleeping unless an address is read, and a set of addresses that must be read, without intervening writes, before the end of the program.

Observers in Concuerror. Concuerror [8] is a stateless model checking tool for Erlang, a functional programming language based on the actor model of concurrency [4]. In Erlang, actors are realized by language-level processes implemented by the runtime system instead of being directly mapped to OS threads. Each Erlang process communicates with other processes via asynchronous message passing. Messages are placed in the mailbox of the receiving process in the order they are delivered. A process can consume messages using *selective receive*, which is a *blocking* operation when the mailbox does not contain any matching message, unless a timeout clause is specified. If multiple messages can match, the oldest message is picked from the mailbox.

Concuerror already implemented Optimal DPOR, but treated any two message deliveries to the same mailbox as interfering. With the extension, Concuerror uses receives as observers of sends. When examining a complete scheduling, an extra pass is performed, annotating each message delivery event with the patterns that were used in the receive that picked the message (if present) and the receive order. If the message of a later delivery matches any of the pattern annotations of an earlier delivery, the deliveries interfere. The *notobs* sequence is constructed from all the events that lead up to the corresponding receive (which is the observer), excluding events in the *notdep* sequence. Because the resulting wakeup sequence contains fewer events, observer information is recomputed, and then all the earlier *done* sets are checked for weak initials of the wakeup sequence, exactly as described in Algorithm 1.

7 Experimental Results

We report experimental results that compare the performance of two algorithms: Optimal DPOR (denoted in the tables as “optimal”) and Optimal DPOR with Observers (denoted as “observers”). We ran all benchmarks on a desktop with an i7-3770 CPU (3.40 GHz) with 16 GB of RAM running Debian 4.12.0-2-amd64 and LLVM 3.8.1. The machine has four physical cores, but presently both tools use only one of them.

Observers in Nidhugg. Table 1 shows the effect of observers on shared memory C/threads programs. We used two kinds of programs: (1) synthetic benchmarks similar to those of Sect. 2, and (2) programs from SV-COMP and/or from “similar” papers. We report the number of traces that the two algorithms explore, the time it takes to explore them, and the memory used (although this number is not interesting for an SMC tool).

Table 1: Performance of Optimal DPOR vs. Optimal DPOR with Observers in Nidhugg.

Benchmark	Traces Explored		Time		Memory	
	optimal	observers	optimal	observers	optimal	observers
lastwrite(2)	2	2	<0.1s	<0.1s	10MB	10MB
lastwrite(7)	5040	7	0.5s	<0.1s	10MB	10MB
lastwrite(8)	40320	8	5.2s	<0.1s	10MB	10MB
lastwrite(9)	362880	9	52.0s	<0.1s	10MB	10MB
floating_read(2)	6	5	<0.1s	<0.1s	10MB	10MB
floating_read(6)	5040	193	0.5s	<0.1s	11MB	10MB
floating_read(7)	40320	449	5.0s	<0.1s	11MB	11MB
floating_read(8)	362880	1025	53.3s	0.2s	11MB	11MB
apr_1	1145	1145	4.8s	5.0s	19MB	20MB
fib	218243	218243	18.9s	20.1s	11MB	11MB
lamport(2)	16+16	14+12	<0.1s	<0.1s	10MB	10MB
lamport(3)	9216+11525	5466+6132	4.0s	2.7s	11MB	11MB

lastwrite(n). A synthetic program where n threads write to a shared variable x and a single process first joins (awaits the termination of) the writing threads, and then reads that variable.

floating_read(n). A synthetic program where n threads write to a shared variable x and a single process reads that variable without waiting for the writing processes to exit.

apr_1. A benchmark adapted from the sources of the Apache Portable Runtime library version 1.5.1. Also used in [1], there called apr_1.c. (Here, no loop bounding was applied.)

fib. A benchmark from SV-COMP, also used in [1] where it was called fib_true.c.

lamport(n). This standard benchmark has n worker processes acquiring a mutex implemented by Lamport’s second fast mutual exclusion protocol [16] and immediately releasing it. We show it for 2 and 3 processes which are the only sizes that are both non-trivial and tractable.

For lastwrite(n), we see a reduction in the number of interleavings explored from $n!$ to n , as explained in Sect. 2. For floating_read(n), optimal shows the predicted $(n+1)!$ interleavings, and for $n=2$, observers reduce the interleaving count from 6 to 5 as expected. In general, the benchmark has $n \times 2^{n-1} + 1$ interleavings with observers. Notice that any technique that differentiates equivalence classes by the partial order of program steps must explore at least as many interleavings or violate Property 4. The next

two programs (`apr_1` and `fib`) are examples of programs for which observers have no effect. We see that the extra overhead is very moderate for both programs.

In the last benchmark (`lamport`), we see that observers improve performance. As Nidhugg does not implement `await` statements (which are used by `lamport`), it emulates these with `assumes`. In such cases, Nidhugg might explore some traces in which these assumptions are violated. We list those traces separately, so for this benchmark the “Traces Explored” columns show $a + b$ entries, which means that Nidhugg explored $a + b$ traces but b of those times an `assume` statement was violated.

Observers in Concuerror. Table 2 shows the effect of observers in message passing programs; we omit memory used, as both algorithms have similar requirements.

Table 2: Comparison of Optimal DPOR vs. Optimal DPOR with Observers in Concuerror.

Benchmark	Traces Explored		Time		Benchmark	Traces Explored		Time	
	optimal	observers	optimal	observers		optimal	observers	optimal	observers
<code>not_selective(2)</code>	2	2	<1.0s	<1.0s	<code>lock(3)</code>	30	6	0.9s	0.9s
<code>not_selective(6)</code>	720	720	1.7s	1.8s	<code>lock(4)</code>	336	24	1.4s	0.9s
<code>not_selective(7)</code>	5040	5040	6.0s	6.8s	<code>lock(5)</code>	5040	120	9.0s	1.3s
<code>not_selective(8)</code>	40320	40320	48.0s	56.0s	<code>lock(6)</code>	95040	720	3m27s	2.6s
<code>selective(2)</code>	2	1	<1.0s	<1.0s	<code>poolboy</code>	746	265	6.6s	4.0s
<code>selective(6)</code>	720	1	1.8s	<1.0s	<code>gproc</code>	1168	784	12.7s	10.0s
<code>selective(7)</code>	5040	1	6.3s	<1.0s	<code>corfu-repair</code>	92750496	3864604	1022h	52h
<code>selective(8)</code>	40320	1	51.0s	<1.0s					

`not_selective(n)`. n processes send messages to a process that can receive any message sent to it. `selective(n)`. This is a generalized version of the last example of Sect. 2. A process uses pattern matching to choose between messages from n different senders. Code is given in Appendix C. `lock(n)`. This is a program in which n workers acquire and release a lock simulated by an Erlang process. When using observers, it has $n!$ schedulings. Without observers the number of schedulings is higher. Its code is also given in Appendix C.

`poolboy`. A benchmark created from a unit test of a worker pool library [2].

`gproc`. A benchmark created from a library implementing an extended process dictionary [2].

`corfu-repair`. A program that verifies the correctness of a repair protocol of CORFU, a distributed database using a variant of Chain Replication. From a paper [5] that motivated our work.

The two benchmarks on the left sub-table confirm the behaviour we expect. When receives are not selective, the number of traces explored by both algorithms is $n!$. With selective receive (`selective` benchmark) observers explore only one trace.

The first program on the right sub-table (`lock`) is originally a shared-memory program that when translated to Erlang simulate locks using message passing. To acquire the lock, a process sends a message with its identifier to the “lock process” and then waits for a reply. Upon receiving the `acquire` message, the lock process uses the identifier to reply and then waits for a `release` message. Other `acquire` messages become queued in the mailbox of the lock process. Upon receiving the `release` message, the lock process loops back to the start, retrieving the next `acquire` message and notifying the next process. Notice that, without observers, the delivery of the `release` message of a process interferes (redundantly) with the delivery of `acquire` messages of other

processes, unlike acquire operations on true locks which cannot be executed before a release operation (such messages were treated exceptionally in the evaluation of Optimal DPOR). Observers remove the need for special handling: the receive statements are enough to precisely determine which pairs of send operations are interfering.

The next two table rows (poolboy and gproc) show results from “real” Erlang programs. We see that observers provide a moderate reduction in both the number of traces that need to be explored as well as in time.

Finally, the last program (corfu-repair) is the one that triggered this work. As can be seen in the table, observers allow Concuerror to complete SMC in a bit more than two days, while without observers the tool needs to explore exactly 24 times as many traces, taking more than 42 days to finish.

8 Related Work

POR techniques have been continuously evolving w.r.t. how they determine interference. Refining the conditions under which higher-level operations interfere has been shown to have significant impact, regardless of whether the states in which such operations are executed is also a parameter or not [13]. In this work, we have extended this idea, parameterizing the interference between operations using distinct observer operations.

DPOR techniques have also been extended to take advantage of special properties of the underlying concurrency model. For the actor model, the transitivity of the dependency relation for send operations has been exploited to defer early planning of interleavings [21]. This improvement is orthogonal with Optimal DPOR (and with our extension), as it reduces the number of wakeup sequences that are added “early” in an exploration. For event-driven systems, it has been shown [17] that two post operations to an event dispatch queue need not be considered dependent: reordering of such operations can be decided later, upon detection of interference between other operations within the respective event handlers. However, this treatment applies only under a specific interpretation of ‘message passing’ that exploits additional semantic structure of an actor’s mailbox. Our technique is applicable to a wider spectrum of programs.

Context-Sensitive DPOR [3] uses an external procedure to decide whether alternative schedulings would lead to identical states and, like Optimal DPOR with observers, is also able to achieve exponential reduction in certain cases. However, since it needs to compare states, it is an inherently stateful technique, in contrast to our technique that inspects only one trace at a time to lazily construct reversible races.

Data-Centric DPOR (DC-DPOR) [7] is an SMC technique that explores a related but different notion of observability. It defines two executions to be equivalent if each read reads from (“observes”) the same write in both executions. In contrast, our notion of observability is based on observing *interference of operations*, not just individual writes. DC-DPOR’s resulting equivalence relation is coarser than ours, which is based on Mazurkiewicz traces. However, DC-DPOR is optimal only for programs with acyclic communication graphs, while being non-optimal otherwise. Also, DC-DPOR models message passing using locks and shared memory, which at best gives as few traces as Optimal DPOR gives without the improvements presented in this paper.

9 Concluding Remarks

In this paper we presented an extension to the Optimal DPOR algorithm for SMC that uses observability to refine which operations are considered as interfering. We described the challenges and motivated the necessary modifications, gave a formal description of the algorithm and the theory behind it and reported on two implementations in SMC tools, demonstrating that Optimal DPOR with Observers can achieve significantly better reduction in both shared memory and message passing programs.

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Data Availability Statement. The versions of Nidhugg and Concuerror, as well as all the programs we used to obtain the experimental results of Tables 1 and 2 are available in the Figshare repository [6]. Also included in the artifact are instructions on how to use it to reproduce the results reported in this paper. As per the TACAS 2018 submission rules, the artifact is designed for use with the TACAS 2018 Artifact Evaluation Virtual Machine [14], although, as source code is included, it can probably be used on any Linux platform. We refer to the documentation of the respective tool on how to compile them from source code; the tools may of course evolve over time, but the way to build them will not change significantly.

References

1. Abdulla, P., Aronis, S., Atig, M.F., Jonsson, B., Leonardsson, C., Sagonas, K.: Stateless model checking for TSO and PSO. In: Baier, C., Tinelli, C. (eds.) Tools and Algorithms for the Construction and Analysis of Systems - 21st International Conference, TACAS 2015. Proceedings. LNCS, vol. 9035, pp. 353–367. Springer, Berlin Heidelberg (2015), http://dx.doi.org/10.1007/978-3-662-46681-0_28
2. Abdulla, P.A., Aronis, S., Jonsson, B., Sagonas, K.: Source sets: A foundation for optimal dynamic partial order reduction. *Journal of the ACM* 64(4), 25:1–25:49 (Sep 2017), <http://doi.acm.org/10.1145/3073408>
3. Albert, E., Arenas, P., de la Banda, M.G., Gómez-Zamalloa, M., Stuckey, P.J.: Context-sensitive dynamic partial order reduction. In: Computer Aided Verification, 29th International Conference, CAV 2017, Proceedings, Part I. LNCS, vol. 10426, pp. 526–543. Springer, Berlin Heidelberg (2017), https://doi.org/10.1007/978-3-319-63387-9_26
4. Armstrong, J.: Erlang. *Communications of the ACM* 53(9), 68–75 (Sep 2010), <http://doi.acm.org/10.1145/1810891.1810910>
5. Aronis, S., Fritchie, S.L., Sagonas, K.: Testing and verifying chain repair methods for CORFU using stateless model checking. In: Polikarpova, N., Schneider, S. (eds.) Integrated Formal Methods, 13th International Conference, IFM 2017, Proceedings. LNCS, vol. 10510, pp. 227–242. Springer, Berlin Heidelberg (2017), https://doi.org/10.1007/978-3-319-66845-1_15
6. Aronis, S., Jonsson, B., Lång, M., Sagonas, K.: Binary Artifact for TACAS-2018 paper "Optimal DPOR with Observers". Figshare (Feb 2018), <https://doi.org/10.6084/m9.figshare.5918701>

7. Chalupa, M., Chatterjee, K., Pavlogiannis, A., Sinha, N., Vaidya, K.: Data-centric dynamic partial order reduction. *Proc. ACM on Program. Lang.* 2(POPL), 31:1–31:30 (Dec 2017), <http://doi.acm.org/10.1145/3158119>
8. Christakis, M., Gotovos, A., Sagonas, K.: Systematic testing for detecting concurrency errors in Erlang programs. In: Sixth IEEE International Conference on Software Testing, Verification and Validation (ICST 2013). pp. 154–163. IEEE Computer Society, Los Alamitos, CA, USA (2013), <https://doi.org/10.1109/ICST.2013.50>
9. Clarke, E.M., Grumberg, O., Minea, M., Peled, D.: State space reduction using partial order techniques. *International Journal on Software Tools for Technology Transfer* 2(3), 279–287 (Nov 1999), <http://dx.doi.org/10.1007/s100090050035>
10. Flanagan, C., Godefroid, P.: Dynamic partial-order reduction for model checking software. In: Proceedings of the 32nd ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages. pp. 110–121. POPL '05, ACM, New York, NY, USA (2005), <http://doi.acm.org/10.1145/1040305.1040315>
11. Godefroid, P.: Partial-Order Methods for the Verification of Concurrent Systems: An Approach to the State-Explosion Problem. Ph.D. thesis, University of Liège (1996), <http://www.springer.com/gp/book/9783540607618>, also, volume 1032 of LNCS, Springer.
12. Godefroid, P.: Model checking for programming languages using VeriSoft. In: Proceedings of the 24th ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages. pp. 174–186. POPL '97, ACM, New York, NY, USA (1997), <http://doi.acm.org/10.1145/263699.263717>
13. Godefroid, P., Pirotin, D.: Refining dependencies improves partial-order verification methods. In: Courcoubetis, C. (ed.) *Computer Aided Verification*. LNCS, vol. 697, pp. 438–449. Springer, Berlin, Heidelberg (1993), https://doi.org/10.1007/3-540-56922-7_36
14. Hartmanns, A., Wendler, P.: TACAS 2018 Artifact Evaluation VM. Figshare (2018), <https://doi.org/10.6084/m9.figshare.5896615>
15. Lamport, L.: Time, clocks and the ordering of events in a distributed system. *Comm. of the ACM* 21(7), 558–565 (Jul 1978), <http://doi.acm.org/10.1145/359545.359563>
16. Lamport, L.: A fast mutual exclusion algorithm. *ACM Trans. on Computer Systems* 5(1), 1–11 (Jan 1987), <http://doi.acm.org/10.1145/7351.7352>
17. Maiya, P., Gupta, R., Kanade, A., Majumdar, R.: Partial order reduction for event-driven multi-threaded programs. In: *Tools and Algorithms for the Construction and Analysis of Systems*. LNCS, vol. 9636, pp. 680–697. Springer, Berlin, Heidelberg (2016), http://dx.doi.org/10.1007/978-3-662-49674-9_44
18. Mattern, F.: Virtual time and global states of distributed systems. In: Cosnard, M., et al. (eds.) *Proceedings of the Workshop on Parallel and Distributed Algorithms*. pp. 215–226. North-Holland / Elsevier (1989)
19. Mazurkiewicz, A.: Trace theory. In: Brauer, W., Reisig, W., Rozenberg, G. (eds.) *Petri Nets: Applications and Relationships to Other Models of Concurrency*. LNCS, vol. 255, pp. 279–324. Springer, Berlin Heidelberg (1987), http://dx.doi.org/10.1007/3-540-17906-2_30
20. Peled, D.: All from one, one for all, on model-checking using representatives. In: Courcoubetis, C. (ed.) *Computer Aided Verification*. LNCS, vol. 697, pp. 409–423. Springer-Verlag, London, UK (1993), https://link.springer.com/chapter/10.1007%2F3-540-56922-7_34
21. Tasharofi, S., Karmani, R.K., Lauterburg, S., Legay, A., Marinov, D., Agha, G.: TransDPOR: A novel dynamic partial-order reduction technique for testing actor programs. In: Giese, H., Rosu, G. (eds.) *Formal Techniques for Distributed Systems*. LNCS, vol. 7273, pp. 219–234. Springer, Berlin Heidelberg (2012), https://link.springer.com/chapter/10.1007/978-3-642-30793-5_14
22. Valmari, A.: Stubborn sets for reduced state space generation. In: Rozenberg, G. (ed.) *Advances in Petri Nets 1990*. LNCS, vol. 483, pp. 491–515. Springer-Verlag, London, UK (1991), https://link.springer.com/chapter/10.1007/3-540-53863-1_36

A Proof of Correctness

In this appendix, we prove the correctness of Theorem 1. We repeat its formulation here.

Theorem 1. Whenever a call to $Explore(E, WuT)$ returns during Algorithm 1, then for all maximal execution sequences $E.w$, the algorithm has explored some execution sequence E' which is in $[E.w]_{\simeq}$.

The proof begins by establishing some lemmas about the definitions in Section ??.

Lemma 1. For an execution sequence $E.v$ and a process p , we have:

1. If $v.p \in \text{redundant}(E, \text{done})$ then $v \in \text{redundant}(E, \text{done})$;
2. If $\langle \rangle \notin \text{redundant}(E, \text{done})$, then $\text{redundant}(E, \text{done}) = \emptyset$.

Proof. The first property follows from an analogous property of weak initials, and the second property follows from the first. \square

Lemma 2. For a process p , an execution sequence $E.p.v$, and a function done from prefixes of $E.p$ to sets of processes, if $\text{done}(E.p) = \emptyset$, then if $v \in \text{redundant}(E.p, \text{done})$, also $p.v \in \text{redundant}(E, \text{done})$.

Proof. Since $v \in \text{redundant}(E.p, \text{done})$, there exists a partitioning $E.p = E'.w$ and a process $q \in \text{done}(E')$ such that $q \in WI_{[E']}(w.v)$. Since $\text{done}(E.p) = \emptyset$, E' must not be $E.p$, and thus $p \in w$. Therefore, we can write w as $w'.p$ and have that $q \in WI_{[E']}(w'.p.v)$. But then the partitioning $E = E'.w'$ and process q satisfy the definition of $p.v \in \text{redundant}(E, \text{done})$. \square

Lemma 3. If $E.p.v.q.w$ is an execution sequence such that $\text{next}_{[E]}(p) \lesssim_{E.p.v.q.w} \text{next}_{[E.p.v]}(q)$, then there exists an execution sequence $E.p.v'.q.w \simeq E.p.v.q.w$ such that for all execution sequences $E.p.v'.q.w.w'$, $\text{next}_{[E]}(p) \lesssim_{E.p.v'.q.w.w'} \text{next}_{[E.p.v]}(q)$.

Proof. A satisfying v' can be constructed in the following way: Partition the events that comprise v into the following disjoint sets; the events that happen-before $\text{next}_{[E.p.v]}(q)$, the events that neither happen-before $\text{next}_{[E.p.v]}(q)$ nor happen-after $\text{next}_{[E]}(p)$, and the events that happen-after $\text{next}_{[E]}(p)$. The sets are disjoint since $\text{next}_{[E]}(p) \lesssim_{E.p.v.q.w} \text{next}_{[E.p.v]}(q)$. Then construct v' by concatenating three subsequences of events from v that are in the first, second, and third sets respectively. This construction cannot have violated any edge in $\rightarrow_{E.p.v.q.w}$, and so $E.p.v'.q.w \simeq E.p.v.q.w$.

Now, the only way for an extension w' to create a nondirect path from $\text{next}_{[E]}(p)$ to $\text{next}_{[E.p.v]}(q)$ in $\rightarrow_{E.p.v'.q.w.w'}$ is to add an edge from $\text{next}_{[E]}(p)$ to an event in the first set or to $\text{next}_{[E.p.v]}(q)$ from an event in the last set, but either is forbidden by Property 6.(e). \square

Let us now prove the correctness of Algorithm 1. The proof will follow the same structure as the proof for Optimal DPOR in [2]. Throughout, we assume a particular completed execution of Optimal DPOR for observers. This execution consists of a number of terminated calls to $Explore(E, WuT)$ for some values of the parameters E and WuT . Let \mathcal{E} denote the set of execution sequences E that have been explored in some call

$Explore(E, \cdot)$. Define the ordering \prec on \mathcal{E} by letting $E \prec E'$ if $Explore(E, \cdot)$ returned before $Explore(E', \cdot)$. Intuitively, if one were to draw an ordered tree that shows how the exploration has proceeded, then \mathcal{E} would be the set of nodes in the tree, and \prec would be the post-order between nodes in that tree.

We begin by establishing some useful invariants.

Lemma 4. *Whenever Algorithm 1 is inside a call $Explore(E, WuT)$, then the following invariants hold:*

1. $wut(E)$ is a wakeup tree after $\langle E, done \rangle$,
2. if WuT is empty, then $redundant(E, done) = \emptyset$; no execution sequence starting with E is redundant.

This invariant is equivalent to Lemma 7.1 in [2], and is proven in the same way.

Proof. We establish these two invariants jointly, by induction over the steps of the algorithm. The properties hold at the beginning of the initial call, since $done(E)$ and $wut(E)$ are both empty. We verify that each property is preserved by the steps of the algorithm.

1. We need to consider the following cases.
 - Whenever $wut(E)$ is updated at line 12, it follows by Property 1 of the operation $insert_{[E]}(v, wut(E))$ that $wut(E)$ remains a wakeup tree after $\langle E, done \rangle$.
 - Consider a new call to $Explore(E.p, WuT')$ at line 22, which occurs inside a call $Explore(E, WuT)$, and where WuT' are obtained from the current value of $wut(E)$ by $WuT' = subtree(wut(E), p)$, at line 21. Since the parameter WuT' will be used to initialize $wut(E.p)$ (at line 15), we must check that WuT' is a wakeup tree after $\langle E.p, done \rangle$. By the inductive hypothesis, $wut(E)$ is a wakeup tree after $\langle E, done \rangle$. We establish that WuT' is a wakeup tree after $\langle E.p, done \rangle$ by checking the two properties of Definition 4. Let $WuT' = \langle B', \prec' \rangle$ and let $wut(E) = \langle B, \prec \rangle$.
 - (a) Any leaf w of B' is also a leaf $p.w$ of B . Since $done(E.p) = \emptyset$ (as initialised on line 2) and $p.w \notin redundant(E, done)$, the contrapositive of Lemma 2 implies $w \notin redundant(E.p, done)$.
 - (b) Let $u.q$ and $u.w$ be nodes in B' with $u.q \prec' u.w$, and $u.w$ is a leaf. Then $p.u.q$ and $p.u.w$ are nodes in B with $p.u.q \prec p.u.w$, and $p.u.w$ a leaf. By the inductive hypothesis, it follows that $q \notin WI_{[E.p.u]}(w)$, which is precisely what we must establish for the inductive step.
 If $wut(E.p)$ is initialized at line 18 instead of at line 15, then this initialization is performed in a call of form $Explore(E.p, WuT')$ where WuT' is the empty wakeup tree. Since $\{\langle \rangle, p\}, \{(p, \langle \rangle)\}$ is trivially a wakeup tree after $\langle E.p, done \rangle$ where $done(E.p) = \emptyset$, the property follows.
 - Consider the update to $wut(E)$ at line 23. Since it is only deleting from the wakeup tree, the property is trivially preserved.
 - Consider the update to $done(E)$ and $wut(E)$ at lines 23 and 24. We must prove that they preserve the two properties of Definition 4.
 - (a) Since the update removes nodes from $wut(E)$, and p is added to $done(E)$, it suffices to check that $p \notin WI_{[E]}(w)$ whenever w is a leaf of $wut(E)$ that remains after the operation, i.e., w is not of form $p.w'$. Before the operation,

- it was the case that p and w were nodes of $wut(E)$ with $p \prec w$ and w a leaf. From the inductive hypothesis, Property (2) of Definition 4 implies that $p \notin WI_{[E]}(w)$, which completes the proof of this case.
- (b) Since $wut(E)$ is modified by removing a branch from the root, this property is preserved.
2. Consider a new call to $Explore(E.p, WuT')$ at line 22. If WuT' is empty, then p must have been a leaf in $wut(E)$ just before the call. By Property 1 of the inductive hypothesis, $wut(E)$ was then a wakeup tree after $\langle E, done \rangle$, therefore (by Property (1) of Definition 4), we have $p \notin redundant(E, done)$, which by Lemma 1 implies that $redundant(E.p, done) = \emptyset$. □

Now we will proceed to prove Theorem 1 which was stated in Sect. 5.2.

Proof (of Theorem 1). By induction on the set of execution sequences E that are explored during the considered execution, using the ordering ∞ (i.e., the order in which the corresponding calls to $Explore$ returned).

Base Case: This case corresponds to the first sequence E for which the call $Explore(E, \cdot, \cdot)$ returns. By the algorithm, E is already maximal, so the theorem trivially holds.

Inductive Step: Let us assume that there exist values E and WuT , such that when the call to $Explore(E, WuT)$ returns, there is a maximal sequence $E.w$ such that the algorithm has not explored any execution sequence E' in $[E.w]_{\simeq}$. We will show that this leads to a contradiction.

Note that for such w to exist, E cannot be maximal, so $done(E)$ contains at least one process. For processes $p \in redundant(E, done)$, define

- E'_p , such that $E'_p \leq E$, $E'_p.p \in \mathcal{E}$, and $E'_p.p$ is the last execution sequence of this form that precedes E (w.r.t. ∞). If $p \in done(E)$ then $E'_p = E$, otherwise E'_p is a strict prefix of E .
- w'_p , as $E = E'_p.w'_p$.

Inductive Hypothesis: The theorem holds for all execution sequences E' with $E' \infty E$.

Claim 1. $w \notin redundant(E, done)$.

Proof. By contradiction. Assume that there is a partitioning $v.v' = E$ such that there is a $p \in (WI_{[v]}(v'.w) \cap done(v))$. Since $E.w$ is maximal, $p \in WI_{[v]}(v'.w)$ implies $p \in I_{[v]}(v'.w)$, therefore from Definition 2.1 there is a w'' such that $v.v'.w \simeq v.p.w''$. By the inductive hypothesis applied to $v.p$, the algorithm has explored some execution sequence in $[v.p.w'']_{\simeq} = [E.w]_{\simeq}$, which contradicts the main assumption about w . □

For processes $p \in redundant(E, done)$, also choose

- a sequence w_p as a longest prefix of any w' such that $E.w \simeq E.w'$ and $p \in WI_{[E'_p]}(w'_p.w_p)$,
- o_p as the first event in $dom_{[E]}(w')$ which is not in w_p . Such an event o_p must exist, otherwise $w_p = w'$, which implies $p \in WI_{[E'_p]}(w'_p.w')$, which implies $p \in WI_{[E'_p]}(w'_p.w)$, which implies $w \in redundant(E, done)$, which contradicts Claim 1.

Now, choose

- $t \in \text{redundant}(E, \text{done})$ as an arbitrary process with a longest w_t .

Claim 2. $w_t.o_t \notin \text{redundant}(E, \text{done})$

Proof. By contradiction. Assume $w_t.o_t \in \text{redundant}(E, \text{done})$. Then there exists some process r such that $r \in WI_{[E'_t]}(w'_r.w_t.o_t)$, which means that some extension of $w_t.o_t$ was a valid choice for w_r . But that means that w_r must be longer than w_t , which contradicts the definition of t . \square

Finally,

- choose w''_t as a longest sequence such that $E.w_t \vdash w''_t$, and $w_t.w''_t \in \text{redundant}(E, \text{done})$, and such that there exists a process r such that $E.w_t.w''_t \vdash r$ and $w_t.w''_t.r \notin \text{redundant}(E, \text{done})$. Such a sequence must exist, as $w''_t = \langle \rangle$ and $r = \widehat{o}_t$ trivially satisfies.
- Define Q as the set of processes r such that $E.w_t.w''_t \vdash r$ and $w_t.w''_t.r \notin \text{redundant}(E, \text{done})$.
- Choose an arbitrary process $q \in \text{redundant}(E, \text{done})$, such that $q \in WI_{[E'_q]}(w'_q.w_t.w''_t)$. Such a process must exist because $w_t.w''_t \in \text{redundant}(E, \text{done})$ by construction.

For the rest of the proof, we will distinguish two cases, distinguished by the type of race we will consider. In the first case, we will consider a race with an empty observer set, and in the second case a race with observers. Mathematically, the cases are distinguished by whether $q \in w'_q.w_t.w''_t$. Both cases will conclude in a contradiction, and will together prove Theorem 1.

Case 1 (Independent race). Assume that $q \notin w'_q.w_t.w''_t$.

We will now define the sequence w_R , which will serve later in the proof as a wakeup sequence found while exploring the $E'_q.q$ subtree.

- Pick an arbitrary process $s \in Q$. It must be the case that $E.w_t.w''_t \not\vdash q \diamond s$; otherwise $w''_t.q$ would have been a longer choice of w''_t , with s having the satisfying property of r from the definition of w''_t .
- Define w_R as $w_t.w''_t.s.q$.
Note that s is the last event in w_R such that $s \rightarrow_{E.w_R} \text{next}_{[E'_q]}(q)$.

Claim 3. $w_R \notin \text{redundant}(E, \text{done})$

Proof. By contradiction. Assume $w_R \in \text{redundant}(E, \text{done})$. Then there exists some process r such that $r \in WI_{[E'_r]}(w'_r.w_R)$, but then also $r \in WI_{[E'_r]}(w'_r.w_t.w''_t.s)$. Since $s \in Q$, and by definition $w_t.w''_t.s \notin \text{redundant}(E, \text{done})$, then $r \notin WI_{[E'_r]}(w'_r.w_t.w''_t.s)$. We have a contradiction. \square

Note that $E'_q.w_t.w''_t.q.s$ is an execution sequence with the same domain as $E.w_R$ where s and $\text{next}_{[E'_q]}(q)$ are reversed but all other edges of $\rightarrow_{E.w_R}$ are preserved.

Note that since $q \in I_{[E'_q]}(w_t.w''_t.q)$, by the construction of w_R , we have $E'_q.q.w_t.w''_t.s \simeq E'_q.w_t.w''_t.q.s$. Since $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.w_t.w''_t.q.s} s$ by construction, then also $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.q.w_t.w''_t.s} s$.

By Lemma 3 we receive an execution sequence $E'_q.q.E''' .s \simeq E'_q.w_t.w'_t.q.s$ such that for any $E'_q.q.E''' .s \vdash w''$, we still have $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.q.E''' .s.w''} s$.

By the inductive hypothesis for $E'_q.q$ applied to $E'_q.q.E''' .s.w''$ for some arbitrary $E'_q.q.E''' .s \vdash w''$, the algorithm has explored some sequence $E'_q.q.z$ in $[E'_q.q.E''' .s.w'']_{\simeq}$. While exploring $E'_q.q.z$, the algorithm will encounter the race $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.q.z} s$. When handling it,

- E in the algorithm will correspond to $E'_q.q.z$ in this proof,
- e in the algorithm will correspond to $\text{next}_{[E'_q]}(q)$ in this proof, and
- e' in the algorithm will correspond to s in this proof.
- Let $x = \text{notdep}(\text{next}_{[E'_q]}(q), E'_q.q.z)$.

Since $\text{next}_{[E'_q]}(q)$ and s are independently racing then the algorithm will go to line 10 and $v = x.\hat{s}$ will be the sequence v in Algorithm 1.

Now, let $x' = \text{notdep}(\text{next}_{[E'_q]}(q), E'_q.q.E''' .s.w'')$ and $v' = x'.\hat{s}$.

We will define a sequence v'' which has the same domain as v , and the same happens-before order $\rightarrow_{E'_q.v''}$, except that the edge between s and $\text{next}_{[E'_q]}(q)$ is reversed. Define $v'' = x.q$.

We will show that $E'_q.v''$ is equivalent to $E'_q.q.w_t.w'_t.s$ and from that derive that $E'_q.v$ is equivalent to $E.w_R$.

Claim 4. $\text{dom}_{[E'_q]}(v) = \text{dom}_{[E'_q]}(w'_q.w_R)$.

Proof. We have that $\text{dom}_{[E'_q]}(w'_q.w_R) = \text{dom}_{[E'_q]}(q.E''' .s)$

By the construction of w_t , everything in $\text{dom}_{[E'_q.q.E''' .s]}(w'')$ conflict with s , and can therefore not be included in x' . Conversely, since only s happens after $\text{next}_{[E'_q]}(q)$ in $E'_q.q.E''' .s$ (by construction), by property 6.(f) nothing outside of w'' happen after $\text{next}_{[E'_q]}(q)$ in $E'_q.q.E''' .s.w''$, and thus $x' = E'''$ and $\text{dom}_{[E'_q]}(q.E''' .s) = \text{dom}_{[E'_q]}(v')$.

Finally, since $E'_q.v \simeq E'_q.v'$, we have $\text{dom}_{[E'_q]}(v) = \text{dom}_{[E'_q]}(w'_q.w_R)$. \square

Claim 5. $E'_q.v'' \simeq E'_q.q.w_t.w'_t.s$.

Proof. Since $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.q.E''' .s.w''} s$, it is obvious that $v'' \sqsubseteq_{[E'_q]} q.z$, therefore, since $E'_q.q.E''' .s.w'' \simeq E'_q.q.z$, we also have $v'' \sqsubseteq_{[E'_q]} q.E''' .s.w''$, and then using claim 4, we have $v'' \sqsubseteq_{[E'_q]} q.E''' .s$ and moreover $E'_q.v'' \simeq E'_q.q.E''' .s$. Since $E'_q.q.E''' .s \simeq E'_q.q.w_t.w'_t.s$, we have $E'_q.v'' \simeq E'_q.q.w_t.w'_t.s$. \square

Lemma 5. $E'_q.v \simeq E.w_R$.

Proof. Since v is the same as v'' but with $\text{next}_{[E'_q]}(q)$ and s reversed, and $E'_q.w_R$ is the same as $E'_q.q.w_t.w'_t.s$ but with $\text{next}_{[E'_q]}(q)$ and s reversed, claim 5 implies $E'_q.v \simeq E.w_R$. \square

From Lemma 5 and $w_R \notin \text{redundant}(E, \text{done})$, we conclude that the test on line 11 must have succeeded and that the algorithm will have inserted v into the wakeup tree. Since E is the first branch compatible with $E'_q.v \simeq E.w_R$, wakeup tree insertion will

have ensured that we will explore some sequence startable with $E.w_R$ after E . However, $w_R \notin \text{redundant}(E, \text{done})$ means that none of the continuations of E that the algorithm explored was compatible with w_R .

We have a contradiction, which concludes the first case.

Case 2 (Observed race). Assume that $q \in w'_q.w_t.w''_t$.

- Choose $Q'' \subseteq Q$ as a largest subset of Q such that for all $r \neq r', r, r' \in Q''$, they are independent; $E.w_t.w''_t \vdash r \diamond r'$.
- Choose w_Q as an arbitrary sequence containing all processes in Q'' exactly once.

We are now ready to define the sequence w_R , which will serve later in the proof as a wakeup sequence found while exploring the $E'_q.q$ subtree. Now,

- Let w''_R be $w_t.w''_t.w_Q$.
- Let e_q be the last event in w''_R such that $e_q \rightarrow_{E.w''_R} \text{next}_{[E'_q]}(q)$. Since $q \in WI_{[E'_q]}(w'_q.w_t.w''_t)$, it must be the case that e_q and $\text{next}_{[E'_q]}(q)$ are in an observed race. Then $O = \text{observers}(e_q, \text{next}_{[E'_q]}(q), w''_R) \subseteq Q''$.
- Let w'_Q be the subsequence of w_Q that is in O . Note that w'_Q is never empty.
- Define w_R as $w_t.w''_t.w'_Q$. Note how it is defined like w''_R was defined above, but using w'_Q . Note also that e_q is the last event in w_R such that $e_q \rightarrow_{E.w_R} \text{next}_{[E'_q]}(q)$, and that $O = \text{observers}(e_q, \text{next}_{[E'_q]}(q), w_R)$.

Claim 6. $w_R \notin \text{redundant}(E, \text{done})$

Proof. By contradiction. Assume $w_R \in \text{redundant}(E, \text{done})$. Then there exists some process r such that $r \in WI_{[E'_q]}(w'_q.w_R)$, but then also $r \in WI_{[E'_q]}(w'_q.w_t.w''_t.w'_Q)$. Let r' be the first process in w'_Q . We have that $r \in WI_{[E'_q]}(w'_q.w_t.w''_t.r')$. But $r' \in Q$, and by definition $w_t.w''_t.r' \notin \text{redundant}(E, \text{done})$, and thus $r \notin WI_{[E'_q]}(w'_q.w_t.w''_t.r')$. We have a contradiction. \square

By construction $e_q \lesssim_{E.w_R} \text{next}_{[E'_q]}(q)$. This means that we can construct an execution sequence $E'_q.E'.w'_Q$ with the same domain as $E.w_R$ where e_q and $\text{next}_{[E'_q]}(q)$ are reversed but all other edges of $\rightarrow_{E.w_R}$ are preserved. We claim that

Claim 7. $q \in I_{[E'_q]}(E')$

Proof. Assume $q \notin I_{[E'_q]}(E')$. Then there is a last event e'' in $E'_q.E'$ such that $e'' \rightarrow_{E'_q.E'} \text{next}_{[E'_q]}(q)$. Since $q \in WI_{[E'_q]}(w'_q.w_t.w''_t)$, that means that the race between e'' and $\text{next}_{[E'_q]}(q)$ is also an observed race and that some $o' \in \text{observers}(e'', \text{next}_{[E'_q]}(q), E'_q.E')$ is in $o' \in O$. But since $o' \in \text{observers}(\text{next}_{[E'_q]}(q), e_q, E'_q.E')$, that contradicts property 6.(f). \square

Then there is some sequence $E'_q.q.E'' .w'_Q \simeq E'_q.E'.w'_Q$. Since $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.E'.w'_Q} e_q$ by construction, then also $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.q.E'' .w'_Q} e_q$.

By Lemma 3 we receive an execution sequence $E'_q.q.E''' .w'_Q \simeq E'_q.E'.w'_Q$ such that for any $E'_q.q.E''' .w'_Q \vdash w''$, we still have $\text{next}_{[E'_q]}(q) \lesssim_{E'_q.q.E''' .w'_Q} e_q$.

By the inductive hypothesis for $E'_q.q$ applied to $E'_q.q.E''' .w'_Q.w''$ for some arbitrary $E'_q.q.E''' .w'_Q \vdash w''$, the algorithm has explored some sequence $E'_q.q.z$ in $[E'_q.q.E''' .w'_Q.w'']_{\simeq}$. While exploring $E'_q.q.z$, the algorithm will encounter the race $next_{[E'_q]}(q) \lesssim_{E'_q.q.z} e_q$. When handling it,

- E in the algorithm will correspond to $E'_q.q.z$ in this proof,
- e in the algorithm will correspond to $next_{[E'_q]}(q)$ in this proof, and
- e' in the algorithm will correspond to e_q in this proof.
- Let $x = notdep(next_{[E'_q]}(q), E'_q.q.z)$.

Since $next_{[E'_q]}(q)$ and e_q are in an observed race, the algorithm will go in the first branch of the if-statement on line 6.

Then let $o \in observers(next_{[E'_q]}(q), e_q, E'_q.q.z)$ be the o that the algorithm chooses on line 7 and $y = notobs(next_{[E'_q]}(q), e_q, E'_q.q.z) \setminus \widehat{e}_q$. Then $v = x.\widehat{e}_q.y.\widehat{o}$ will be the sequence v in Algorithm 1.

Now,

- let $x' = notdep(next_{[E'_q]}(q), E'_q.q.E''' .w'_Q.w'')$,
- let $y' = notobs(next_{[E'_q]}(q), e_q, E'_q.q.E''' .w'_Q.w'') \setminus \widehat{e}_q$, and
- let $v' = x' .\widehat{e}_q .y' .\widehat{o}$.

We will now define a sequence v'' which has the same domain as v , and the same happens-before order $\rightarrow_{E'_q.v''}$, except that the edge between e_q and $next_{[E'_q]}(q)$ is reversed. Define $v'' = x.q.\widehat{e}_q.y.\widehat{o}$. Finally, define w'_R as w_R but with w'_Q replaced by o . It trivially follows that also $w'_R \notin redundant(E, done)$.

We will show that $E'_q.v''$ is equivalent to $E'_q.q.E'' .o$ and from that derive that $E'_q.v$ is equivalent to $E.w'_R$.

Claim 8. $dom_{[E'_q]}(v) = dom_{[E'_q]}(w'_q.w'_R)$.

Proof. We have that $dom_{[E'_q]}(w'_q.w'_R) = dom_{[E'_q]}(q.E'' .o)$

By the construction of w'_R , everything in $dom_{[E'_q.q.E''' .w'_Q]}(w'')$ conflict with $o \in w'_Q$, and can therefore not be included in x' or y' . Conversely, since $E'_q.q.E'' .o$ ends with o , nothing outside of w'' happen after o in $E'_q.q.E''' .w'_Q.w''$, and thus $E'_q.q.x' .\widehat{e}_q .y' .o \simeq E'_q.q.E'' .o$ and $dom_{[E'_q]}(q.E'' .o) = dom_{[E'_q]}(v')$.

Finally, since $E'_q.v \simeq E'_q.v'$, we have $dom_{[E'_q]}(v) = dom_{[E'_q]}(w'_q.w'_R)$. \square

Claim 9. $E'_q.v'' \simeq E'_q.q.E'' .o$.

Proof. Since $next_{[E'_q]}(q) \lesssim_{E'_q.q.E''' .w'_Q.w''} e_q$, it is obvious that $v'' \sqsubseteq_{[E'_q]} q.z$, therefore, since $E'_q.q.E''' .w'_Q.w'' \simeq E'_q.q.z$, we also have $v'' \sqsubseteq_{[E'_q]} q.E'' .w'_Q.w''$, and then using claim 8, we have $v'' \sqsubseteq_{[E'_q]} q.E'' .o$ and moreover $E'_q.v'' \simeq E'_q.q.E'' .o$. Since $E'_q.q.E''' .w'_Q \simeq E'_q.q.E'' .w'_Q$, we have $E'_q.v'' \simeq E'_q.q.E'' .o$. \square

Lemma 6. $E'_q.v \simeq E.w'_R$.

Proof. Since v is the same as v' but with $next_{[E'_q]}(q)$ and e_q reversed, and $E'_q.w'_R$ is the same as $E'_q.q.E''.o$ but with $next_{[E'_q]}(q)$ and e_q reversed, claim 9 together with property 6.(g) implies $E'_q.v \simeq E.w'_R$. \square

From Lemma 6 and $w'_R \notin \text{redundant}(E, \text{done})$, we conclude that the test on line 11 must have succeeded and that the algorithm will have inserted v into the wakeup tree. Since E is the first branch compatible with $E'_q.v \simeq E.w'_R$, wakeup tree insertion will have ensured that we will explore some sequence startable with $E.w'_R$ after E . However, $w'_R \notin \text{redundant}(E, \text{done})$ means that none of the continuations of E that the algorithm explored was compatible with w'_R .

We have a contradiction, which concludes the second case.

Since both cases ended in contradiction, the assumption that there was a sequence w that the algorithm missed after E must be false and the theorem true. \square

B Proof of Optimality

First we state a lemma, similar to lemma 7.2 from [2].

Lemma 7. *If $E.p \propto E.w$ and $E.w$ is maximal then $p \notin I_{[E]}(w)$.*

Proof. Assume $E.p \propto E.w$. This implies that the sequence $E.w$ is explored after a call to $Explore(E.p, \cdot)$ on line 22 has returned. For the algorithm to explore $E.w$, the wakeup tree $wut(E)$ contains nodes corresponding to prefixes of w and such a node is eventually picked by lines 19 and 20. Moreover, when the algorithm is inside any call to $Explore(E.w', \cdot)$, where w' is a prefix of w , it holds (due to line 24) that $p \in \text{done}(E)$ (1).

Eventually the algorithm will make a call to $Explore(E.w, WuT')$. Right before this call, on line 21, WuT' is picked as a subtree of a wakeup tree, which is valid by invariant 1 of Lemma 4. Because $E.w$ is maximal, the only possible such choice is for WuT' to be empty. Then, by invariant 2 of Lemma 4, we then have $\text{redundant}(E.w, \text{done}) = \emptyset$. This together with Definition 3 and (1) implies that $p \notin WI_{[E]}(w)$ from which we can conclude that $p \notin I_{[E]}(w)$. \square

Using this lemma, we can prove Theorem 2.

Proof (Theorem 2). Assume that E_1 and E_2 are two maximal execution sequences that are explored by the algorithm. Then they are both in \mathcal{E} . Assume w.l.o.g. that $E_1 \propto E_2$. Let E be their longest common prefix, and let $E_1 = E.p.v_1$ and $E_2 = E.v_2$. Since $E_1 \simeq E_2$, by Definition 2.1 we have $p \in I_{[E]}(v_2)$. This contradicts Lemma 7 which states that $p \notin I_{[E]}(w)$ for any maximal sequence $E.w$ explored by the algorithm. \square

C Code for some Erlang Benchmarks

Figure 2 shows the example of selectively receiving N messages in a fixed order from Sect. 2 written in Erlang. The main process R spawns the senders (using a list comprehension), and stores a list of their PIDs in the `Senders` variable (line 6). Each

```

1 -module(selective).
2 -export([test/1]).
3
4 test(N) ->
5   R = self(),
6   Senders = [spawn(fun() -> S = self(), R ! {S, I} end) || I <- lists:seq(1, N)],
7   [receive {S, X} -> X end || S <- Senders].

```

Fig. 2: An example from Sect. 2 in Erlang.

sender sends a message to R, which is a tuple containing its own PID (obtained by a call to the `self` operation) and an integer. After spawning the senders, the main process executes N selective receive statements, each one using a pattern that can match the PIDs of only one of the senders (line 7). Since all messages can be delivered independently, there exist $N!$ interleavings of the deliveries. Using observers, however, we can see that only one scheduling needs to be explored, as each message can be picked by only one receive statement.

```

1 -module(lock).
2 -export([test/1]).
3
4 test(N) ->
5   L = spawn(fun lock/0),
6   [spawn(fun() -> worker(L) end) || _ <- lists:seq(1, N)].
7
8 lock() ->
9   receive
10    {acquire, P} ->
11     P ! acquired,
12     receive release -> lock() end
13   end.
14
15 worker(L) ->
16   acquire(L),
17   release(L).
18
19 acquire(L) ->
20   L ! {acquire, self()},
21   receive acquired -> ok end.
22
23 release(L) ->
24   L ! release.

```

Fig. 3: Workers using a lock that is simulated by an Erlang process.

Figure 3 shows the code for the ‘locks’ example described in Section 7. The main process spawns a lock process and N workers. The lock process first expects an `acquire` message, that also identifies a worker that is requesting the lock. This worker will be signaled to proceed with a `acquired` message. Then the lock process expects a `release` message. In the given program, the signaled worker is the only one that can send this message.

While acquire messages interfere with other acquire messages, they cannot be received by the receive statement expecting a release message, and as a result we can treat them as independent with release messages. By taking advantage of this information, only $N!$ schedulings need to be explored.