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Synthesis of Composition and Discrimination Operators for Divide-and-Conquer Logic Programs

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This chapter gives a brief overview of our framework for stepwise synthesis of logic programs from examples and properties. Directives are extracted for the development of a particular synthesis mechanism whose steps are guided by a divide-and-conquer schema. It features deductive and inductive reasoning. Examples and properties are presented to it in a non-incremental fashion. The objectives and methods of its last steps (synthesis of composition and discrimination operators) are formalized, and illustrated on some sample problems.

This chapter is organized as follows. After the introduction, three sample problems are presented in section 1.2. Sections 1.3 to 1.5 present the objectives and methods of some synthesis steps, and illustrate them on the sample problems. Some conclusions on the results are drawn in section 1.6, related work is stated, and future research directions are outlined.

1.1 INTRODUCTION

Program synthesis research [Bie92] aims at automating the passage from specifications to programs, in opposition to more traditional, mostly manual, programming techniques. The key question here is: “what is a specification?”. Today, an emerging

consensus is that one may speak of *synthesis* if the specification does not explicitly reveal recursion or iteration. Otherwise, the technique could be classified as *transformation*. In this introductory section, we present a brief overview of our framework for the stepwise synthesis of logic programs from examples and properties of the relation to be implemented. This framework is developed in more detail in [Fle93], [FD91], and [FD93].

1.1.1 Specifications by examples and properties

We first define a possible starting point of synthesis.

Definition 1 *A specification by examples and properties of a procedure r/n consists of:*

- i) a set $E(r)$ of ground examples of the behavior of r ;*
- ii) a set $P(r)$ of properties (first-order logic statements) of r .*

Examples are a very appealing means of conveying information about a relation: they are easy to elaborate or understand, and they implicitly reveal some manipulations of the parameters. However, examples alone constitute an incomplete information source, and they lack in expressive power. We believe the specifier knows the missing details, and should thus be able to provide them, rather than have the synthesis mechanism guess them. We depart thus from traditional example-based synthesis and allow a property set in our specifications, where properties are meant to overcome the drawbacks of examples, and yet have the same appeal as examples. Non-recursive Horn clauses are a very convenient format. (Since we assume here that synthesis starts from a non-recursive specification, we do not consider recursive properties.) Sample sets of examples and properties appear in section 1.2.

1.1.2 Logic algorithms

We are actually only interested in synthesizing algorithms, rather than full-fledged programs. Indeed, algorithm design in itself is already very hard, and we do not want to encumber ourselves with the additional burdens of algorithm optimization, transformation, and implementation, which are well-researched topics anyway ([Dev90]). Algorithms expressed in a logic formalism are here called logic algorithms ([Dev90]).

Definition 2 *A logic algorithm of a procedure r , denoted $LA(r)$, consists of a formula of the form: $r(X, Y) \Leftrightarrow Def[X, Y]$, where the body Def is a first-order logic statement.¹*

Executable Prolog programs can be easily derived from logic algorithms with bodies in disjunctive normal form [Dev90]. Sample logic algorithms appear in section 1.2.

¹ Extending the syntactical conventions stated in section ??, (predicate) variable names start with an uppercase; functors and predicate names start with a lowercase. The string $F[X, Y]$ denotes a formula F whose free variables are X and Y ; the string $F[a, b]$ denotes $F[X, Y]$ where the free occurrences of X and Y have been replaced by the terms a and b , respectively. The variables X and Y are assumed to be universally quantified over $LA(r)$; other free variables in Def are assumed to be existentially quantified over Def .

$$\begin{array}{l}
 R(X, Y) \Leftrightarrow \\
 \vee \bigvee_{1 \leq k \leq c} \quad \begin{array}{l} \textit{Minimal}(X) \\ \textit{NonMinimal}(X) \end{array} \quad \begin{array}{l} \wedge \textit{Solve}(X, Y) \\ \wedge \textit{Decompose}(X, \mathbf{HX}, \mathbf{TX}) \\ \wedge \textit{Discriminate}_k(\mathbf{HX}, \mathbf{TX}, Y) \\ \wedge \mathbf{R}(\mathbf{TX}, \mathbf{TY}) \\ \wedge \textit{Process}_k(\mathbf{HX}, \mathbf{HY}) \\ \wedge \textit{Compose}_k(\mathbf{HY}, \mathbf{TY}, Y) \end{array}
 \end{array}$$

Figure 1.1 The divide-and-conquer logic algorithm schema

1.1.3 Schema-guided design of logic algorithms

Algorithm schemata are an old idea of computer science (see an early survey in [Man74]). They are template algorithms with fixed control flows. They embody the essence of algorithm design strategies (such as divide-and-conquer, generate-and-test, global search, ...) and are thus an invaluable knowledge source for guiding (semi-) automated algorithm design.

Example 1 Loosely speaking, a *divide-and-conquer* algorithm for a binary predicate r over parameters X and Y works as follows. Let X be the induction parameter. If X is minimal, then Y is found by directly solving the problem. Otherwise, i.e. if X is non-minimal, we decompose X into a series² \mathbf{HX} of heads of X and a series \mathbf{TX} of tails of X , the latter being of the same type as X , as well as smaller than X according to some well-founded relation. The tails \mathbf{TX} recursively yield tails \mathbf{TY} of Y . The heads \mathbf{HX} are processed into a series \mathbf{HY} of heads of Y . Finally, Y is composed from its heads \mathbf{HY} and tails \mathbf{TY} . It may happen that different processing and composition operators emerge for the non-minimal form of X : we have to discriminate between them according to the values of \mathbf{HX} , \mathbf{TX} , and Y . Non-determinism of the intended relation results in some discriminants that are always *true*. \diamond

Logic algorithm schemata can be expressed as second-order logic algorithms. For instance, many logic algorithms designed by a divide-and-conquer strategy, and having a single minimal case and a single non-minimal case, fit the schema of figure 1.1, where $\mathbf{R}(\mathbf{TX}, \mathbf{TY})$ stands for an optional conjunction of recursive atoms. To simplify the presentation of this chapter, the proposed examples will be based on the schema of figure 1.2. This is a particular case of figure 1.1, where the induction parameter X is a list, decomposed in the traditional head/tail form. In this particular case, the non-minimal test and the decomposition operator could have been merged. We also assume that the processing and composition operators are handled by a single predicate.

² In a logic algorithm schema, a bold term (resp. atom) denotes a vector (resp. conjunction) of terms (resp. atoms).

$$\begin{array}{l}
R(X, Y) \Leftrightarrow \\
\forall \bigvee_{1 \leq k \leq c} \quad \begin{array}{l} X = [] \\ X = [_ | _] \end{array} \quad \begin{array}{l} \wedge \text{Solve}(X, Y) \\ \wedge X = [HX | TX] \\ \wedge \text{Discriminate}_k(HX, TX, Y) \\ \wedge R(TX, TY) \\ \wedge \text{ProcComp}_k(HX, TY, Y) \end{array}
\end{array}$$

Figure 1.2 The divide-and-conquer logic algorithm schema for lists

1.1.4 Directives for the development of a synthesis mechanism

We have developed in [FD91, FD93] a general strategy for stepwise, progressive, consistent, and sound synthesis of logic algorithms from specifications by examples and properties. In stepwise synthesis, there is a series of refinements towards a correct logic algorithm:

$$LA_1(r), LA_2(r), \dots, LA_i(r), \dots, LA_f(r)$$

At each step, we measure the current logic algorithm against the intended relation: correctness criteria useful for characterizing the soundness of synthesis have been identified. Across several steps, we measure the progression of the synthesized logic algorithms towards the intended relation: comparison criteria useful for characterizing the progression of synthesis have also been identified. These criteria may be used during the development of a synthesis mechanism to ensure a sound synthesis.

This general strategy can be particularized to schema-guided synthesis, where each step instantiates some predicate variable(s) of a schema. We adopt the divide-and-conquer schema of figure 1.1 for guiding synthesis. Indeed, the class of algorithms that can be designed by this strategy is fairly large and important. There are eight steps to our mechanism [Fle91]:

- Step 1: Syntactic creation of a first approximation
- Step 2: Synthesis of *Minimal* and *NonMinimal*
- Step 3: Synthesis of *Decompose*
- Step 4: Introduction of the recursive atoms
- Step 5: Synthesis of *Solve*
- Step 6: Synthesis of the *Process_k* and *Compose_k*
- Step 7: Synthesis of the *Discriminate_k*
- Step 8: Syntactic generalization

Steps 1 to 3 are straightforward, and are based on type knowledge. Step 4 is relatively easy, and is performed by deductive reasoning from the property set. Step 5 is a particular case of steps 6 and 7. The latter are the real challenges of synthesis, and are the main topics of this paper. Step 8 is straightforward again, but is also covered here. The development of all these steps can be found in [Fle93].

Note that the divide-and-conquer schema is not an input to the synthesis mechanism, but rather hardwired into it.

We implement deterministic and non-deterministic relations using the same synthesis mechanism. The choice of an induction parameter is a priori independent of the way (*mode*) the resulting program can be used.

Examples give rise to inductive synthesis (generalization ([Sum77, BS79, Bie84]), learning ([Sha82, Tin90, Mug92])), whereas axiomatic specifications (and thus properties) give rise to deductive synthesis (proofs-as-programs ([MW79, BSW90, Fri90]), transformations ([Han80, Cla81, Hog81, LP90])). Since we have both kinds of specification information, we want to avoid using only one kind of inference, and thus degrading the non-used information source into validation information. We therefore strive for inductive *and* deductive synthesis, using whichever inference kind is best suited at each step. This approach of course precludes synthesis if only examples or only properties are given. It also gives a constructive role to each kind of specification information.

There are two ways of presenting examples (and properties): *one-by-one (incrementally)*, or *all-at-once*. The former approach, advocated in [Sha82] and by the Inductive Logic Programming (ILP) school of thought [Mug92], has some nice convergence properties. But we adhere to the school of thought advocated in [Sum77], where the examples are presented all-at-once, so that a maximum of information is available at each step.

With example-based synthesis, constants from $E(r)$ inevitably appear in the logic algorithms, thus destroying completeness unless they are generalized. We thus need one more notion:

Definition 3 *Let Γ be a total function in the set of logic algorithms, such that $\Gamma(LA(r))$ is $LA(r)$ without its equality atoms involving constants introduced from $E(r)$.*

This is illustrated in subsequent sections. It can be shown that Γ is a generalization function, i.e. that $\Gamma(LA(r))$ is at least as general as $LA(r)$. Informally speaking, a logic algorithm is more general than another one iff its body is more often *true* than the body of the other one.

The rest of this chapter is organized as follows. In section 1.2, we present three sample problems. In sections 1.3 to 1.5, we present the objectives and methods of steps 6 to 8, respectively, and illustrate them on the sample problems. In section 1.6, we draw some conclusions on our results, state some related work, and outline future research directions.

1.2 SAMPLE PROBLEMS

In this section, we state three sample problems that are used throughout the remainder of this chapter. The problems can be studied independently, both here and in subsequent sections. But they are complementary in the sense that they illustrate distinct intricacies of logic algorithm synthesis. Each problem is first specified informally, then by examples and properties. These examples and properties are supposed to be given: this chapter does not discuss the elaboration of such specifications. For each problem, a sample logic algorithm (constructed by the methodology of [Dev90]) is shown in order to illustrate what the synthesis mechanism should achieve. For each problem,

| | | | |
|--------------------|-----|---|----------|
| $E(\text{efface})$ | = { | $\text{efface}(a, [a], [])$ | (EE_1) |
| | | $\text{efface}(b, [b, c], [c])$ | (EE_2) |
| | | $\text{efface}(e, [d, e], [d])$ | (EE_3) |
| | | $\text{efface}(f, [f, g, h], [g, h])$ | (EE_4) |
| | | $\text{efface}(j, [i, j, k], [i, k])$ | (EE_5) |
| | | $\text{efface}(p, [m, n, p], [m, n])$ | (EE_6) |
| $P(\text{efface})$ | = { | $\text{efface}(X, [X T], T)$ | (PE_1) |
| | | $\text{efface}(X, [Y, X T], [Y T]) \Leftarrow X \neq Y$ | (PE_2) |

Figure 1.3 Sample versions of $E(\text{efface})$ and $P(\text{efface})$

| | |
|--|-----------------------------------|
| $\text{efface}(E, L, R) \Leftrightarrow$ | |
| $L = [HL TL]$ | $\wedge E = HL \wedge R = TL$ |
| $\vee L = [HL TL]$ | $\wedge E \neq HL$ |
| | $\wedge \text{efface}(E, TL, TR)$ |
| | $\wedge R = [HL TR]$ |

Figure 1.4 A sample version of $LA(\text{efface})$

we also give the logic algorithm synthesized by step 5, but, again, we do not discuss how this is performed.

1.2.1 The *efface*/3 problem

The $\text{efface}(\mathbf{E}, \mathbf{L}, \mathbf{R})$ procedure succeeds iff term \mathbf{E} belongs (at least once) to the non-empty list \mathbf{L} , and list \mathbf{R} is \mathbf{L} without its first occurrence of \mathbf{E} .

Figure 1.3 gives sample versions of $E(\text{efface})$ and $P(\text{efface})$. Note that properties PE_1 and PE_2 generalize the examples of $\{EE_1, EE_2, EE_4\}$ and $\{EE_3, EE_5\}$, respectively.

Figure 1.4 gives a sample version of $LA(\text{efface})$.

Figure 1.5 shows $LA_5(\text{efface})$, where disjunct DE_i corresponds to example EE_i . We assume that step 2 chose L as induction parameter, and introduced one minimal case (disjunct DE_1 , where L has exactly one element), and one non-minimal case (disjuncts DE_2 to DE_6 , where L has at least two elements). Step 3 decomposed the non-minimal form into its head HL and tail TL . Step 4 introduced recursive atoms into some non-minimal disjuncts. Note that E was judged to be an *auxiliary parameter* (because it is of a non-inductive type³): this prevented the search for a tail TE of E while introducing recursive atoms; moreover, this prevented a superfluous introduction

³ Inductive types are here assumed to be either integers or lists.

| | |
|--------------------------|--|
| efface(E, L, R) ⇔ | |
| L = [] | $\wedge \mathbf{L} = [\mathbf{E}] \wedge \mathbf{R} = []$ $\wedge E = a \wedge L = [a] \wedge R = []$ (DE₁) |
| ∨ L = [_, _] | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{E} = \mathbf{HL} \wedge \mathbf{R} = \mathbf{TL}$ $\wedge E = b \wedge L = [b, c] \wedge R = [c]$ $\wedge HL = b \wedge TL = [c]$ (DE₂) |
| ∨ L = [_, _] | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{efface}(\mathbf{E}, \mathbf{TL}, \mathbf{TR})$ $\wedge E = e \wedge L = [d, e] \wedge R = [d]$ $\wedge HL = d \wedge TL = [e] \wedge TR = []$ (DE₃) |
| ∨ L = [_, _] | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{E} = \mathbf{HL} \wedge \mathbf{R} = \mathbf{TL}$ $\wedge E = f \wedge L = [f, g, h] \wedge R = [g, h]$ $\wedge HL = f \wedge TL = [g, h]$ (DE₄) |
| ∨ L = [_, _] | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{efface}(\mathbf{E}, \mathbf{TL}, \mathbf{TR})$ $\wedge E = j \wedge L = [i, j, k] \wedge R = [i, k]$ $\wedge HL = i \wedge TL = [j, k] \wedge TR = [k]$ (DE₅) |
| ∨ L = [_, _] | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{efface}(\mathbf{E}, \mathbf{TL}, \mathbf{TR})$ $\wedge E = p \wedge L = [m, n, p] \wedge R = [m, n]$ $\wedge HL = m \wedge TL = [n, p] \wedge TR = [n]$ (DE₆) |

Figure 1.5 LA₅(efface)

of recursion in disjuncts DE₂ and DE₄. Step 5 solved the minimal case and the non-recursive, non-minimal case. The atoms in boldface represent Γ(LA₅(efface)).

1.2.2 The perm/2 problem

The perm(L,P) procedure succeeds iff list P is a permutation of list L.

Figure 1.6 gives sample versions of E(perm) and P(perm). Note that properties PP₁ to PP₃ generalize examples EP₂ to EP₄, respectively.

Figure 1.7 gives a sample version of LA(perm).

Figure 1.8 shows LA₅(perm), where disjunct DP_i corresponds to example EP_i. We assume that step 2 chose L as induction parameter, and introduced one minimal case (disjunct DP₁, where L is empty), and one non-minimal case (disjuncts DP₂ to DP₁₀, where L has at least one element). Step 3 decomposed the non-minimal form into its head HL and tail TL. Step 4 introduced a recursive atom into each non-minimal disjunct: note that the values of TP (the tail of P) could not have been computed

$$\begin{array}{l}
 E(\text{perm}) = \{ \text{perm}([], []) \quad (EP_1) \\
 \quad \text{perm}([a], [a]) \quad (EP_2) \\
 \quad \text{perm}([b, c], [b, c]) \quad (EP_3) \\
 \quad \text{perm}([b, c], [c, b]) \quad (EP_4) \\
 \quad \text{perm}([d, e, f], [d, e, f]) \quad (EP_5) \\
 \quad \text{perm}([d, e, f], [d, f, e]) \quad (EP_6) \\
 \quad \text{perm}([d, e, f], [e, d, f]) \quad (EP_7) \\
 \quad \text{perm}([d, e, f], [e, f, d]) \quad (EP_8) \\
 \quad \text{perm}([d, e, f], [f, d, e]) \quad (EP_9) \\
 \quad \text{perm}([d, e, f], [f, e, d]) \} \quad (EP_{10}) \\
 P(\text{perm}) = \{ \text{perm}([X], [X]) \quad (PP_1) \\
 \quad \text{perm}([X, Y], [X, Y]) \quad (PP_2) \\
 \quad \text{perm}([X, Y], [Y, X]) \} \quad (PP_3)
 \end{array}$$

Figure 1.6 Sample versions of $E(\text{perm})$ and $P(\text{perm})$

$$\begin{array}{l}
 \text{perm}(L, P) \Leftrightarrow \\
 \quad L = [] \quad \wedge \quad P = [] \\
 \vee \quad L = [HL|TL] \quad \wedge \quad \text{perm}(TL, TP) \\
 \quad \quad \quad \quad \quad \quad \wedge \quad \text{efface}(HL, P, TP)
 \end{array}$$

Figure 1.7 A sample version of $LA(\text{perm})$

deterministically in disjuncts DP_5 to DP_{10} . Step 5 solved the minimal case. The atoms in boldface represent $\Gamma(LA_5(\text{perm}))$.⁴

| | | |
|---|--|-------------|
| $\text{perm}(\mathbf{L}, \mathbf{P}) \Leftrightarrow$ | | |
| $\mathbf{L} = []$ | $\wedge \mathbf{P} = []$ | |
| | $\wedge L = [] \wedge P = []$ | (DP_1) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [a] \wedge P = [a]$ | |
| | $\wedge HL = a \wedge TL = [] \wedge TP = []$ | (DP_2) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [b, c] \wedge P = [b, c]$ | |
| | $\wedge HL = b \wedge TL = [c] \wedge TP = [c]$ | (DP_3) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [b, c] \wedge P = [c, b]$ | |
| | $\wedge HL = b \wedge TL = [c] \wedge TP = [c]$ | (DP_4) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge HL = d \wedge TL = [e, f] \wedge TP \in \{[e, f], [f, e]\}$ | (DP_5) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [d, e, f] \wedge P = [d, f, e]$ | |
| | $\wedge HL = d \wedge TL = [e, f] \wedge TP \in \{[e, f], [f, e]\}$ | (DP_6) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [d, e, f] \wedge P = [e, d, f]$ | |
| | $\wedge HL = d \wedge TL = [e, f] \wedge TP \in \{[e, f], [f, e]\}$ | (DP_7) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [d, e, f] \wedge P = [e, f, d]$ | |
| | $\wedge HL = d \wedge TL = [e, f] \wedge TP \in \{[e, f], [f, e]\}$ | (DP_8) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [d, e, f] \wedge P = [f, d, e]$ | |
| | $\wedge HL = d \wedge TL = [e, f] \wedge TP \in \{[e, f], [f, e]\}$ | (DP_9) |
| $\vee \mathbf{L} = [_]$ | $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}] \wedge \text{perm}(\mathbf{TL}, \mathbf{TP})$ | |
| | $\wedge L = [d, e, f] \wedge P = [f, e, d]$ | |
| | $\wedge HL = d \wedge TL = [e, f] \wedge TP \in \{[e, f], [f, e]\}$ | (DP_{10}) |

Figure 1.8 $LA_5(\text{perm})$

1.2.3 The *firstSeq/3* problem

The $\text{firstSeq}(\mathbf{L}, \mathbf{F}, \mathbf{S})$ procedure succeeds iff list \mathbf{F} is the first maximal sequence of identical elements at the beginning of list \mathbf{L} , and list \mathbf{S} is the corresponding suffix of \mathbf{L} .

⁴ The $E \in S$ predicate holds iff term E belongs to the set S . Note that, in a logic algorithm, boldface terms and atoms do not denote vectors and conjunctions, contrary to logic algorithm schemas.

| | | | |
|------------------------|-----|--|------------|
| $E(\mathit{firstSeq})$ | = { | $\mathit{firstSeq}([a],[a],[\])$ | (EF_1) |
| | | $\mathit{firstSeq}([b,c,d,e],[b],[c,d,e])$ | (EF_2) |
| | | $\mathit{firstSeq}([f,f,g,h],[f,f],[g,h])$ | (EF_3) |
| | | $\mathit{firstSeq}([i,i,i,j],[i,i,i],[j])$ | (EF_4) |
| $P(\mathit{firstSeq})$ | = { | $\mathit{firstSeq}([X],[X],[\])$ | (PF_1) |
| | | $\mathit{firstSeq}([X,X],[X,X],[\])$ | (PF_2) |
| | | $\mathit{firstSeq}([X,Y T],[X],[Y T]) \Leftarrow X \neq Y$ | (PF_3) |

Figure 1.9 Sample versions of $E(\mathit{firstSeq})$ and $P(\mathit{firstSeq})$

Figure 1.9 gives sample versions of $E(\mathit{firstSeq})$ and $P(\mathit{firstSeq})$. Note that properties PF_1 and PF_3 generalize examples EF_1 and EF_2 , respectively.

Figure 1.10 gives a sample version of $LA(\mathit{firstSeq})$.

| | |
|--|--|
| $\mathit{firstSeq}(L, F, S) \Leftrightarrow$ | |
| $L = [HL]$ | $\wedge F = L \wedge S = [\]$ |
| $\vee L = [HL_1, HL_2 TL]$ | $\wedge HL_1 \neq HL_2 \wedge F = [HL_1] \wedge S = [HL_2 TL]$ |
| $\vee L = [HL_1, HL_2 TL]$ | $\wedge HL_1 = HL_2$ |
| | $\wedge \mathit{firstSeq}([HL_2 TL], TF, TS)$ |
| | $\wedge F = [HL_1 TF] \wedge S = TS$ |

Figure 1.10 A sample version of $LA(\mathit{firstSeq})$

Figure 1.11 shows $LA_5(\mathit{firstSeq})$, where disjunct DF_i corresponds to example EF_i . We assume that step 2 chose L as induction parameter, and introduced one minimal case (disjunct DF_1 , where L has exactly one element), and one non-minimal case (disjuncts DF_2 to DF_4 , where L has at least two elements). Step 3 decomposed the non-minimal form into its head HL and tail TL . Step 4 introduced a recursive atom into the last two non-minimal disjuncts only: indeed, recursion would be useless in the first non-minimal disjunct where F and S can already be computed directly from HL and TL . Step 5 solved the minimal case and the non-recursive, non-minimal case. The atoms in boldface represent $\Gamma(LA_5(\mathit{firstSeq}))$.

1.3 SYNTHESIS OF THE $PROCESS_K$ AND $COMPOSE_K$ (STEP 6)

In the non-minimal recursive case, the $Process_k(\mathbf{HX}, \mathbf{HY})$ procedures process the heads \mathbf{HX} of the induction parameter X into the heads \mathbf{HY} of the other parameter Y , in case X is non-minimal. The $Compose_k(\mathbf{HY}, \mathbf{TY}, Y)$ procedures compose

| | |
|---|--|
| firstSeq(L, F, S) \Leftrightarrow | |
| L = [] | \wedge F = L \wedge S = [] |
| | \wedge $L = [a] \wedge F = [a] \wedge S = []$ (DF₁) |
| \vee L = [—, — —] | \wedge L = [HL TL] |
| | \wedge TL = [HTL —] \wedge HL \neq HTL |
| | \wedge F = [HL] \wedge S = TL |
| | \wedge $L = [b, c, d, e] \wedge F = [b] \wedge S = [c, d, e]$ |
| | \wedge $HL = b \wedge TL = [c, d, e]$ (DF₂) |
| \vee L = [—, — —] | \wedge L = [HL TL] |
| | \wedge firstSeq(TL, TF, TS) |
| | \wedge $L = [f, f, g, h] \wedge F = [f, f] \wedge S = [g, h]$ |
| | \wedge $HL = f \wedge TL = [f, g, h]$ |
| | \wedge $TF = [f] \wedge TS = [g, h]$ (DF₃) |
| \vee L = [—, — —] | \wedge L = [HL TL] |
| | \wedge firstSeq(TL, TF, TS) |
| | \wedge $L = [i, i, i, j] \wedge F = [i, i, i] \wedge S = [j]$ |
| | \wedge $HL = i \wedge TL = [i, i, j]$ |
| | \wedge $TF = [i, i] \wedge TS = [j]$ (DF₄) |

Figure 1.11 $LA_5(\text{firstSeq})$

parameter Y from its heads \mathbf{HY} (obtained via processing \mathbf{HX}) and tails \mathbf{TY} (obtained via recursion on \mathbf{TX}). We merge each $Process_k(\mathbf{HX}, \mathbf{HY})$ with its counterpart $Compose_k(\mathbf{HY}, \mathbf{TY}, Y)$ into $ProcComp_k(\mathbf{HX}, \mathbf{TY}, Y)$ so that their implementations are synthesized at the same time. In this section, we first formally present the objective and methods of step 6. Then we synthesize $ProcComp_k$ operators for the sample problems given in section 1.2.

1.3.1 Formalization: objective and methods

Given $LA_5(r)$ as follows:

$$\begin{aligned}
 r(X, Y) \Leftrightarrow & \\
 & \begin{array}{l}
 \text{minimal}(X) \qquad \wedge \text{solve}(X, Y) \\
 \vee \text{nonMinimal}(X) \quad \wedge \bigvee_{1 \leq i \leq b} X = x_i \wedge Y = y_i \\
 \qquad \qquad \qquad \wedge \text{decompose}(X, \mathbf{HX}, \mathbf{TX}) \\
 \qquad \qquad \qquad \wedge \mathbf{r}(\mathbf{TX}, \mathbf{TY}) \\
 \qquad \qquad \qquad \wedge \bigvee_{b < i \leq m} X = x_i \wedge Y = y_i \\
 \qquad \qquad \qquad \wedge \mathbf{HX} = \mathbf{hx}_i \wedge \mathbf{TX} = \mathbf{tx}_i \wedge \mathbf{TY} \in \mathbf{ty}_i
 \end{array}
 \end{aligned}$$

(where the last $m - b$ of the m examples are “covered” by the non-minimal disjuncts, and the first b examples are “covered” by the minimal disjuncts), the aim at step 6 is to transform $LA_5(r)$ into $LA_6(r)$ such that it fits the following schema:

$$\begin{aligned}
 r(X, Y) \Leftrightarrow & \\
 & \begin{array}{l}
 \text{minimal}(X) \qquad \wedge \text{solve}(X, Y) \\
 \vee \bigvee_{1 \leq k \leq c} \text{nonMinimal}(X) \quad \wedge \bigvee_{1 \leq i \leq b} X = x_i \wedge Y = y_i \\
 \qquad \qquad \qquad \wedge \text{decompose}(X, \mathbf{HX}, \mathbf{TX}) \\
 \qquad \qquad \qquad \wedge \mathbf{r}(\mathbf{TX}, \mathbf{TY}) \\
 \qquad \qquad \qquad \wedge ProcComp_k(\mathbf{HX}, \mathbf{TY}, Y) \\
 \qquad \qquad \qquad \wedge \bigvee_{i \in |k|} X = x_i \wedge Y = y_i \\
 \qquad \qquad \qquad \wedge \mathbf{HX} = \mathbf{hx}_i \wedge \mathbf{TX} = \mathbf{tx}_i \wedge \mathbf{TY} = \mathbf{ty}'_i
 \end{array}
 \end{aligned}$$

This amounts to partitioning the non-minimal recursive disjuncts into c equivalence classes (named $|1|, \dots, |c|$) where the disjuncts of class $|k|$ have equal instantiations of $ProcComp_k$. Moreover, step 4 (introduction of the recursive atoms) produces several potential values of the parameters \mathbf{TY} if the intended relation is non-deterministic: the second objective of step 6 is to trim these sets \mathbf{ty}_i to singleton sets \mathbf{ty}'_i .

We have identified two methods to synthesize implementations of the $ProcComp_k$:

- i) computation of most specific generalizations (msg, for short): the MSG Method applies if each $ProcComp_k$ is implemented as a conjunction of equality atoms;
- ii) synthesis from an inferred specification by examples and properties: the Synthesis Method applies if each $ProcComp_k$ itself needs a full-fledged recursive logic algorithm, i.e. is implemented as a disjunction of conjunctions of literals.

We discuss these methods in turn.

1.3.1.1 THE MSG METHOD

The MSG Method constructs equivalence classes incrementally and non-deterministically.

Definition 4 *The msg of a set of disjuncts \mathcal{D} is the msg of the $\langle \mathbf{hx}_i, \mathbf{ty}_i, y_i \rangle$ value-tuples extracted from the $\mathbf{HX} = \mathbf{hx}_i$, $\mathbf{TY} \in \mathbf{ty}_i$, and $Y = y_i$ atoms of the disjuncts $D_i \in \mathcal{D}$, if the \mathbf{ty}_i are singletons, and undefined otherwise.*

Definition 5 *A disjunct D'_i is an alternative of disjunct D_i iff D'_i is obtained from D_i by non-deterministically trimming the sets \mathbf{ty}_i of the $\mathbf{TY} \in \mathbf{ty}_i$ atoms to singletons \mathbf{ty}'_i .*

Definition 6 *An alternative D'_i of disjunct D_i is compatible with a set of disjuncts \mathcal{S} iff the msg $\langle \mathbf{hx}, \mathbf{ty}, y \rangle$ between the $\langle \mathbf{hx}_i, \mathbf{ty}'_i, y_i \rangle$ value-tuple extracted from D'_i and the msg of \mathcal{S} is such that y is a term whose variables are among the ones occurring in \mathbf{hx} and \mathbf{ty} .*

Now, the algorithm goes as follows. Initially, there are no classes. At any moment, the non-minimal recursive disjuncts can be classified according to whether or not they belong to some equivalence class. Progression is achieved by selecting a disjunct D_i that doesn't belong to any class. If some alternative D'_i of D_i is compatible with some class \mathcal{C} , then \mathcal{C} becomes $\mathcal{C} \cup \{D'_i\}$. Otherwise a new singleton class $\{D'_i\}$ is added, where D'_i is some alternative of D_i .

Once the equivalence classes have been computed, an assessing heuristic is applied:

Heuristic 1 *If there are more equivalence classes than non-minimal properties, then the msgs probably only cover the given examples, but not examples with larger parameters: invoke the Synthesis Method.*

Unless the Synthesis Method is judged to be applicable, the MSG Method continues. Let $procComp_k$ be the chosen instantiations of $ProcComp_k$. The msg $\langle \mathbf{hx}_k, \mathbf{ty}_k, y_k \rangle$ of class $|k|$ is rewritten as follows:

$$procComp_k(\mathbf{HX}, \mathbf{TY}, Y) \Leftrightarrow \mathbf{HX} = \mathbf{hx}_k \wedge \mathbf{TY} = \mathbf{ty}_k \wedge Y = y_k$$

so that it can be unfolded into the corresponding disjuncts.

1.3.1.2 THE SYNTHESIS METHOD

The Synthesis Method assumes that there is one single equivalence class, and that its $ProcComp(\mathbf{HX}, \mathbf{TY}, Y)$ is implemented as a possibly recursive disjunction of conjunctions of literals, i.e. it could be synthesized from scratch, just like any other logic algorithm. Let $procComp$ be the chosen instantiation of $ProcComp$. A specification by examples and properties for $procComp(\mathbf{HX}, \mathbf{TY}, Y)$ has to be inferred from $LA_5(r)$.

The inference of an example set is straightforward: just extract all the $\langle \mathbf{hx}_i, \mathbf{ty}_i, y_i \rangle$ tuples from the non-minimal disjuncts of $LA_5(r)$. If there are several alternatives, extract from the alternatives that led to compatibility during the MSG Method.

The inference of a property set is based on the observation that properties of the original problem are inherited by the subproblem. For every property of the form:

$$r(\mathbf{x}, \mathbf{y}) \Leftarrow \text{Body} \quad (P_i)$$

find variants:

$$r(\mathbf{tx}, \mathbf{ty}) \quad (E_j/P_j)$$

of examples or body-less properties, such that:

$$\text{decompose}(\mathbf{x}, \mathbf{tx}, \mathbf{ty})$$

where *decompose* is the *Decompose* predicate synthesized at step 3. Then infer:

$$\text{procComp}(\mathbf{hx}, \mathbf{ty}, \mathbf{y}) \Leftarrow \text{Body} \quad (P'_i)$$

as a property of *procComp*.

A logic algorithm $LA(\text{procComp})$ can now be synthesized from this inferred specification by examples and properties. The synthesis of $LA(r)$ proceeds using *procComp*.

1.3.2 Illustration on sample problems

We illustrate the above methods and heuristic on the sample problems of section 1.2.

1.3.2.1 THE *EFFACE/3* PROBLEM

The logic algorithm being synthesized for the *efface/3* problem is what we call a *partial scan* logic algorithm: the recursion may stop scanning the induction parameter before being through. Indeed, once *E* has been located in *L*, one can already compute the final value of *R* without further scanning *L*. At step 4, the actual values of the introduced parameter *TR* could be determined because *efface/3* is a deterministic problem, so no lifting of non-determinacy is required here.

According to the MSG Method, there are initially no classes, so no disjunct belongs to any class. We first consider disjunct DE_3 . It can't be compatible with any class, because there are none so far. So we create a singleton class $\{DE_3\}$.

We pursue with DE_5 . To see whether DE_5 is compatible with class $\{DE_3\}$, we compute the msg of their $\langle HL, TR, R \rangle$ value-tuples:

| <i>HL</i> | <i>TR</i> | <i>R</i> | |
|-----------|-----------|----------|--|
| <i>d</i> | $[\]$ | $[d]$ | $\text{msg}\{DE_3\}$ |
| <i>i</i> | $[k]$ | $[i, k]$ | DE_5 |
| <i>A</i> | <i>T</i> | $[A T]$ | $\text{msg}(\text{msg}\{DE_3\}, DE_5)$ |

Disjunct DE_5 goes into class $\{DE_3\}$, because it is compatible with that class ($[A|T]$ is constructed in terms of *A* and *T*).

We pursue with DE_6 . To see whether DE_6 is compatible with class $\{DE_3, DE_5\}$,

| | |
|---|--|
| efface(E, L, R) \Leftrightarrow | |
| L = [] | \wedge L = [E] \wedge R = [] \wedge $E = a \wedge L = [a] \wedge R = []$ |
| \vee L = [, -] | \wedge L = [HL TL] \wedge E = HL \wedge R = TL \wedge $E = b \wedge L = [b, c] \wedge R = [c] \wedge \dots$ \vee $E = f \wedge L = [f, g, h] \wedge R = [g, h] \wedge \dots$ |
| \vee L = [, -] | \wedge L = [HL TL] \wedge efface(E, TL, TR) \wedge R = [HL TR] \wedge $HL = d \wedge TR = [] \wedge R = [d] \wedge \dots$ \vee $HL = i \wedge TR = [k] \wedge R = [i, k] \wedge \dots$ \vee $HL = m \wedge TR = [n] \wedge R = [m, n] \wedge \dots$ |

Figure 1.12 $LA_6(\text{efface})$

we compute the msg of their $\langle HL, TR, R \rangle$ value-tuples:

| HL | TR | R | |
|------|-------|----------|--------------------------------|
| A | T | $[A T]$ | $msg\{DE_3, DE_5\}$ |
| m | $[n]$ | $[m, n]$ | DE_6 |
| A | T | $[A T]$ | $msg(msg\{DE_3, DE_5\}, DE_6)$ |

Disjunct DE_6 goes into class $\{DE_3, DE_5\}$, because it is compatible with that class.

There are no other disjuncts. We have partitioned the non-minimal disjuncts into $c = 1$ equivalence class, namely $\{DE_3, DE_5, DE_6\}$. Let $pcEfface$ be the chosen instantiation of $ProcComp_1$. It is implemented by re-expression of the msg:

$$pcEfface(HL, TR, R) \Leftrightarrow R = [HL|TR].$$

This result is unfolded into the corresponding disjuncts of $LA_5(\text{efface})$, and, after regrouping of disjuncts, $LA_6(\text{efface})$ looks as depicted in figure 1.12.

1.3.2.2 THE $PERM/2$ PROBLEM

The logic algorithm being synthesized for the $perm/2$ problem is what we call a *total scan* logic algorithm: the recursion scans the induction parameter entirely. Indeed, all elements of L need to be visited so that they can be stuffed into other locations in P . The other mission of step 6 is to lift the non-determinacy about the actual values of parameter TP introduced at step 4 because of the non-determinism of the $perm/2$ problem.

According to the MSG Method, there are initially no classes, so no disjunct belongs to any class. We first consider disjunct DP_2 , and make it a singleton class $\{DP_2\}$.

We pursue with DP_3 . To see whether DP_3 is compatible with class $\{DP_2\}$, we

compute the msg of their $\langle HL, TP, P \rangle$ value-tuples:

| HL | TP | P | |
|------|--------|----------|--------------------------|
| a | $[\]$ | $[a]$ | $msg\{DP_2\}$ |
| b | $[c]$ | $[b, c]$ | DP_3 |
| A | T | $[A T]$ | $msg(msg\{DP_2\}, DP_3)$ |

Disjunct DP_3 goes into class $\{DP_2\}$, because it is compatible with that class ($[A|T]$ is composed in terms of A and T).

We pursue with DP_4 . To see whether DP_4 is compatible with class $\{DP_2, DP_3\}$, we compute the msg of their $\langle HL, TP, P \rangle$ value-tuples:

| HL | TP | P | |
|------|-------|----------|--------------------------------|
| A | T | $[A T]$ | $msg\{DP_2, DP_3\}$ |
| b | $[c]$ | $[c, b]$ | DP_4 |
| A | T | $[B U]$ | $msg(msg\{DP_2, DP_3\}, DP_4)$ |

Disjunct DP_4 is not compatible with class $\{DP_2, DP_3\}$, because $[B|U]$ is not composed in terms of A and T . There are no other classes, so we create a new singleton class $\{DP_4\}$.

We pursue with DP_5 . To see whether DP_5 is compatible with class $\{DP_2, DP_3\}$, we compute the msg of their $\langle HL, TP, P \rangle$ value-tuples. Two alternatives arise:

| HL | TP | P | |
|------|----------|-------------|-----------------------------------|
| A | T | $[A T]$ | $msg\{DP_2, DP_3\}$ |
| d | $[e, f]$ | $[d, e, f]$ | DP_{5a} |
| A | T | $[A T]$ | $msg(msg\{DP_2, DP_3\}, DP_{5a})$ |

or:

| HL | TP | P | |
|------|----------|-------------|-----------------------------------|
| A | T | $[A T]$ | $msg\{DP_2, DP_3\}$ |
| d | $[f, e]$ | $[d, e, f]$ | DP_{5b} |
| A | T | $[A U]$ | $msg(msg\{DP_2, DP_3\}, DP_{5b})$ |

Disjunct DP_{5a} is compatible with class $\{DP_2, DP_3\}$, but DP_{5b} isn't. So DP_{5a} goes into class $\{DP_2, DP_3\}$.

The computations eventually identify $c = 3$ equivalence classes (see exercise 3).

| HL | TP | P | |
|------|------------|---------------|---------------------------------------|
| A | T | $[A T]$ | $msg\{DP_2, DP_3, DP_{5a}, DP_{6b}\}$ |
| A | $[B T]$ | $[B, A T]$ | $msg\{DP_4, DP_{7a}, DP_{9b}\}$ |
| A | $[B, C T]$ | $[B, C, A T]$ | $msg\{DP_{8a}, DP_{10b}\}$ |

Applying heuristic 1 (there are more equivalence classes than non-minimal properties), we invoke the Synthesis Method. Let $pcPerm$ be the chosen instantiation of $ProcComp_1$. A specification by examples and properties for $pcPerm(HL, TP, P)$ has to be inferred from $LA_5(perm)$.

The inference of an example set is done by extracting all the $\langle HL, TP, P \rangle$ value-

| | | | |
|-------------|-----|--------------------------------|----------|
| $E(pcPerm)$ | = { | $pcPerm(a, [], [a])$ | (ES_1) |
| | | $pcPerm(b, [c], [b, c])$ | (ES_2) |
| | | $pcPerm(b, [c], [c, b])$ | (ES_3) |
| | | $pcPerm(d, [e, f], [d, e, f])$ | (ES_4) |
| | | $pcPerm(d, [f, e], [d, f, e])$ | (ES_5) |
| | | $pcPerm(d, [e, f], [e, d, f])$ | (ES_6) |
| | | $pcPerm(d, [e, f], [e, f, d])$ | (ES_7) |
| | | $pcPerm(d, [f, e], [f, d, e])$ | (ES_8) |
| | | $pcPerm(d, [f, e], [f, e, d])$ | (ES_9) |
| $P(pcPerm)$ | = { | $pcPerm(X, [], [X])$ | (PS_1) |
| | | $pcPerm(X, [Y], [X, Y])$ | (PS_2) |
| | | $pcPerm(X, [Y], [Y, X])$ | (PS_3) |

Figure 1.13 Derived example and property sets for $pcPerm(E, L, R)$

tuples from the non-minimal disjuncts of $LA_5(perm)$. If there are several possible values for TP , we extract the one that led to compatibility during the MSG Method. The result is shown in figure 1.13. Note that example ES_5 is a variant of ES_4 , that ES_8 is a variant of ES_6 , and that ES_9 is a variant of ES_7 .

The inference of a property set is here performed as follows. For every property:

$$perm(x_2, y_2) \Leftarrow Body \quad (PP_i)$$

find a variant:

$$perm(x_1, y_1) \quad (EP_j/PP_j)$$

of an example or body-less property, such that:

$$x_2 = [h | y_1]$$

Infer:

$$pcPerm(h, y_1, y_2) \Leftarrow Body \quad (PS_i)$$

as a property of $pcPerm$.

In our case, the pairs $\langle PP_1, EP_1 \rangle$, $\langle PP_2, PP_1 \rangle$, and $\langle PP_3, PP_1 \rangle$ infer the properties shown in figure 1.13. The informal specification is that $pcPerm(\mathbf{E}, \mathbf{L}, \mathbf{R})$ succeeds iff list \mathbf{R} is list \mathbf{L} with term \mathbf{E} stuffed into a random location. But, in order to prevent redundant solutions, element \mathbf{E} should only be stuffed into some location of \mathbf{L} that precedes its own first occurrence (if any) in \mathbf{L} . The following new version of PP_3 achieves this:

$$perm([X, Y], [Y, X]) \Leftarrow X \neq Y \quad (PP'_3)$$

because PS_3 then reads:

$$pcPerm(X, [Y], [Y, X]) \Leftarrow X \neq Y \quad (PS'_3)$$

$$\begin{array}{l}
\mathbf{perm}(\mathbf{L}, \mathbf{P}) \Leftrightarrow \\
\quad \mathbf{L} = [] \quad \wedge \mathbf{P} = [] \\
\quad \wedge L = [] \wedge P = [] \\
\vee \quad \mathbf{L} = [_|_] \quad \wedge \mathbf{L} = [\mathbf{HL}|\mathbf{TL}] \\
\quad \wedge \mathbf{perm}(\mathbf{TL}, \mathbf{TP}) \\
\quad \wedge \mathbf{efface}(\mathbf{HL}, \mathbf{P}, \mathbf{TP}) \\
\quad \wedge HL = a \wedge TP = [] \wedge P = [a] \wedge \dots \\
\quad \vee HL = b \wedge TP = [c] \wedge P = [b, c] \wedge \dots \\
\quad \vee HL = b \wedge TP = [c] \wedge P = [c, b] \wedge \dots \\
\quad \vee HL = d \wedge TP = [e, f] \wedge P = [d, e, f] \wedge \dots \\
\quad \vee HL = d \wedge TP = [f, e] \wedge P = [d, f, e] \wedge \dots \\
\quad \vee HL = d \wedge TP = [e, f] \wedge P = [e, d, f] \wedge \dots \\
\quad \vee HL = d \wedge TP = [e, f] \wedge P = [f, d, e] \wedge \dots \\
\quad \vee HL = d \wedge TP = [f, e] \wedge P = [f, d, e] \wedge \dots \\
\quad \vee HL = d \wedge TP = [f, e] \wedge P = [f, e, d] \wedge \dots
\end{array}$$

Figure 1.14 $LA_6(perm)$

Note that when replacing all occurrences of the empty list $[]$ in $\{PS_1, PS_2, PS'_3\}$ by a variable T , this property set collapses into the one of the *efface/3* problem, where the second and third parameters have been exchanged. When taking into account the above-noted variants in $E(pcPerm)$, then the example set also collapses into the one of the *efface/3* problem, where the second and third parameters have been exchanged. The new synthesis is thus, up to renaming of the predicate and re-ordering of the parameters, the same as the one of the *efface/3* problem. Hence $LA_6(perm)$ looks as depicted in figure 1.14.

1.3.2.3 THE *FIRSTSEQ/3* PROBLEM

The logic algorithm being synthesized for the *firstSeq/3* problem a partial scan logic algorithm: the recursion may stop scanning L before it is through. Indeed, once F has been identified in L , one can already compute the final value of S without further scanning L . At step 4, the actual values of the introduced parameters TF and TS could be determined because *firstSeq/3* is a deterministic problem, so no lifting of non-determinacy is required here.

According to the MSG Method, there are initially no classes, so no disjunct belongs to any class. We first consider disjunct DF_3 , and create a singleton class $\{DF_3\}$.

We pursue with DF_4 . To see whether DF_4 is compatible with class $\{DF_3\}$, we

| | |
|---|--|
| firstSeq(L, F, S) ⇔ | |
| $\mathbf{L} = [_]$ $\vee \mathbf{L} = [_, _ _]$ $\vee \mathbf{L} = [_, _ _]$ | $\wedge \mathbf{F} = \mathbf{L} \wedge \mathbf{S} = []$ $\wedge L = [a] \wedge F = [a] \wedge S = []$ $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{TL} = [\mathbf{HTL} _] \wedge \mathbf{HL} \neq \mathbf{HTL}$ $\wedge \mathbf{F} = [\mathbf{HL}] \wedge \mathbf{S} = \mathbf{TL}$ $\wedge L = [b, c, d, e] \wedge F = [b] \wedge S = [c, d, e]$ $\wedge HL = b \wedge TL = [c, d, e]$ $\wedge \mathbf{L} = [\mathbf{HL} \mathbf{TL}]$ $\wedge \mathbf{firstSeq}(\mathbf{TL}, \mathbf{TF}, \mathbf{TS})$ $\wedge \mathbf{F} = [\mathbf{HL} \mathbf{TF}] \wedge \mathbf{S} = \mathbf{TS}$ $\wedge HL = f \wedge TF = [f] \wedge TS = [g, h] \wedge \dots$ $\quad \vee HL = i \wedge TF = [i, i] \wedge TS = [j] \wedge \dots$ |

Figure 1.15 $LA_6(\mathit{firstSeq})$

compute the msg of their $\langle HL, TF, TS, F, S \rangle$ value-tuples:

| <i>HL</i> | <i>TF</i> | <i>TS</i> | <i>F</i> | <i>S</i> | |
|-----------|-----------------|-----------------|--------------------|-----------------|---------------------|
| <i>f</i> | [<i>f</i>] | [<i>g, h</i>] | [<i>f, f</i>] | [<i>g, h</i>] | $msg\{DF_3\}$ |
| <i>i</i> | [<i>i, i</i>] | [<i>j</i>] | [<i>i, i, i</i>] | [<i>j</i>] | DF_4 |
| <i>A</i> | [<i>A T</i>] | [<i>B U</i>] | [<i>A, A T</i>] | [<i>B U</i>] | $msg\{DF_3, DF_4\}$ |

Disjunct DF_4 is compatible with class $\{DF_3\}$, because $[A, A|T]$ is composed in terms of A, A , and T , and because $[B|U]$ is composed in terms of A, B , and U . Thus DF_4 goes into that class.

There are no other disjuncts. We have partitioned the non-minimal disjuncts into $c = 1$ equivalence class, namely $\{DF_3, DF_4\}$. Let $pcFirstSeq$ be the chosen instantiation of $ProcComp_1$. It is implemented by re-expression of the msg:

$$pcFirstSeq(HL, TF, TS, F, S) \Leftrightarrow F = [HL|TF] \wedge S = TS.$$

This result is unfolded into the corresponding disjuncts of $LA_5(\mathit{firstSeq})$, and, after regrouping of disjuncts, $LA_6(\mathit{firstSeq})$ looks as depicted in figure 1.15.

1.4 SYNTHESIS OF THE *DISCRIMINATE_K* (STEP 7)

In the non-minimal recursive case, the $Discriminate_k(\mathbf{HX}, \mathbf{TX}, Y)$ procedures perform some tests so as to ensure that the parameters effectively should be processed and composed as in class $|k|$. In this section, we first formally present the objective and methods of step 7. Then we synthesize $Discriminate_k$ operators for the sample problems given in section 1.2.

1.4.1 Formalization: objective and methods

At step 7, the aim is to transform $LA_6(r)$ into $LA_7(r)$ such that it fits the following schema:

$$\begin{array}{l}
 r(X, Y) \Leftrightarrow \\
 \quad \text{minimal}(X) \quad \wedge \text{solve}(X, Y) \\
 \quad \vee \bigvee_{1 \leq k \leq c} \text{nonMinimal}(X) \quad \wedge \bigvee_{1 \leq i \leq b} X = x_i \wedge Y = y_i \\
 \quad \quad \quad \quad \quad \quad \quad \wedge \text{decompose}(X, \mathbf{HX}, \mathbf{TX}) \\
 \quad \quad \quad \quad \quad \quad \quad \wedge \text{Discriminate}_k(\mathbf{HX}, \mathbf{TX}, Y) \\
 \quad \quad \quad \quad \quad \quad \quad \wedge \mathbf{r}(\mathbf{TX}, \mathbf{TY}) \\
 \quad \quad \quad \quad \quad \quad \quad \wedge \text{procComp}_k(\mathbf{HX}, \mathbf{TY}, Y) \\
 \quad \quad \quad \quad \quad \quad \quad \wedge \bigvee_{i \in |k|} X = x_i \wedge Y = y_i \\
 \quad \quad \quad \quad \quad \quad \quad \quad \wedge HX = hx_i \wedge TX = tx_i \wedge TY = ty'_i
 \end{array}$$

This objective is achieved by two consecutive tasks:

- i) synthesis of specialized logic algorithms of the Discriminate_k ;
- ii) generalization of these specialized logic algorithms of the Discriminate_k .

They are performed by a *Proofs-as-Programs Method* and a *Generalization Method*, respectively. We discuss these methods in turn.

1.4.1.1 THE PROOFS-AS-PROGRAMS METHOD

The Proofs-as-Programs Method extracts specialized implementations of the Discriminate_k from the proofs that the heads of the properties are logical consequences of the recursive, non-minimal disjuncts. Indeed, properties contain explicit information that has not yet been synthesized into these disjuncts. This is thus the right moment to use this information.

When considering property P_i , let \mathcal{T}_i be a theory composed of:

- i) the recursive, non-minimal disjuncts of the generalization $\Gamma(LA_6(r))$;
- ii) the example set $E(r)$;
- iii) the property set $P(r) \setminus \{P_i\}$;
- iv) logic algorithms of the primitive predicates, such as $LA(=)$.

The Proofs-as-Programs Method first attempts to prove that the head of property P_i is a logical consequence of theory \mathcal{T}_i . The body of property P_i is then used during the discriminant extraction.

These proofs can be done by a slight modification of SLD resolution (see section ??). A clausal version of \mathcal{T}_i has first to be generated: this is straightforward due to our restriction to logic algorithms whose bodies are in disjunctive normal form. Examples and properties are already in clausal form.

The initial goal is the head of property P_i .

The *computation rule* satisfies the following condition: never select an atom with predicate r/n if there are atoms with primitive predicates.

The search rule is as follows:

- if the selected atom has a primitive predicate, then it is resolved according to its semantics;

- the root of the proof tree is resolved using the clauses generated from $\Gamma(LA_6(r))$;
- if the selected atom (in a non-root node) has predicate r/n , then it is resolved using the clauses generated from $\mathcal{T}_i \setminus \{\Gamma(LA_6(r))\}$.

In the resolution of the root, we use $\Gamma(LA_6(r))$ rather than the examples or properties because that wouldn't make sense: we are trying to prove the logic algorithm correct wrt its specification, not to prove the specification consistent. In the resolution of atoms with predicate r/n , we use the examples and other properties rather than $\Gamma(LA_6(r))$ because that wouldn't make sense either: $\Gamma(LA_6(r))$ is still incorrect. Our restriction to properties that are non-recursive comes in handy here: there is no further nesting of goals in r/n .

A derivation of the head of property P_i succeeds iff it ends in the empty clause. Let k be the number of the (recursive, non-minimal) disjunct of $\Gamma(LA_6(r))$ whose generated clause was used in the initial resolution step. Let σ_{ki} be the corresponding computed answer substitution. We instantiate *Discriminate_k* by *discriminate_k* such that it is partly defined by the following clause:

$$\mathbf{discriminate}_k(\mathbf{HX}, \mathbf{TX}, y)\sigma_{ki} \leftarrow \mathbf{Body}_i\sigma_{ki}$$

where \mathbf{Body}_i is the body of property P_i , and where y is the value of Y in the head of P_i .

Note that this is not like classical program extraction from proofs (compare with [BSW90, Fri90, MW79]), since the program is here extracted from the unique final result of the proof, rather than on-the-fly (or a posteriori) from multiple intermediate proof-steps.

A derivation *fails* iff it doesn't succeed. Nothing is done in that case. Failure is of course not always detectable, since an infinite derivation may occur. In practice, success is approximated by limiting the size (or the CPU time) of a derivation.

The proof of a property set *succeeds* iff every property has at least one successful derivation. The revealed clauses of discriminants are then assembled into logic algorithms:

$$\begin{aligned} \mathbf{discriminate}_k(\mathbf{HX}_k, \mathbf{TX}_k, Y_k) \Leftrightarrow \\ \bigvee_{i \in \mathcal{S}(k)} (\mathbf{HX}_k = \mathbf{HX} \wedge \mathbf{TX}_k = \mathbf{TX} \wedge Y_k = y \wedge \mathbf{Body}_i)\sigma_{ki} \end{aligned}$$

where $\mathcal{S}(k)$ is the finite, possibly empty, set of indices of the properties whose proofs revealed clauses of *discriminate_k*. If $\mathcal{S}(k)$ is empty, then the corresponding discriminant always evaluates to *true*.

The proof of a property set *fails* iff some property has no successful derivation. In such a case, the Proofs-as-Programs Method does not synthesize the discriminants. Previous steps of the synthesis have then to be reconsidered.

1.4.1.2 THE GENERALIZATION METHOD

Since the properties only embody fragmentary information, the discriminants obtained so far are too specialized. The Generalization Method applies some heuristics, and postulates that the resulting discriminants are the intended ones.

We pursue with PF_3 :

$$\begin{array}{c}
\leftarrow \underline{firstSeq}([X, Y|T], [X], [Y|T]) \\
\text{disjunct 3 of } \Gamma(LA_6(firstSeq)) \quad \Bigg| \quad \{\} \\
\leftarrow \underline{[X, Y|T] = [HL|TL]}, firstSeq(TL, TF, TS), \underline{[X] = [HL|TF]}, \underline{[Y|T] = TS} \\
LA(=) \quad \Bigg| \quad \{HL/X, TL/[Y|T], TF/[], TS/[Y|T]\} \\
\leftarrow \underline{firstSeq}([Y|T], [], [Y|T])
\end{array}$$

The last goal doesn't unify with the head of any clause in \mathcal{TF}_3 : the derivation fails. The head of property PF_3 is not a logical consequence of theory \mathcal{TF}_3 . So nothing is done to disjunct 3.

We pursue with PF_2 :

$$\begin{array}{c}
\leftarrow \underline{firstSeq}([X, X], [X, X], []) \\
\text{disjunct 3 of } \Gamma(LA_6(firstSeq)) \quad \Bigg| \quad \{\} \\
\leftarrow \underline{[X, X] = [HL|TL]}, firstSeq(TL, TF, TS), \underline{[X, X] = [HL|TF]}, \underline{[] = TS} \\
LA(=) \quad \Bigg| \quad \{HL/X, TL/[X], TF/[X], TS/[]\} \\
\leftarrow \underline{firstSeq}([X], [X], []) \\
PF_1 \quad \Bigg| \quad \{\} \\
\quad \quad \quad \square
\end{array}$$

The head of property PF_2 is a logical consequence of theory \mathcal{TF}_2 . A specialized discriminant for disjunct 3 is partly defined as follows:

$$\text{discFirstSeq}(HL, TL, [X, X], [])\sigma \leftarrow \text{true}\sigma$$

where σ is the computed answer substitution of the above SLD-derivation. This yields:

$$\text{discFirstSeq}(X, [X], [X, X], [])$$

There is no other property. There are no alternative derivations.

The Generalization Method applies heuristic 2 to decide that the values of the third and fourth parameters are irrelevant in this case. It applies heuristic 3 to generalize the TL parameter into non-empty lists. After generalization of the implication into an

$$\begin{array}{l}
\mathbf{firstSeq}(\mathbf{L}, \mathbf{F}, \mathbf{S}) \Leftrightarrow \\
\mathbf{L} = [_] \quad \wedge \mathbf{F} = \mathbf{L} \wedge \mathbf{S} = [] \\
\quad \wedge L = [a] \wedge F = [a] \wedge S = [] \\
\vee \mathbf{L} = [_, _ | _] \quad \wedge \mathbf{L} = [\mathbf{HL} | \mathbf{TL}] \\
\quad \wedge \mathbf{TL} = [\mathbf{HTL} | _] \wedge \mathbf{HL} \neq \mathbf{HTL} \\
\quad \wedge \mathbf{F} = [\mathbf{HL}] \wedge \mathbf{S} = \mathbf{TL} \\
\quad \wedge L = [b, c, d, e] \wedge F = [b] \wedge S = [c, d, e] \\
\quad \wedge HL = b \wedge TL = [c, d, e] \\
\vee \mathbf{L} = [_, _ | _] \quad \wedge \mathbf{L} = [\mathbf{HL} | \mathbf{TL}] \\
\quad \wedge \mathbf{TL} = [\mathbf{HTL} | _] \wedge \mathbf{HL} = \mathbf{HTL} \\
\quad \wedge \mathbf{firstSeq}(\mathbf{TL}, \mathbf{TF}, \mathbf{TS}) \\
\quad \wedge \mathbf{F} = [\mathbf{HL} | \mathbf{TF}] \wedge \mathbf{S} = \mathbf{TS} \\
\quad \wedge HL = f \wedge TF = [f] \wedge TS = [g, h] \wedge \dots \\
\quad \quad \vee HL_1 = i \wedge TF = [i, i] \wedge TS = [j] \wedge \dots
\end{array}$$

Figure 1.17 $LA_7(\mathit{firstSeq})$

equivalence, plus renaming of some variables, the discriminant reads:

$$\mathit{discFirstSeq}(HL, TL, F, S) \Leftrightarrow TL = [HTL | _] \wedge HL = HTL \wedge F = _ \wedge S = _.$$

This result is simplified and then unfolded into disjunct 3, so $LA_7(\mathit{firstSeq})$ looks as depicted in figure 1.17.

1.5 SYNTACTIC GENERALIZATION (STEP 8)

In this section, we first formally present the objective and method of step 8. Then we compute the final generalizations for the sample problems given in section 1.2.

1.5.1 Formalization: objective and method

At step 8, the objective is to transform $LA_7(r)$ into $LA_8(r)$ that looks like:

$$\begin{array}{l}
r(X, Y) \Leftrightarrow \\
\vee \bigvee_{1 \leq k \leq c} \quad \begin{array}{l} \mathit{minimal}(X) \\ \mathit{nonMinimal}(X) \end{array} \quad \begin{array}{l} \wedge \mathit{solve}(X, Y) \\ \wedge \mathit{decompose}(X, \mathbf{HX}, \mathbf{TX}) \\ \wedge \mathit{discriminate}_k(\mathbf{HX}, \mathbf{TX}, Y) \\ \wedge \mathbf{r}(\mathbf{TX}, \mathbf{TY}) \\ \wedge \mathit{procComp}_k(\mathbf{HX}, \mathbf{TY}, Y) \end{array}
\end{array}$$

All predicate variables of the divide-and-conquer schema have already been instantiated until step 7, and we have used all the information contained in the specification:

$$\begin{array}{l}
\text{efface}(E, L, R) \Leftrightarrow \\
\quad L = [_] \quad \wedge L = [E] \wedge R = [] \\
\vee L = [_ , _ | _] \quad \wedge L = [HL|TL] \\
\quad \wedge E = HL \wedge R = TL \\
\vee L = [_ , _ | _] \quad \wedge L = [HL|TL] \\
\quad \wedge E \neq HL \\
\quad \wedge \text{efface}(E, TL, TR) \\
\quad \wedge R = [HL|TR]
\end{array}$$

Figure 1.18 $LA_8(\text{efface})$

$$\begin{array}{l}
\text{perm}(L, P) \Leftrightarrow \\
\quad L = [] \quad \wedge P = [] \\
\vee L = [_ | _] \quad \wedge L = [HL|TL] \\
\quad \wedge \text{perm}(TL, TP) \\
\quad \wedge \text{efface}(HL, P, TP)
\end{array}$$

Figure 1.19 $LA_8(\text{perm})$

- examples are injected at step 1 and are trailed along all subsequent steps in the form of equality atoms, so that they be present when needed;
- examples and properties are used at step 4 to deductively infer values of the **TY**;
- examples and properties are used at step 6 to infer specifications of subproblems;
- properties are used at steps 5 and 7 to infer discriminants.

So we may consider the synthesis task finished. We postulate that $LA_8(r)$ is $\Gamma(LA_7(r))$.

1.5.2 Illustration on sample problems

The final generalizations for the sample problems given in section 1.2 are depicted in figures 1.18, 1.19, and 1.20, respectively. Note that they can be proven to be equivalent to the sample logic algorithms listed in figures 1.4, 1.7, and 1.10, respectively.

1.6 CONCLUSION

In this last section, we present the framework, results, and contributions of this research, and mention some related research, as well as future research.

$$\begin{array}{l}
firstSeq(L, F, S) \Leftrightarrow \\
\quad L = [_] \quad \wedge F = L \wedge S = [] \\
\vee L = [_, _ | _] \quad \wedge L = [HL|TL] \\
\quad \quad \quad \wedge TL = [HTL|_] \wedge HL \neq HTL \\
\quad \quad \quad \wedge F = [HL] \wedge S = TL \\
\vee L = [_, _ | _] \quad \wedge L = [HL|TL] \\
\quad \quad \quad \wedge TL = [HTL|_] \wedge HL = HTL \\
\quad \quad \quad \wedge firstSeq(TL, TF, TS) \\
\quad \quad \quad \wedge F = [HL|TF] \wedge S = TS
\end{array}$$

Figure 1.20 $LA_8(firstSeq)$

1.6.1 Framework, results, and contributions

This research was partly led within the framework of the *Folon* research project (Facultés Universitaires Notre-Dame de la Paix, Namur, Belgium) whose objective is twofold. First, it aims at elaborating a methodology of logic program development, such as described in [Dev90]. Second, it aims at designing an integrated set of tools supporting this methodology. Our research tackles the aspects of logic program synthesis from examples and properties.

The main results of our work on logic program synthesis so far are the definition of a synthesis calculus ([FD91, FD93]), the identification of a particular synthesis mechanism ([Fle91]), and the development of methods for each of its steps ([Fle91]).

One of the originalities of our approach is the combination of examples with properties, so as to cope with some classical difficulties of example-based synthesis.

1.6.2 Related research

Pointers to related research in program synthesis have been given throughout the chapter, and we have already stressed in detail how our approach differs from the state of the art. The use of schemata is also advocated in [Smi85, DB89, GH89, Tin90] (divide-and-conquer), [Smi88] (global search), and others, although sometimes in different contexts (e.g. programming tutors/assistants). An early study of the concept of most specific generalization is [Plö70].

1.6.3 Future research

In the future, we plan to pursue research on the following aspects:

Development of a proof-of-concept implementation (in Prolog) of the synthesis mechanism, called SYNAPSE (SYNthesis of logic Algorithms from PropertieS and Examples). This should allow the identification of points of interaction with the specifier so that we can better cope with incompleteness: wherever inductive inference is used, the specifier should be able to give his feedback. It is important to keep this dialogue

easy, i.e. a question-and-answer method asking for the classification of ground atoms as examples or counter-examples seems to be an appropriate choice.

Incorporation of counter-examples in the specifications, the general synthesis strategy, and the synthesis mechanism. Indeed, negative information is quite useful in avoiding over-generalization during inductive inference.

Formulation of a methodology of choosing examples and properties. It is important to guide the specifier towards choosing “good” examples and properties. This reduces interaction with the specifier, and results thus in highly automated synthesis.

1.7 EXERCISES

1. Give a sample logic program fitting the divide-and-conquer schema, where the instances of the *NonMinimal* and the *Decompose* predicates are different.
2. Extend the schema of figure 1.1 to n -ary relations.
3. Perform the remaining msg computations of subsection 1.3.2.2.
4. Perform the Proofs-as-Program Method to the *perm/2* problem. Show that the obtained discriminants are redundant with the existing literals of $LA_6(perm)$.
5. Show that $LA_8(efface)$, $LA_8(perm)$, and $LA_8(firstSeq)$ are logically equivalent to (i.e. have the same models than) the logic algorithms listed in figures 1.4, 1.7, and 1.10, respectively.

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REFERENCES

- [Bie84] A.W. Biermann. Dealing with Search. In A.W. Bierman, G. Guiho, and Y. Kodratoff, editors, *Automatic Program Construction Techniques*, pages 375–392. Macmillan, New York, NY, USA, 1984.
- [Bie92] A.W. Biermann. Automatic Programming. In S.C. Shapiro, editor, *Encyclopedia of Artificial Intelligence*, Wiley Interscience Publication, pages 59–83. John Wiley & Sons, New York, NY, USA, second, extended edition, 1992.
- [BS79] A.W. Biermann and D.R. Smith. A Production Rule Mechanism for Generating LISP Code. *IEEE Transactions on Systems, Man, and Cybernetics*, 9(5):260–276, 1979.
- [BSW90] A. Bundy, A. Smail, and G. Wiggins. The Synthesis of Logic Programs from Inductive Proofs. In J.W. Lloyd, editor, *Symposium on Computational Logic*, Esprit Basic Research Series, pages 135–149. Springer-Verlag, Heidelberg, Germany, 1990.

- [Cla81] K.L. Clark. The Synthesis and Verification of Logic Programs. Research Report DOC-81/36, Imperial College, London, United Kingdom, September 1981.
- [DB89] Y. Deville and J. Burnay. Generalization and Program Schemata: A Step Towards Computer-Aided Construction of Logic Programs. In E.L. Lusk and R. Overbeek, editors, *Proc. North American Conference on Logic Programming*, Series in Logic Programming, pages 409–425, Cleveland, USA, October 1989. The MIT Press.
- [Dev90] Y. Deville. *Logic Programming: Systematic Program Development*. International Series in Logic Programming. Addison-Wesley, Wokingham, United Kingdom, 1990.
- [FD91] P. Flener and Y. Deville. Towards Stepwise, Schema-Guided Synthesis of Logic Programs. In K. Lau and T. Clement, editors, *Workshop on Logic Program Synthesis and Transformation*, Workshops in Computing Series, pages 46–64, Manchester, United Kingdom, July 1991. Springer-Verlag.
- [FD93] P. Flener and Y. Deville. Logic Program Synthesis from Incomplete Specifications. *Journal of Symbolic Computation: Special Issue on Automatic Programming*, 1993. To appear.
- [Fle91] P. Flener. Towards Programming by Examples and Properties. Research Report CS-1991-09, Duke University, Durham, NC, USA, March 1991.
- [Fle93] P. Flener. *Algorithm Synthesis from Incomplete Specifications*. PhD thesis, Unité d'Informatique, Université Catholique de Louvain, Louvain-la-Neuve, Belgium, 1993. Forthcoming.
- [Fri90] L. Fribourg. Extracting Logic Programs from Proofs that Use Extended Prolog Execution and Induction. In D.H.D. Warren and P. Szeredi, editors, *Proc. Seventh International Conference on Logic Programming*, Series in Logic Programming, pages 685–699, Jerusalem, Israel, June 1990. The MIT Press.
- [GH89] T.S. Gegg-Harrison. Basic Prolog Schemata. Research Report CS-1989-20, Duke University, Durham, NC, USA, September 1989.
- [Han80] Å. Hansson. *A Formal Development of Programs*. PhD thesis, Department of Computer Science, University of Stockholm, Stockholm, Sweden, 1980.
- [Hog81] C.J. Hogger. Derivation of Logic Programs. *Journal of the ACM*, 28(2):372–392, 1981.
- [LP90] K. Lau and S. Prestwich. Top-Down Synthesis of Recursive Logic Procedures from First-Order Logic Specifications. In D.H.D. Warren and P. Szeredi, editors, *Proc. Seventh International Conference on Logic Programming*, Series in Logic Programming, pages 667–684, Jerusalem, Israel, June 1990. The MIT Press.
- [Man74] Z. Manna. *Mathematical Theory of Computation*. Computer Science Series. McGraw-Hill, New York, NY, USA, 1974.
- [Mug92] S. Muggleton, editor. *Inductive Logic Programming*, volume 38 of *APIC Series*. Academic Press, London, United Kingdom, 1992.
- [MW79] Z. Manna and R. Waldinger. Synthesis: Dreams \Rightarrow Programs. *IEEE Transactions on Software Engineering*, 5(4):294–328, 1979.
- [Plo70] G.D. Plotkin. A Note on Inductive Generalization. *Machine Intelligence*, 5:153–163, 1970.
- [Sha82] E. Shapiro. *Algorithmic Program Debugging*. The ACM Distinguished Dissertation Series. The MIT Press, Cambridge, MA, USA, 1982.
- [Smi85] D.R. Smith. Top-down Synthesis of Divide-and-Conquer Algorithms. *Artificial Intelligence*, 27(1):43–96, 1985.
- [Smi88] D.R. Smith. The Structure and Design of Global Search Algorithms. Technical Report KES.U.87.12, Kestrel Institute, Palo Alto, CA, USA, July 1988.
- [Sum77] P. Summers. A Methodology for LISP Program Construction from Examples. *Journal of the ACM*, 24(1):161–175, 1977.
- [Tin90] N.L. Tinkham. *Induction of Schemata for Program Synthesis*. PhD thesis, Duke University, Durham, NC, USA, 1990.